DIGITAL PHOTO CAMERA

DKC-ST5 VCL-1205BYS

SERVICE MANUAL

CAUTION

Danger of explosion if battery is incorrectly replaced.

Replace only with the same or equivalent type recommended by the manufacturer. Dispose of used batteries according to the manufacturer's instructions.

Vorsicht!

Explosionsgefahr bei unsachgemäßem Austausch der Batterie.

Ersatz nur durch denselben oder einen vom Hersteller empfohlenen ähnlichen Typ. Entsorgung gebrauchter Batterien nach Angaben des Herstellers.

ATTENTION

Il y a danger d'explosion s'il y a remplacement incorrect de la batterie.

Remplacer uniquement avec une batterie du même type ou d'un type équivalent recommandé par le constructeur.

Mettre au rebut les batteries usagées conformément aux instructions du fabricant.

ADVARSEL!

Lithiumbatteri-Eksplosionsfare ved fejlagtig håndtering. Udskiftning må kun ske med batteri af samme fabrikat og type. Levér det brugte batteri tilbage til leverandøren.

ADVARSEL

Lithiumbatteri - Eksplosjonsfare.
Ved utskifting benyttes kun batteri som anbefalt av apparatfabrikanten.
Brukt batteri returneres apparatleverandøren.

VARNING

Explosionsfara vid felaktigt batteribyte.
Använd samma batterityp eller en likvärdig typ
som rekommenderas av apparattiliverkaren.
Kassera använt batteri enligt gällande
föreskrifter.

VAROITUS

Paristo voi räjähtää jos se on virheellisesti asennettu. Vaihda paristo ainoastaan laitevalmistajan suosittelemaan tyyppiin. Hävitä käytetty paristo valmistajan ohjeiden mukaisesti.

TABLE OF CONTENTS

1. OPERATING INSTRUCTION 1-1	3-17. OUTPUT LEVEL ADJUSTMENT 3-8
	3-18. KNEE ADJUSTMENT 3-9
2. SERVICE INFORMATION	3-19. V SAW ADJUSTMENT (ALL PIXELS) 3-9
2-1. BOARD LAYOUT2-1	3-20. 5600K COLOR TEMPERATURE
2-2. PROCESSOR BLOCK DISASSEMBLY 2-1	CONVERSION SETTING3-10
2-2-1. Removal of Cabinet2-1	3-21. GAIN ADJUSTMENT (5600K) 3-10
2-2-2. Removal of the ADA-52 and SY-243 Boards 2-2	3-22. SETTING VALUE WRITTING 3-10
2-2-3. Removal of the Rear Panel Block 2-2	
2-2-4. Removal of the MB-725 Board	
2-2-5. Removal of the Switching Regulator 2-3	4. CIRCUIT DESCRIPTION
2-2-6. Removal of the CN-1396 Board	4-1. PROCESSOR SECTION 4-1
2-3. CAMERA BLOCK DISASSEMBLY2-4	4-2. CAMERA SECTION 4-20
2-3-1. Removal of the Cabinet	
2-3-2. Removal of the AT-115 Board2-4	5. TROUBLE SHOOTING
2-3-3. Removal of the PR-228 Board	5-1. PROCESSOR BLOCK5-1
2-3-4. Removal of the Rear Block	5-2. CAMERA BLOCK 5-5
2-3-5. Removal of the Bottom Base Assembly 2-5	
2-3-6. Removal of the TG-180 Board and	6. SEMICONDUCTOR PIN ASSIGNMENTS 6-1
Top Chassis	7. SPARE PARTS
2-3-7. Removal of the CCD Block Assembly 2-6	7-1. NOTE ON REPAIR PARTS 7-1
2-4. HOW TO USE EXTENSION BOARD 2-6	7-2. EXPLODED VIEWS
2-4-1. Processor block	7-3. ELECTRICAL PARTS LIST
2-4-2. Camera block	7-5. LEISTRICAL I ARTO LIGI
2-5. AFTER CCD BLOCK IS REPLACED2-7	8. BLOCK DIAGRAMS
2-5-1. Equipment required	PROCESSOR OVERALL 8-1
2-5-2. Operation description	• ADA-52 BOARD8-3
2-5-3. DKC-ST5 Interface	• SY-243 BOARD8-9
A FIRATRIAL AD MATERIA	• CAMERA OVERALL
3. ELECTRICAL ADJUSTMENT 3-1. PREPARATION	• CAMERA OVERALL 8-13
	9. PRINTED WIRING BOARDS AND
3-1-1. Equipment Required	
3-1-2. Connection	SCHEMATIC DIAGRAMS
3-1-3. Adjustment mode	• FRAME (PROCESSOR)
3-1-4. Iris Setting 3-1	• ADA-52 BOARD
3-2. VSUB ADJUSTMENT (TG-180)	• CN-1396 BOARD9-21
3-3. RG DC ADJUSTMENT 3-2	• MB-725 AND LED-280 BOARDS 9-29
3-4. TERMINOLOGY DEFINITION 3-2	• SY-243 BOARD
3-5. EVR INITIALIZATION	• FRAME (CAMERA) 9-49
3-6. INPUT LEVEL ADJUSTMENT 3-3	• CN-1395 AND CN-1401 BOARDS9-53
3-7. DARK ADJUSTMENT 3-3	• AT-115 AND CN-1462 BOARDS 9-55
3-8. HSAW ADJUSTMENT 3-4	• MB-724 BOARD9-61
3-9. VSAW ADJUSTMENT 3-4	• PA-200, PA-201 AND PA-202 BOARDS 9-65
3-10. GAIN ADJUSTMENT	• PR-228 BOARD9-69
3-11. BLACK 1 ADJUSTMENT 3-5	• TG-180 BOARD9-81
3-12. PRENEE ADJUSTMENT 3-6	• RM-171 BOARD9-89
3-13. V SAW ADJUSTMENT 3-6	
3-14. YINPUT LEVEL ADJUSTMENT3-7	
3-15. PEDESTAL ADJUSTMENT 3-7	
3-16. γ ADJUSTMENT	
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SECTION 1 OPERATING INSTRUCTION

This section is extracted from operation manual.

Digital Photo Camera

Operating Instructions
Before operating this unit, please read this manual
thoroughly and retain it for future reference.

JKC-ST5

515-24G

3-859-629-11(1)

SON

Installing the Camera Head..

Installation and Connections Setting the DIP Switches

Connections SCSI Connections .

Table of Contents

Camera (referred to below as "the product"). The DKC-ST5

included with the product as sample software in order to

enable you to utilize the full potential of the product.

control software (referred to below as "the software") is

Thank you for choosing the Sony DKC-ST5 Digital Photo

	About This Manual5	. 22
Chapter 1		
Overview	System Features7	7
	DKC-ST5 Standard Product Configuration	∞ :
	Principal Examples of Use9	6 ::
	Location and Function of Parts11	F
	Camera Head 11	Ξ
	Digital Processor13	. 13
	Remote Controller16	. 16
Chapter 2		
Operation	Powering On and Off21	7
	Powering On 22	22
	Powering Off	. 22
	Live and Memory Monitor Modes	. 22
	Status Indications	. 23
	Image Adjustments and Focusing24	24
	Adjusting the Zoom 24	24
	Focusing 24	24
	3alance	56
	Adjusting the White Balance27	27
	Adjusting the Iris30	90
	Shooting31	31
	Basic Procedure31	31
	Selecting a Memory33	. 33
	Reviewing a Captured Image38	. 38
	Switching Between Landscape and Portrait Orientations	30
Chapter 3		

Owner's Record

The model and serial numbers are located at the rear. Record the serial number in the space provided below. Refer to these numbers whenever you call upon your Sony dealer regarding this product.

WARNING

To prevent fire or shock hazard, do not expose the unit to rain or moisture.

2. The software can only be used with the product, and may

in whole or in part.

not be used by any third party. Export or transfer of the

product and the software to any foreign country is

The software may not be disassembled, decompiled, or

4. The software is provided only as a sample, and Sony

otherwise converted to source code form.

makes no assurance whatsoever concerning the

software's freedom defects.

reproduced, copied, modified, or revised in any manner,

1. The software and related documentation may not be

The software is provided under the following conditions.

To avoid electrical shock, do not open the cabinet. Refer servicing to qualified personnel only.

Sony disclaims all responsibility for any copyright, patent, or other non-tangible property infringement disputes that

may arise with third parties as a consequence of use of

For the customers in the USA

This equipment has been tested and found to comply with the limits for a Class A digital device, pursuant to Part 15 of the First for a Class A digital device, pursuant to Part 15 of the FOC Rutes. These limits are designed to provide reasonable protection against harmful interference when the equipment is operated in a commercial environment. This equipment generates, uses, and can radiate radio frequency energy and, if not installed and used in accordance with the instruction manual, may cause harmful interference to radio communications. Operation of this equipment in a residential area is likely to cause harmful interference in which case the

You are cautioned that any changes or modifications not expressly approved in this manual could void your authority to operate this equipment.

The shielded interface cable recommended in this manual must be used with this equipment in order to comply with the limits for a digital device pursuant to Subpart B of Part 15 of PCC Rules.

natural manual, may cause harmful interference to radio frecorded tapes, clisks, or broadcast programs is a size will be required to correct the interference at his own

obtain proper authorization before using such materials.

 if you determine that you have no need for the software, please destroy the diskette on which it is provided and the accompanying documentation.

N

Table of Contents

Menus	Overview	4
3	Basic Menu Operation Procedure	4
		51
	Main Menu	51
	Live Menu	54
Chapter 5		
Using the Control	Overview	96
Software	Required Hardware	55
	Required Software	55
	installinzg the Control Software	56
	Preparations	56
	Installing the Control Software	56
	Using the Control Software	56
	Uninstalling the Control Software	57
Chapter 6		
Maintenance	Error Messages	56
	Troubleshooting	9
Appendix		
	Notes on Use	61
	Specifications	62

About This Manual

This manual gives an overview of the DKC-STS Digital Photo Camera, and describes connections and installation, and the method of operation, principally with a remote controller.

Using the supplied control software

By installing the DKC-ST5 Control Software supplied with the product on
two 3.5-inch floppy disks, you can control the DKC-ST5 from a computer.
For details of the operation of the software, read the readme doc file
installed with the control software.

For details of the installation of the control software, see Chapter $\mathcal S$ of this manual.

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System Features

The DKC-STS Digital Photo Camera is a high-quality electronic photography system, using a high-resolution CCD camera with a total of 1,400,000 pixels. This system can form the core of a studio portrait photography system, or can be used for a variety of applications in which still image are kept in a filing

The procedure for using the camera is similar to that for a conventional camera, so there are no particular problems for users unaccustomed to video cameras or

Simple camera operation

A maximum of nine images can be held in memory. Stored images can also be transferred to a computer or color printer, for processing and printing. The following are some of the principal features of the system.

digital devices.

In addition to the special-purpose lens (VCL-1205BYS), you can fit any 2/3-inch 48-mm bayonet

mount lens.

High-resolution CCD camera head

The camera head uses a three-chip 2/s-inch high-resolution CCD with 1,400,000 total pixels.

Storage for up to nine images

Inages can be captured into memory consecutively at about 1-second intervals. After checking captured images on a monitor, you can select the images for transfer to a computer.

Flexible color gradation representation

The A/D conversion uses a 10-bit lookup table, which makes for flexible color gradation representation.

Optimized dynamic range

Gamma correction and knee point adjustment allow the dynamic range to be optimized.

Special-purpose lens for smooth manual adjustment

The special-purpose lens (VCL-1205BYS) is available separately, and provides a focusing ring and zoom ring with a light touch, allowing for smooth manual adjustment.

System Features

DKC-ST5 Standard Product Configuration

The following is the standard product configuration of the DKC-ST5.

Camera head

Digital processing unit

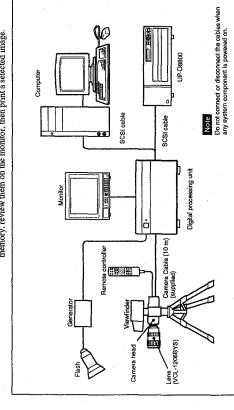
The following items are also supplied as standard accessories.

Portrait system

By combining a computer and a full-color printer, you can build an instant portrait system as shown below. You can capture a number of images into memory, review them on the monitor, then print a selected image.

The following are examples of use of the DKC-ST5 Digital Photo Camera system.

Principal Examples of Use



Two 3.5-inch floppy disks containing the Control Software

Operating Instructions (this manual)

For details of the components required in addition to the standard product configuration, see the section "Principal Examples of Use" on the next page.

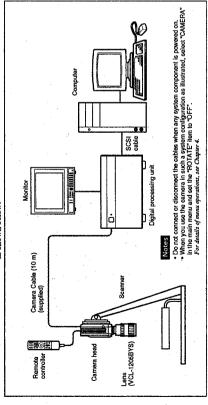
In addition to the standard components, a lens, video monitor, and computer are required.

Chapter 1: Overview 0

System Features

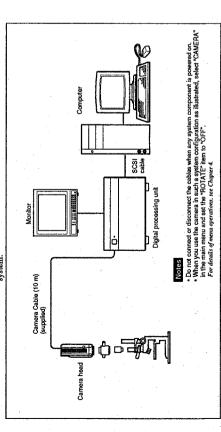
Still image filing system

In combination with a computer, you can create a still image filing system as shown below.



Medical image input system

The camera head can be used with a microscope, in a medical image input system.

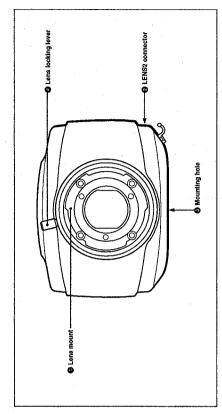


10 Chapter 1 Overview

Location and Function of Parts

Camera Head

Front view



C Lens mount

Mount the lens here. With the back end of the lens inserted, press down the lens locking lever in a clockwise direction to lock the lens.

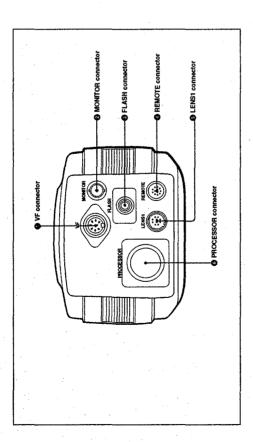
In addition to the special-purpose lens (VCL. 1205BYS), you can fit any ³/₂, inch 48-mm bayonet mount lens.

this connector.

② LENS2 connector (12-pin, female) When using a lens other than the special-purpose one (VCL-1205BYS), connect the cable from the lens to

Mounting hole
This accepts the fixing screw when the camera head is mounted on a tripod.

Rear view



1 VF (viewfinder) connector (DIN 8-pin) Connect the cable from the viewfinder.

© MONITOR connector (BNC type)
This outputs a composite video signal. Using a 75-ohm coaxial cable (not supplied), connect this to the composite video input connector (BNC type) of a

© FLASH connector (X-contact socket)

This connector is not used when the cable from the flash unit is connected to the FLASH connector on the Connect the cable from a flash unit. digital processor.

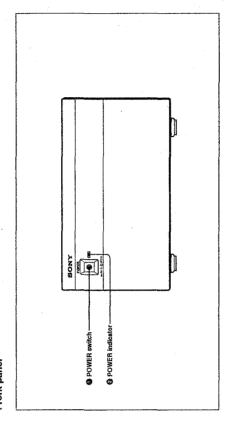
4 REMOTE connector (12-pin, female) Connect the supplied remote controller.

© LENS1 connector (20-pin, female) When using the special-purpose lens (VCL-1205BYS), connect the cable from the lens.

© PROCESSOR connector (36-pin, male)Connect this to the CAMERA connector of the digital processor with the supplied camera cable.

Digital Processor

Front panel



O POWER switch

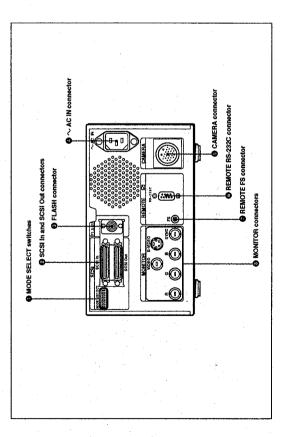
Once you have powered off the digital processor, do not try to power it on again immediately. Doing this may result in failure anyway.

2 POWER indicator

This lights when the digital processor is powered on. Press this in to power on the digital processor. Press it once more to power off.

> Chapter 1 Overview 52

Rear panel



① MODE SELECT switches
These eight DIP switches select the SCSI ID of the

Inese eight DJF switches select the SCSI IO of the digital processor, the attenuation compensation setting for the camera cable, and other settings.

For details of the settings of these switches, see the section "Setting the DIP Switches" on page 46.

SCSI In and SCSI Out connectors (50-pin, high-density)

Use these to connect to other SCSI devices (computer, printer, and so forth).

For details of the SCSI connections, see the section "SCSI. Connections" on page 44.

© FLASH connector (X-contact socket)
Connect the cable from a flash unit

Connect the cable from a flash unit.
This connector is not used when the cable from the flash unit is connected to the FLASH connector on the camera head.

♠ ~ AC IN connector. Use the supplied power cord to connect to a 120 V AC power outlet.

© CAMERA connector (26-pin, female) Connect this to the PROCESSOR connector of the camera head with the supplied camera cable. © REMOTE RS-232C connector (D-sub 9-pin, female) (Undefined.)

© REMOTE FS (foot switch) connector (minijack)
Connect the cable from a foot switch.

MONITOR connectors

These connectors output signals for input to a video monitor.

VIDEO output connector (BNC type): Using a 75-ohm coaxial cable (not supplied), connect this to the composite video input connector (BNC type) of the monitor.

S-VIDEO output connector (Mini-DIN, 4-pin): Using an S-video cable (not supplied). connect this to the S-video input connector of the

monitor.

R, G, B, and SYNC output connectors (BNC type):
Using 73-ohm coaxial cables (not supplied),
connect these to the RGB and sync input
connectors of the monitor.

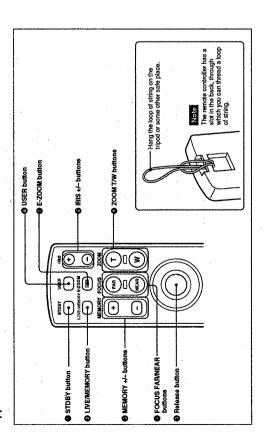
1-8

Chapter 1 Overview

4

Remote Controller

Upper section



D STDBY (standby) button

(VCL-1205BYS), pressing this button puts the camera When using a lens other than the special-purpose lens into the standby mode, and stops down the iris to the current setting.

B LIVE/MEMORY button

output to the monitor. At this point the lens iris is Live mode: The image from the camera lens is Pressing this button toggles between the live and memory monitor modes.

automatically in the fully-open position.

Memory mode: The image saved in the currently selected memory is output to the monitor.

In the memory monitor mode, these buttons select the MEMORY +/- buttons

MEMORY +: Pressing this button switches from the numbered memory $(1 \longrightarrow 2 \longrightarrow 3 \dots)$. MEMORY \rightarrow : Pressing this button switches from the currently selected memory to the next higher currently selected memory to the next lower numbered memory $(9 \rightarrow 8 \rightarrow 7 \dots)$.

POCUS FAR/NEAR buttons

FOCUS FAR: Pressing this button focuses further These adjust the focus.

FOCUS NEAR: Pressing this button focuses closer to the camera.

D E-ZOOM (electronic zoom) button

button to focus quickly. Pressing the button once more center of the live image (E-ZOOM mode). Use this exits the E-ZOOM mode, and returns to the normal Pressing this button zooms in at a 2:1 ratio on the image.

Using other lenses: If the camera is in standby mode, image to memory. If not in standby mode, the iris is first stopped down to the current setting (which

image to memory.

Pressing this button instantaneously stops down Using the special-purpose lens (VCL-1205BYS): the iris to the current setting, and captures the

Press this button to take a picture.

B Release button

pressing this button instantaneously captures the

takes several seconds), and then the image is

captured to memory.

In the E-ZOOM mode, only the FOCUS FAR/NEAR and ZOOM T/W buttons operate.

@ IRIS +/- buttons

These adjust the lens iris.

IRIS +: Pressing this button increases the numerical value of f-stop (i. e. stop down the iris).

IRIS -: Pressing this button decreases the numerical value of f-stop (i. e. open up the iris).

When using the Control Software, press this button to recall user files (USER1, USER2, and USER3).

Recalling one of these files automatically sets up the system according to the settings in the file. You can

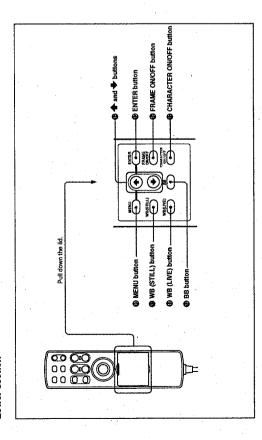
also use this button to switch from one user file to

For details of the Control Software, see Chapter 5.

SOOM T/W (telephoto/wide angle) buttons

ZOOM W: Pressing this button zooms out. ZOOM T: Pressing this button zooms in. These control the zoom.

Lower section



MENU button

Press this button to access the menus.

Press and release quickly to display the main menu on the monitor screen. Press once more to clear the menu from the monitor screen.

Hold down the button for at least two seconds to display the live menu.

For details of the menus, see Chapter 4.

W WB (white balance) (STILL) button

Hold this button down for at least two seconds to adjust the white balance for the still image captured to merory. This carries out the white balance adjustment automatically, with the lens iris stopped down to the current setting.

For details of the procedure for white balance adjustment, see page 27.

WB (white balance) (LIVE) button

Hold this button down for at least two seconds to adjust the white balance for the live image output. This carries out the white balance adjustment automatically, with the lens tris in the fully open position.

For details of the procedure for white balance adjustment see page 27.

BB (black balance) button

Hold this button down for at least two seconds to adjust the black balance.

For details of the procedure for black balance adjustment, see page 26.

🖨 🗣 and 💠 buttons

Press these buttons to move the cursor on the menu screen up and down. Also use them to adjust setting values of certain menu items.

For details of the menus, see Chapter 4.

(ENTER button

Press this button to go down to the next level of the menus, and also to confirm settings made within the menus.

Press this button to toggle the status display on the monitor screen between the on and off settings. The factory default setting is for the status display to be

● CHARACTER ON/OFF button

The status display indicates the live or memory monitor mode, the iris setting, and the memory

enabled.

selection and availability.

For details of the menus, see Chapter 4.

FRAME ON/OFF button

When using the supplied control software, press this button to toggle on and off the display on the monitor screen of a frame showing the effective print area, horizontal and vertical coordinate axes, and a background image.

background image.

The factory default setting is for this display function to be disabled.

For details of the control software, see Chapter 5.

1-10

Chapter 1 Overview

8

Powering On

Use the following procedure to power on:

Note: When using the system for the first time, before powering on be sure to set

For details of the settings of the DIP switches, see page 46.

Power on the monitor, printer, and other peripheral devices.

For details, refer to the user documentation provided with the peripheral

2 Power on the digital processor (press in the POWER switch on the front panel). If the "BEEP" item in the OTHERS menu is set to "ON" (the factory default setting), then the digital processor emits a double beep about five seconds after it is powered on, indicating that it is ready for

For details of the menus, see Chapter 4.

When the digital processor is ready for operation, the system automatically switches to the live monitor mode, and the mode indication "LIVE" is displayed on the monitor screen.

For details of the live monitor mode, see the section "Live and Memory windron Modes" on the next page.

For details of the operating mode indication, see the section "Status Indications" on page 23.

3 If you are using a computer, power on the computer.

For details, refer to the user documentation provided with the computer.

If you power on the computer before the digital processor is ready for operation (after the double beep), the computer may not start up correctly.

Powering On and Off

Use the following procedure to power off.

If you are using a computer, power off the computer.

For details, refer to the user documentation provided with the computer.

 $\boldsymbol{2}$ Power off the digital processor (press the POWER switch on the front panel).

3 Power off the monitor, printer, and other peripheral devices.

Pressing the LIVE/MEMORY button on the remote controller toggles

monitor. At this point the lens ins is automatically in the fully-open position, regardless of the current setting, and "LIVE" appears in the status indication on the monitor in the position indicating the mode.

• In the memory monitor mode, the image saved in the memory currently selected with the MEMORY 4/- buttons is output to the monitor.

"MEMORY" appears in the status indication on the monitor.

"MEMORY" appears in the status indication on the monitor in the position indicating the mode, together with the selection and availability of the memories (1 to 9). between the live and memory monitor modes.

• In the live monitor mode, the image from the camera is output to the

For details of the memory status indication, see the next section.

Status Indications

toggles the status display on the monitor screen between the on and off settings. Pressing the CHARACTER ON/OFF button on the remote controller

memory monitor mode, the irrs setting, and the memory status (selection . When the status display is in the "on" mode, it indicates the live or

and availability of each memory).

• Even when the status display is in the "off" mode, the status display appears, but disappears again after a few seconds.

The following illustration shows the positions of the various status indication items.

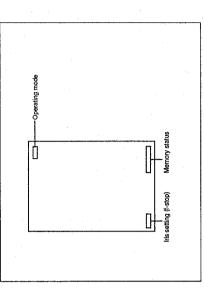
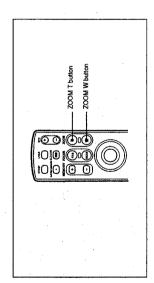


Image Adjustments and Focusing

Adjusting the Zoom



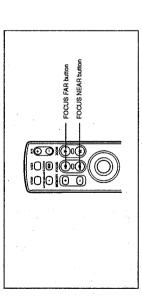


To zoom inPress the ZOOM T (telephoto) button on the remote controller.

To zoom out Press the ZOOM W (wide angle) button on the remote controller.

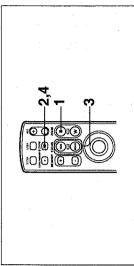
To adjust the zoom angle manually
Set the lens to manual zoom mode, and turn the zoom ring.

Focusing without changing the zoom angle



- · To focus further away, press the FOCUS FAR button on the remote controller.
- •To focus closer, press the FOCUS NEAR button on the remote controller.
 •To focus manually, set the lens to manual focusing mode, and turn the focusing ring.

For most accurate focusing Using the E-ZOOM function, you can focus more accurately.



- Press the ZOOM T button on the remote controller, to select the narrowest telephoto angle.
- 2 Press the E-ZOOM button.

This switches to the E-ZOOM mode, and zooms in at a 2:1 ratio on the center of the image.

- 3 Press the FOCUS FAR button or FOCUS NEAR button to adjust the
- 4 Press the E-ZOOM button.

This exits the E-ZOOM mode, and returns to the normal image.

Note
Until you exit from the E-ZOOM mode, only the FOCUS FAR/NEAR and ZOOM T/W buttons operate.

Adjusting the White Balance

adjustment in the following cases. The adjustment setting is preserved for about ten years, even when the unit is powered off. To obtain images with a natural color balance, carry out black balance

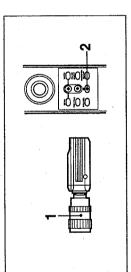
· When using the camera for the first time

. When the camera has not been used for a long interval · When you have replaced the camera

· When the ambient temperature has changed greatly

At the change of the seasons

Use the following procedure to carry out black balance adjustment.



Check that the lens is in automatic iris mode.

If you are using a lens with no automatic iris function, close the iris completely. Hold down the BB button on the remote controller for at least two seconds. This automatically closes the lens iris completely, and carries out black balance adjustment. The message "Reading Color Table..." appears on the monitor screen.

The image on the monitor screen may flicker while the adjustment is in

progress, but this is not a fault.

When the adjustment is completed, the message "BB:OK" appears.

If the adjustment was not achieved

The message "BB:NG Please try again" appears. Repeat the above procedure.

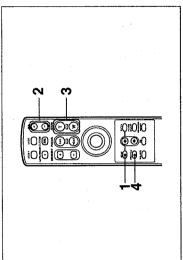
To obtain images with a natural color balance, carry out white balance adjustment in the following cases. The adjustment setting is preserved for

about ten years, even when the unit is powered off.

· When using the camera for the first time · When the lighting conditions change This system keeps separate white balance settings for the still images captured to memory and the live image output to the monitor.

Adjusting the white balance for still images

After setting up the lighting, use the following procedure.



menu, and make the following settings.

• Select the PICTURE menu, and set "C-TEMP" to "AWB" (this is the Press the MENU button on the remote controller to access the main

factory default setting).

•Select the FLASH menu, and if using a flash, set the "FLASH" item to "ON" (this is the factory default setting). Next select the flash If not using a flash, set the "FLASH" item to "OFF." interval with the "INTERVAL" item.

For details of menu operations, see Chapter 4.

2 Press the IRIS +/- buttons to adjust the iris.

For details of the adjustment, see page 30.

(Continued)

Chapter 2 Operation

27

Chapter 2 Operation 26

Adjusting the Black Balance

Adjusting the White Balance

3 Under the same lighting conditions as for shooting, position a white subject (white pattern, white cloth, etc.), and zoom in so that it fills the

Adjusting the white balance for live images

4 Hold down the WB (STILL) button on the remote controller for at least

This automatically starts white balance adjustment. The message "Reading Color Table..." appears on the monitor screen. If the FLASH menu setting is "FLASH ON", the flash flashes several

When the adjustment is completed, the message "WB:OK" appears.

If the adjustment was not achieved
The message "WB:NG Please try again" appears. Repeat the above procedure.

After setting up the lighting, in live monitor mode use the following procedure. 01010 ⊕⊕0 01010 Hold down the MENU button on the remote controller for at least two seconds to access the live menu, and set "C-TEMP" to "AWB".

For details of menu operations, see Chapter 4.

- 2 Under the same lighting conditions as for shooting, position a white subject (white pattern, white cloth, etc.), and zoom in so that it fills the
- 3 Hold down the WB (LIVE) button on the remote controller for at least two seconds.

This automatically starts white balance adjustment. The message "Reading Color Table..." appears on the monitor screen.

When the adjustment is completed, the message "WB:OK" appears.

If the adjustment was not achieved
The message "WB:NG Please try again" appears. Repeat the above
procedure.

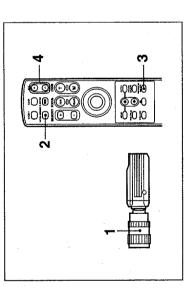
3

Adjusting the Iris

Use the following procedure to adjust the iris setting (f-stop) at the instant that a still image is captured to memory.

Note

For normal purposes select F5.60 (factory default setting).



Set the lens to the automatic iris mode.

2 If in the memory monitor mode, press the LIVE/MEMORY button to switch to the live mode.

The "LIVE" mode indication appears on the monitor screen.

3 If the status display mode is off, press the CHARACTER ON/OFF button to switch on the status display.

The f-stop setting appears on the monitor screen.

- 4 To increase the numerical value of the f-stop (i.e. to stop down the
- iris for still image capture), press the IRIS + button.

 To decrease the numerical value of the f-stop (i.e. to open up the iris for still image capture), press the IRIS button.

Note

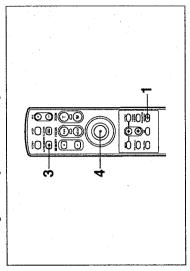
Changing this setting does not affect the iris during output of live images. The f-stop setting only takes effect when the release button is next pressed.

This section describes the procedures for selecting image memories and for shooting (capturing an image into the selected memory).

Basic Procedure

Shooting

The following is the basic procedure for shooting.



If the status display mode is off, press the CHARACTER ON/OFF button to switch on the status display, and check the memory status indication (selection and availability).

2 Select the memory into which you wish to capture an image.

For details, see the section "Selecting a Memory" on page 33.

4

If you select a memory which already holds an image, carrying out step 4 will overwrite this existing image in the memory.

3 If in the memory monitor mode, press the LIVE/MEMORY button to switch to the live monitor mode.

The "LIVE" mode indication appears on the monitor screen.

(Continued)

4 Press the release button.

This instantaneously stops the lens down to the iris setting, and captures an image to the selected memory.

When you switch to standby mode, after about 3 seconds the iris is stopped down to the current setting, and the "STAND-BY" mode When using a lens other than the special-purpose lens (VCL-1205BYS), press the STDBY button first, to switch to the standby mode, then press the release button. indication appears on the monitor screen.

- switches to review mode, and the image captured to memory appears When the memory selection mode is "increment on," first the system Next the system switches back to live monitor mode. During the review, the "REVIEW" mode indication appears on the monitor on the monitor for a few seconds.
- When the memory selection mode is "increment off," the system switches to memory monitor mode, and the image captured to memory is displayed on the monitor screen.

For details of the memory selection mode, see the section "Selecting a Memory" on the next page.

5 To continue shooting, repeat steps 2 to 4.

Selecting a Memory

There are two modes in which you can select a memory: "increment on" mode, and "increment off" mode.

"Increment on" mode

In "increment on" mode, each time you press the release button to capture an image in memory, the memory selection for the next image is incremented automatically (1, 2, 3, ... 9). The following shows how the memory selection changes in "increment on" mode.

An image is captured in memory 2, and after a transient review, the system again returns to the live monitor mode. Pressing the release button once again will capture an image to memory 3. When using the system initially, memory 1 is selected for image capture. (The white squares indicate that the memories do not hold images.) An image is captured in memory 1, and after a transient review, the system returns to the live monitor mode. (The film icon indicates a memory which hodds an image.) Press release button again... Press release button... Memory status indication

To select the "increment on" mode
From the main menu, select the CAMERA menu, then set the
"INCREMENT" item to "ON" (this is the factory default setting).

For details of menu operations, see Chapter 4.

Shooting

"Loop on" mode and "loop off" mode

"Increment off" mode

In "increment on" mode, there are two ways of operating; either the "loop on" mode or the "loop off" mode.

"Loop on" mode: After an image is captured to memory 9, memory 1 is automatically selected for the next image.

If there is already an image stored in memory 1 (i.e. the status indication shows a film icon for memory 1), when you press the release button this captures a new image to memory 1, and the previously stored image is lost.

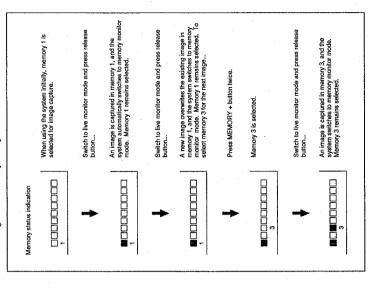
"Loop off" mode: After an image is captured to memory 9, there is no automatic memory selection, and after the review, the system returns to the live monitor mode with a message "MEMORY FULL" display.

To switch between "loop on" mode and "loop off" mode From the main ment, select the CAMERA ment, then set the "LOOP" item to "ON" (this is the factory default setting) or "OFF."

In "increment off" mode, when you press the release button there is no automatic memory selection. To select the memory, enter memory monitor mode, then press the MEMORY +/- buttons. To select memory numbers in ascending order press the MEMORY + button, and to select memory numbers in descending order press the MEMORY - button.

The MEMORY +/- buttons do not operate in the live monitor mode. First press the LIVEMEMORY button to switch to memory monitor mode.

The following shows an example of operation in "increment off" mode.



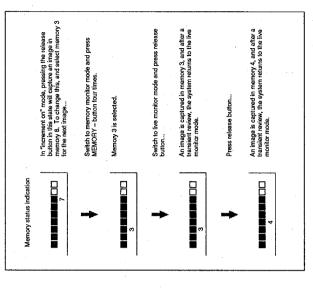
Shooting

To select the "increment off" mode From the main menu, select the CAMERA menu, then set the "INCREMENT" item to "OFF."

For details of menu operations, see Chapter 4.

Manually selecting a memory while in "increment on" mode

Even in "increment on" mode, you can use the MEMORY +/- buttons to select a memory.
The following shows an example of such operation.



Preventing overwriting of memory

By control from the computer, you can lock any particular memory so that it cannot be overwritten. When a memory is locked in this way (status indication $\frac{1}{10}$) the memory selection basically proceeds in the same way as before, except that any locked memories are skipped. In other words, if only memory 4 is locked, an image which would have been captured in memory 4 is in fact captured in memory 5.

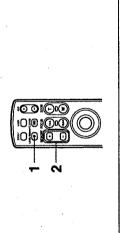
In "increment on" mode

and display the captured image on the monitor screen for several seconds. Thereafter it automatically returns to the live monitor mode. Thus you can release button causes the system to automatically switch to review mode, When the memory selection mode is set to "increment on," pressing the continue to capture images while quickly checking each one.

In "increment off" mode

In this mode, pressing the release button causes the system to switch to the memory monitor mode, and display the captured image on the monitor screen. The same image remains displayed until you select the live monitor mode again.

Selecting memory monitor mode to check images
Whether in the "increment on" or "increment off" mode, you can check the
contents of any particular memory by using the following procedure.



If not in memory monitor mode, press the LIVE/MEMORY button to switch to the memory monitor mode.

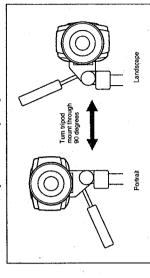
This displays the image held in the currently selected memory on the

2 Press the MEMORY +/- buttons to select other memories to check.

This displays the image held in each selected memory on the monitor.

Switching Between Landscape and Portrait Orientations

You can turn the tripod mount through 90 degrees to alter the camera orientation, for either portrait or landscape images.



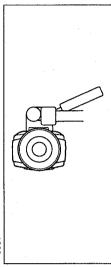
Menu and status indications

When the camera head is in the landscape orientation, the display of menus and status information on the monitor screen is also turned through 90

To make these displays horizontal as in the portrait mode, from the main menu select the CAMERA menu, and set the "ROTATE" item to "OFF."

For details of menu operations, see Chapter 4.

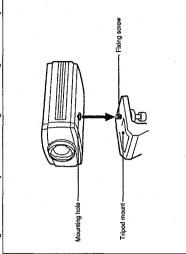
- Physically rotating a monitor through 90 degrees may damage it or cause • It is recommended to use a portrait monitor when shooting in portrait format and a landscape monitor when shooting in landscape format. it to fail.
 - •When using the camera head in the orientation shown below, select the CAMERA menu from the main menu and set the "ROTATE" item to "OFF."



Installing the Camera Head

Mounting the Camera Head on a Tripod

Align the threaded mounting hole on the bottom of the camera with the fixing screw on the tripod mount, and tighten the fixing screw securely.

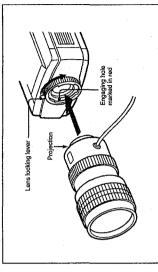




Note The tripod must be designed to support a load of at least 5 kg.

Mounting the Lens

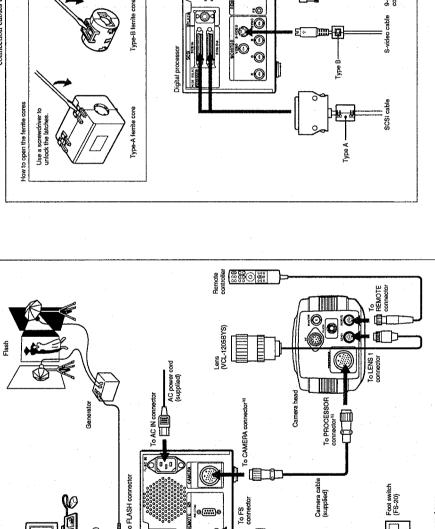
Insert the lens into the camera lens mount, then press down the lens locking lever in a clockwise direction. Make sure that the lens is inserted with the projection on the mounting end in alignment with the engaging hole marked in red.



Connections

The connections required depend on the system components being used.

See the section "Principal Examples of Use" on page 9 to determine the required devices.



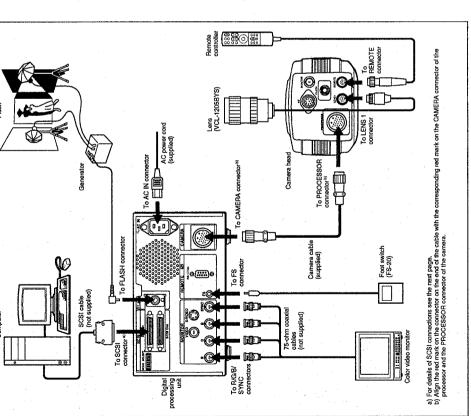
Type-B ferrite core

Type B

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Attaching Ferrite Cores to Cables

To protect the digital processor against noise which may affect its operation, attach supplied ferrite cores (noise filters) to some of the connection cables as shown below.



Chapter 3 Installation and Connections 42

SCSI Connections

This section describes the SCSI connections between the DKC-ST5 and a computer and any other SCSI devices.

Power off the DKC-STS and all of the other connected devices before making the SCSI connections.

SCSI Cables

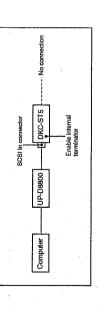
It is recommended to use high-impedance cables complying with the SCSI standard for the connections.

Up to seven SCSI devices can be connected in a daisy-chain to the SCSI port of the computer, but the total length of the SCSI cables should not exceed 6 meters.

SCSI Terminators

Using the DKC-ST5 internal terminator

external terminator, then as shown in the following illustration, connect the DKC-STS as the last device in the chain, and enable the internal terminator. (Set DIP switch 4 on the digital processor to the ON position; The DKC-ST5 has a built-in active SCSI terminator. If you do not have an see page 46.)



SCSI ID

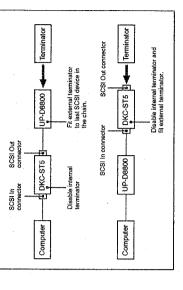
Not using the DKC-ST5 internal terminator

In this case, carry out the two following items:

• Disable the DKC-STS internal terminator. (Set DIP switch 4 on the digital processor to the OFF position; see the next page.)

• Fit an external terminator to the last SCSI device in the chain. It is

recommended to use an active type of external terminator.



Note

When you connect a number of SCSI devices in a daisy-chain, it is important to assign SCSI IDs (0 to 7) to the devices in such a way that they

The maximum number of terminators which can be used in a SCSI chain is

For details of the settings of the DIP switches, see the next page.

To set the SCSI ID of the DKC-STS, use DIP switches 6 to 8 on the digital

do not conflict.

Note that the allocation of IDs is not related to the physical sequence of the devices in the chain.

Setting the DIP Switches

There are eight DIP switches on the rear panel of the digital processor. When using the system for the first time, before powering on set these switches appropriately.

The following shows the function of each of the DIP switches.

Main menu

All settings affecting still images (i.e. those captured into memory) are in the main menu, which consists of a number of submenus.

You use the menus to make various camera settings such as the color temperature conversion characteristics, the electronic shutter speed, and so

There are two principal menus, the main menu, and the live menu.

For details of the settings in the main menu, see page 51.

To access the main menu Press the MENU button.

The live menu contains only color balance settings for live images (i.e. the camera output in live monitor mode).

Live menu

For details of the settings in the live menu, see page 54.

To access the live menuHold down the MENU button for at least two seconds.

7 OFF OFF ON ON OFF OFF ON ON ON 兆 Š Note Carry out the DIP switch setting operation with the power of the system turned off. OFF Š 병 8 CIISOS compensation (ON: 25 m, OFF: 10 m) © 0 0 0 0 SCSI active terminator Unused (set to OFF) Unused (set to OFF) Unused (set to OFF) 100g 9FF Camera cable Unit SCSI ID 1 2 3 병 ĕ

46 Chapter 3 Installation and Connections

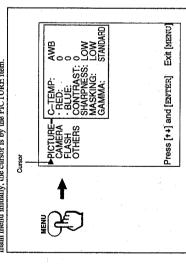
Basic Menu Operation Procedure

This section illustrates the basic menu operation procedure, taking as an example the method of setting the ISO exposure index (the "film speed"). Note that the procedure for accessing the live menu is exactly the same, except that you must hold down the MENU button for at least three

Use the following procedure to set the ISO exposure index.

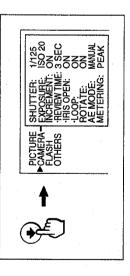
Press the MENU button.

The main menu appears on the monitor screen. When you access the main menu initially, the cursor is by the PICTURE item.



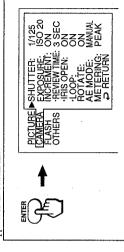
2 Press the ♥ button to align the cursor with "CAMERA."

This selects the CAMERA menu, and the following items appear.

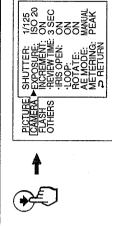


3 Press the ENTER button.

The cursor enters the CAMERA menu, and the extra "RETURN" item appears in the bottom line.

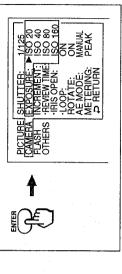


4 Press the \$\inf\$ button to align the cursor with "EXPOSURE."



5 Press the ENTER button.

This displays the setting list for the "EXPOSURE" item, with the cursor pointing to the current setting.

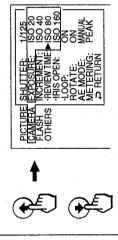


(Continued)

48 Chapter 4 Menus

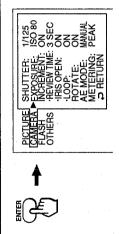


setting.



7 Press the ENTER button.

This updates the setting for "EXPOSURE."



8 To exit from the menus, press the MENU button.



The main menu disappears from the monitor screen.

The main menu consists of the following submenus:

• PICTURE menu

• CAMERA menu

• FLASH menu

• OTHERS menu

The following tables list the settings in each submenu.

PICTURE menu

Menu item	Settings
C-TEMP Set the color temperature conversion characteristics of the camera to match the light source being used.	AWB: Automatically select the color balance setting taken for still images. S600K. Use this setting for daylight or fluorescent fighting. 3200K. Use this setting for lungsten lighting.
•RED Adjust the red gain. (After a white balance adjustment, it is not normally necessary to set this item.)	–127 to +127 Factory default setting: 0
•BLUE Adjust the blue gain. (After a white balance adjustment, it Factory default setting: 0 is not normally necessary to set this item.)	-127 to +127 Factory default setting: 0
CONTRAST Adjust the contrast.	–5 to +5 Factory default setting: 0
SHARPNESS Adjust the degree of outline emphasis (low, middle, or high)	OFF: No outline emphasis LOW, MIDDLE, HIGH Factory default setting: LOW
MASKING Adjust the degree of increasing the color saturation (low, medium, and high).	OFF: No increase in the color saturation. LOW, MIDDLE, HIGH Factory default setting: LOW
GAMMA Adjust the gamma correction setting.	STANDARD: Use the standard setting. PRESET: PRESET2. PRESET3: Use the respective PRESET1. PRESET2. and PRESET3 settings. Factory default setting: STANDARD

Menu Settings

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Menu item	Settings
Set the camera shutter speed (in seconds)	4, 2, 1, 1/2, 1/26, 1/50, 1/60, 1/125, 1/250, 1/500, 1/1000, 1/2000, 1/5000, 1/10000 1/2000, 1/5000, 1/10000 Factory default setting: 1/125
EXPOSURE Set the ISO exposure index for the camera	ISO 20, 40, 80, 160 Factory default setting: ISO 20
INCREMENT Select whether the memory selection is incremented automatically or not. For details of the method of memory selection, see page 33.	ON: The memory selection is incremented automatically. When this setting is selected; the menu items "AEVIEW TIME", "AIRIS OPEN", and "ALOOP" appear. OFF: The memory selection is not incremented automatically. Factory default setting: ON
*** *** *** *** *** *** *** *** *** **	0 sec, 1 sec, 3 sec, 6 sec, 10 sec Factory default setting: 3 sec
INFO OPEN Whon the above "INCREMENT" them is set to "ON," selled whether after the release button is pressed to return to the live monitor mode, or not (i.e. to return the lens iris to the fully open position)	ON: Return to the live monitor mode. Use this setting when year which to check each new image on the monitor screen. OFF: Do not return to the live monitor mode (keep the iris setting unchanged). Use this setting when the timing of manage capture is important. Fecroty default setting:
•LOOP When the above "INCREMENT" item is set to "ON." select whether or not to use the memory loop mode. For details, see page 34.	ON: Select the memory loop mode. After capturing an image to memory 9, memory 1 is automatically selected for the next image. OFF: Do not select the memory loop mode. After capturing an image to memory 9, the message 'MEMORY FULL' appears. Factory default setting: ON
HOTATE Select Whether or not to rotate the menu and status displays on the monitor screen through 90 degrees.	ON: Rotate the displays through 90 degrees. Use this setting to make it easier to check the displays in portral format. OFF: Do not rotate the displays through 90 degrees. Use this setting to make it easier to check the displays in landscape format.
AE MODE Select the manual or automatic exposure mode.	MÄNUAL: Manual adjustment APERTURE: Aperture priority automatic exposure SHUTITE: Shuter priority automatic exposure PROGRAM: Program automatic exposure Factory default setting: MANUAL
METERING Select the exposure metering method in the live monitor mode.	PEAK. Adjust exposure to the maximum intensity "cell of image as defined by dividing the whole image into nine cells having the same area. (The illustration below shows a case in which the maximum intensity cell is the central cell.) With this setting the surroundings of highlights may become dark.
	Maximum intensity cell of image
	AVG: Adjust exposure to the average intensity of the image. With this setting there may be "burn-out" in the highlights.

FLASH menu

Menu item	Settings
FLASH Select whether or not to use the flash.	ON: Use the flash. When this setting is selected, the menu items "WENAL", "CUTOFF SW", and "CALIBATION" appear. "CALIBATION" appear. Items of the flash.
INTERVAL When the above "FLASH" item is set to "ON," select the lash operation interval.	1 sec, 3 sec, 5 sec, 10 sec Factory default setting: 3 sec
	Note If the setting is less than the charge time for the flash used, correct automatic control will not be possible.
CUTOFF SW When the above "FLASH" item is set to "ON" select When the above "FLASH" item is set to "ON" select whether or not to carry out mechanical control of the Accountants on the FLASH connector of the digital processor.	ON: Carry out mechanical control. OFF: Do not carry out mechanical control. Factory default setting: OFF
	Note Even when set to "ON," there is no mechanical control of the X-contacts on the camera.
-CALIBRATION When the above FLASH* item is set to "ON," measure the synchronized timing of the flash and the electronic shuffer.	START: Carry out the measurement. CANCEL: Do not carry out the measurement.

OTHERS menu

Menu item	Settings
BEEP Select whether or not to beep for confirming an operation. OFF: Do not beep. Factory default es	ON: Beep. OFF: Do not beep. Factory default setting: ON
RESET Reset all of the user menu settings to their factory defaults.	START: Carry out the reset. CANCEL: Do not carry out the reset.

Menu Settings

	Menu item The live menu coi	The live menu consists of the following settings. Settings AWB: Automatically select the color balance setting taken for
	•RED Adjust the red gain. (After a white balance adjustment, it is not normally necessary to set this item.)	-127 to +127 Factory default setting: 0
-127 to +127 Adjust the red gain. (After a white balance adjustment, it Factory default setting: 0 is not normally necessary to set this item.)	Adjust the blue gain. (After a white balance adjustment, it Factory default setting: 0 is not norwally necessary to set this feam.)	-127 to +127 Factory default setting: 0

Overview

The two floppy disks supplied with the DKC-ST5 contain control software which runs under Microsoft Windows 9513 and is designed for the following

· Capturing image data from the DKC-ST5 into the

• Carrying out settings and basic operations on the DKC-STS from the computer computer

installation, and also some notes on SCSI connections. running the control software and the procedure for Chapter 5 describes the environment required for

Background information

Microsoft Windows 95 (Microsoft Windows User's familiar with basic computer terminology, and has read the introductory documentation relating to The following explanation assumes the reader is Guide and so forth).

Required Hardware

Computer

An IBM PC/AT2 or compatible computer with the following specification is required:
• CPU: Intel Pentium or PentiumPro³)

RAM capacity: at least 32 MB

· Monitor: 24-bit full-color display function Free disk space: at least 150 MB

SCSI Host Adapter with Windows 95 Support

One of the following Adaptec host adapters is SCSI Direction AHA-1522 (for ISA bus)

 The SCSI Master AHA-1540CP (for ISA bus) PCI SCSI Master AHA-2940 (for PCI bus)

SlimSCSI APA-1460 (for PCMCIA slot)

 The control software may not run correctly using the Microsoft Windows 95 built-in SCSI host adapter device driver and ASPI⁴⁾32 manager.

manager stored on the miniport driver disk for Microsoft Windows 95 shipped with the host adapter. After installation, in the System folder within the directory for Windows, the following file should be · Be sure to install the device driver and ASPI32

Also in the iosubsys folder beneath this should be the Device driver for the SCSI host adapter ASPI32 manager (wnaspi32.dll) following:

driver from Adaptec's EZ-SCSI4) Pro Version 4.5 for · If your host adapter was not shipped with a miniport driver disk for Microsoft Windows 95, install a new Check that these files are dated no earlier than (sparrow.mpd/ahal54x.mpd/aic78xx.mpd) November 1, 1995.

Required Software

Microsoft Windows 95.

The Microsoft Windows 95 operating system is required.

¹⁾ Microsoft and Windows are registered trademarks of Microsoft Corporation.

²⁾ IBM and AT are registered trademarks of International Business Machines Corporation.

³⁾ Pentium and PentiumPro are registered trademarks of

Intel Corporation.
4) ASPI and EZ-SCSI are registered trademarks of Adaptec Inc.

Installing the Control Software

Preparations

Checking the SCSI Host Adapter

Use the following procedure to check that the Adaptec SCSI host adapter is correctly installed.

- 1 Start Microsoft Windows 95.
- 2 Open the Control Panel window, and double-click the System icon.
- 3 Click on the Device Manager tab, and check that the host adapter to be used for the SCSI controller is displayed.

Connecting the DKC-ST5 and computer

Power off the computer, the DKC-STS, and any other devices in the system, then connect the DKC-STS and the computer with the SCSI cable.

For details of the SCSI connection, see pages 42 to 45.

Setting the DKC-ST5 SCSI ID

With all of the devices powered off, set DIP switches 6 to 8 on the rear panel of the digital processor to a SCSI ID which does not conflict with other devices.

For details, see pages 45 and 46.

Note

The DKC-ST5 does not support SCAM (SCSI Configured Automatically). Even when using a host adapter which supports SCAM, you must manually set an ID which does not conflict.

For details, refer to the documentation provided with your SCSI host adapter.

Powering on the system

Following the description on page 21, power on the system.

Installing the Control Software

Use the following procedure to install the control software in the computer.

1 Start Microsoft Windows 95.

When starting Microsoft Windows 95 for the first time after connecting the DKC-ST5 and the computer

In this case a dialog box appears, prompting you to install a device driver. Since the control software is supported by the ASP132 manager, no driver installation is required. In the dialog box, select "Do not install a driver and click the OK button. When starting Microsoft Windows 95 for the second and subsequent times, this operation is not required. The dialog box will, however, appear again if you change the SCSI ID of the DKC-STS.

- Insert floppy disk 1 supplied with the DKC-ST5 into the floppy disk driver.
- 3 Access the floppy disk, and display a file list.
- 4 Double-click SETUP.EXE.

The setup program starts. Thereafter, follow the directions given in dialog boxes.

Using the Control Software

Installing the control software simultaneously installs a readme.doc file. For details of how to use the control software, consult the readme.doc file.

.

The control software will hang if after starting it you do any of the following on the DKC-STS:

• Power off the digital processor

Power off the digital processor
Disconnect the camera cable from the digital
processor or camera head

 Disconnect the SCSI interface between the DKC-ST5 and the computer
 If the control software hangs, it will be necessary to

restart the system.

Uninstalling the Control Software

Use the following procedure to uninstall the control software.

Note

Before beginning this operation, check the following.

- The control software is not running.
 The readme.doc file about the control software is not
 - The readme doc file about the control software is in open.
- Open the Microsoft Windows 95 Control Panel window, and double-click "Add/Remove Programs," then click on "Install/Uninstall."
- Programs," then click on "Install/Uninstall."

 Select "DKC-ST5" from the list of programs, and click the Add/Remove button.

Thereafter, follow the directions given in dialog hoxes.

Error Messages

If the system is unable to operate normally because of a particular combination of operations, an error message may appear on the monitor. The following table lists the error messages, with their meanings and appropriate action to take.

Message	Meaning	Action to take
BB:NG Please try again	Black balance adjustment failed.	Check the conditions, and try again.
COMMANDER IS LOCKED	The remote controller has been locked by the computer.	Unlock the remote controller from the computer.
FLASH CALIBRATION: NG	Flash calibration failed.	Check the conditions, and try again.
MEMORY FULL	The memory for captured images is full.	Delete images no longer required from memory.
MEMORY IS LOCKED	The currently selected memory for captured images is locked (write-protected).	Either unlock this memory, or select a different memory.
Not available during transferring image data	This operation is not possible during image data transfer.	Wait until image data transfer is finished.
Not available except AWB setting as C- Temp	This operation is not possible unless the "C-TEMP" menu item is set to "AWB."	Set the "C-TEMP" menu item to "AWB."
Not available in E-ZOOM mode	This operation is not possible in the electronic zoom mode.	Exit from the electronic zoom mode and retry.
WB:NG Please try again	White balance adjustment failed.	Check the conditions, and try again.

If a problem should occur when you are using the system, consult the following troubleshooting chart, and carry out any suggested action. If you are unable to resolve the problem, or if the problem occurs persistently, contact your Sony dealer.

Symptom	Cause / Items to check / action to take
The system does not power on.	is the power cord connected correctly? (See page 42.)
The buttons of the remote controller do not operate.	is the remote controller cable connected correctly? (See page 42.)
There is no image on the monitor.	Are the video signal cables (R, G, B) connected correctly? (See page 42.)
The monitor image colors are peculiar.	
Captured images are blurred.	There may be blurring when photographing a fast-moving subject. This is not a fault.
The picture quality from the camera is poor.	Is the attenuation compensation setting for the camera cable set correctly? Set DIP switch 1 according to the length of the cable. (See page 46.)
Changing the camera settings does not affect the image on the monitor.	If the image being displayed is from memory, there will be no change. In this case, switch from memory monitor mode to live monitor mode.
There are a number of horizontal lines on a live image.	When shooting a moving subject, this may occur in the image on the monitor. This is not a fault, and does not affect captured images.
When I change the focus and zoom settings, the lens does not respond.	Set the FOCUS/ZOOM switch to the POWER position.
When an image is transferred to a computer and displayed, the color gradation is completely different from that in the monitor image.	In this system, when an image is displayed on the monitor the color look- up table is optimized to give the best gradation. The image may not therefore necessarity appear identically on a computer screen.
When a captured image is displayed on the monitor, the color gradation is peculiar.	Have the gamma settings been changed since the image was captured? For the same reason as in the previous item, if the current gamma settings are different from the gamma settings when the image was captured, it will not be displayed correctly on the monitor.
When shooting with the flash on, the image flickers at the instant an image is captured to memory.	This is not a fault.
The color temperature of the live image sometimes changes under fluorescent lighting.	This is caused by the electronic shutter reacting to the high frequency variations in thensity which are a characteristic of fluorescent lighting, and for a fault. When using flash the images shot are not affected, if not using flash, as far as possible shoot under fungsten lightling.
However I change the settings, the dynamic range of an image transferred to the computer vides not fill the 0 to 255 range.	is the "CONTRAST" setting in the PICTURE menu set to a negalive value? (See page 51.)
I have used cropping on images captured to be memory to transfer them part by part to the computer. However, it does not seem to be prossible to join them back together again correctly.	Because of the details of the image processing algorithm, it is not in general possible to join parts of images back together without subtle problems of pixel mismatch.
The computer does not recognize the digital processor.	
SCSI operations are unstable.	Is the SCSI terminator fitted correctly? (See page 44.)

Do not obstruct ventilation openings

To prevent the camera and processor from overheating, do not obstruct ventilation openings, by for example wrapping the camera and processor in a cloth while they are in operation.

Operation and storage locations

- Avoid operation or storage in any of the following places.

 *Location subject to extremes of temperature (operating temperature range 5°C 35°C (41°F to 95°F))
 - · Location subject to direct sunlight for long periods, or close to heating appliances (Note that the interior of a car left in summer with the windows closed can exceed 50°C (122°F).)
 - Damp or dusty places
 - · Location subject to severe vibrations
- Location near equipment generating strong electromagnetic emissions · Location near transmitting stations generating strong radio waves

Operate the processor in a horizontal position

The processor is designed to be operated in a horizontal position. Do not operate it on its side, or tilted through an excessive angle (exceeding 20°).

Avoid violent impacts

Dropping the camera and processor, or otherwise imparting a violent shock to them, is likely to cause them to malfunction.

event of extreme dirt, use a cloth steeped in a neutral detergent to remove the dirt, then wipe with a dry cloth. Applying alcohol, thinners, insecticides, or other volatile solvents may result in deforming the casing If the casing or panel is dirty, wipe it gently with a soft dry cloth. In the or damaging the finish.

Shipping

Pack the camera and processor their original carton or equivalent packing, and take care not to impart violent shocks in transit.

Specifications

	Camera head		External dimensions	
				120 × 90 × 188.5 mm
	Imaging element	² / ₃ -inch CCD, with approximately 1,400,000 pixels	Mass	$(4.74 \times 3.78 \times 1.71 \text{ inches})$ Approx. 1,300 g (2 lb 13 oz)
	Effective image area	ea		
		$1,280 \text{ (H)} \times 1,024 \text{ (V) pixels}$	Digital processor	Sor
	Pixel offset	Vertical and horizontal	•	
	Photography system	II 3 alements	Input A/D	10-bits/color (R, G, B)
	I and mount	2 dements 24-inch 48 havonet mount	Gamma	STANDARD, PRESET1,
	Auto exposure	Program (live mode, with flash		PRESETZ, PRESET3
	•	off)	Output LUT	All 8-bit (R. G. B)
		 Shutter priority (with flash off) 	Processing	
		 Aperture priority (with flash off) 	Sharpness	OFF, LOW, MIDDLE, HIGH
		• Manual	Contrast	-5 to +5
	Exposure compensation	sation 5 to ±5	Masking	OFF, LOW, MIDDLE, HIGH
	Sensitivity	C+ 03 C-	Memory	Image memory size: 1,280 (H)
	Live mode	Auto (ISO 20 to 640)		× 1,024 (v) × 3 (A IKUB)
	Still mode	ISO 20, 40, 80, 160	External interface	× 8 (bits) SCSI: SCSI-2
	White balance	· Auto (with flash on/off)	Contraction interest	Transfer method: Asynchronous
		• Manual (R and B gains: (-127 to		transfer
		+127)		Data bus width: 8 bits
	Electronic shutter	1		Termination method: Single-Ended
	Live mode	Auto, 1/25, 1/50, 1/125, 1/250,	Input/output and control connectors	ontrol connectors
		1/500, 1/1000, 1/2000, 1/4000,	1	SCSI connectors (loop-through):
		1/10000		50-pin, half-pitch
	Still mode	4, 2, 1, 1/23, 1/30, 1/123, 1/230,		FLASH: X contact (terminal
		1/300, 1/1000, 1/2000, 1/4000,		voltage: Less than 400 V)
	Input/output and	Introduction and control connectors		RS-232C: D-sub 9-pin (undefined)
	vintage on the continu	PROCESSOR: 26-nin, analog		FS: Mini-jack
		RGB (diagonal color offset)		VIDEO: BNC type
		LENS 1: 25-pin		S-VIDEO: Mim DIN 4-pin
		LENS 2: 12-pin		R, G, B, and S I NC: BNC-type D G B: 0.7 Vp. 75 chme
		VIDEO OUT: BNC type		SYNC: 4 Vn-n 75 ohms
		REMOTE: 12-pin		
		VF: 8-pin, DIN		Note
		voltage: 400 V or less)		Pixel skew offset signal is output
	Power requirements			45 It IS.
	•	12 V DC	Others	Camera cable length compensation:
	Current consumption	ion	Omers	10 m / 5 m
		6.0 W		Power requirements: 120 V. 50 Hz/
	Ambient operating temperature	g temperature		60 Hz
		5°C (41°F) to 35°C (95°F)		Current consumption: 0.45 A
,	Ambient storage temperature	emperature		External dimensions (w/h/d): $211 \times$
		-20°C (-4°F) to +60°C (140°F)		365 × 128.5 mm
				$(8^3/8 \times 14^3/8 \times 5^4/8 \text{ inches})$
				Mass: Approx. 6 kg (13 lb 3 oz)

Accessories supplied

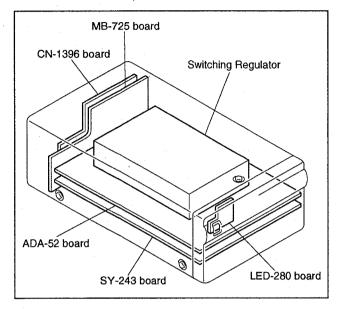
Remote controller (1)
Camera cable (1)
AC power cord (1)
Ferrite cores (4)
3.5-inch floppy disks containing the control software (2)
Operating Instructions (1)

62 Appendix

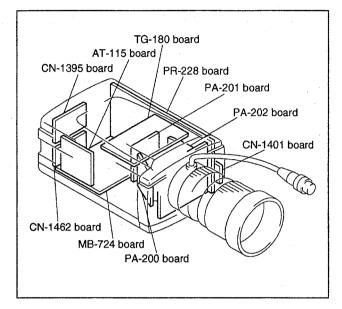
EX 631 EXPER SECTION 2 EX 635 SERVICE INFORMATION

2-1. BOARD LAYOUT

• Processor block

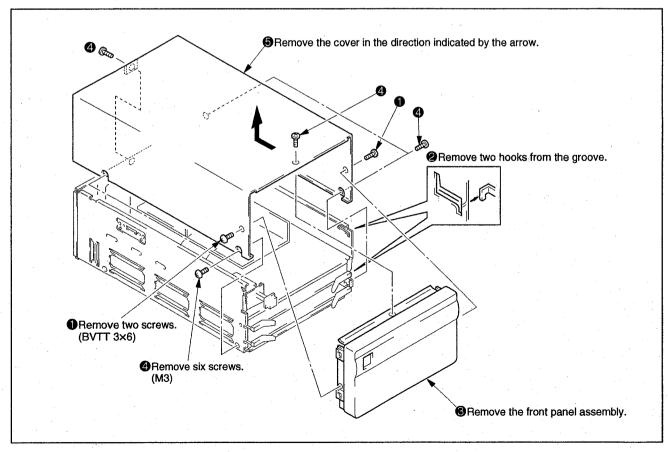


• Camera block

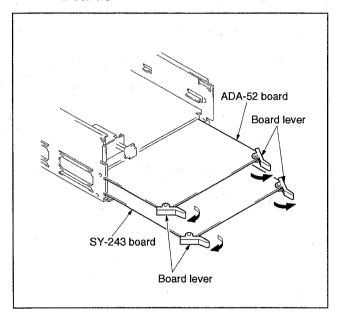


2-2. PROCESSOR BLOCK DISASSEMBLY

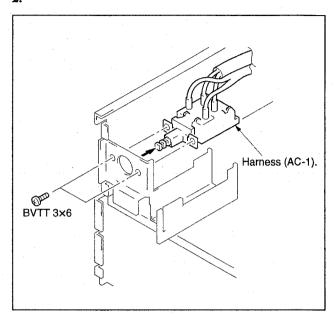
2-2-1. Removal of Cabinet



2-2-2. Removal of the ADA-52 and SY-243 Boards

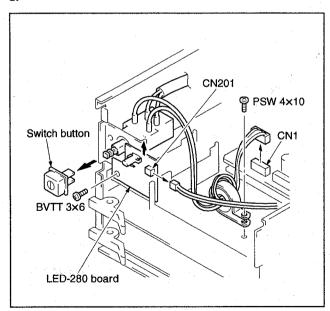


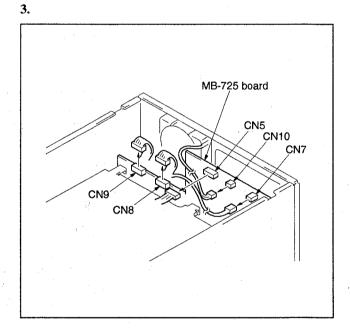
2.



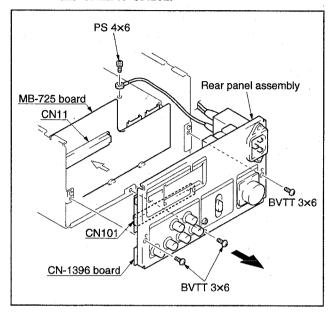
2-2-3. Removal of the Rear Panel Block

1.

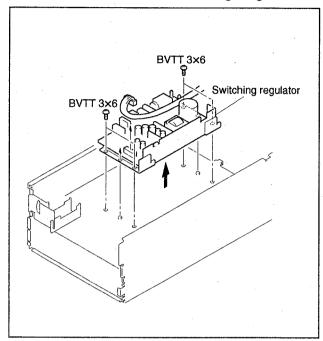




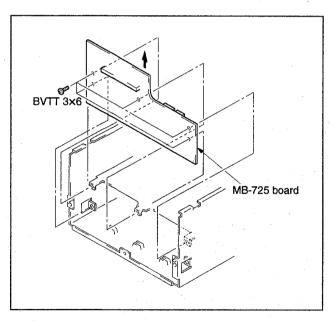
NOTE: When assembling rear panel assembly, connect the CN11 to CN101.



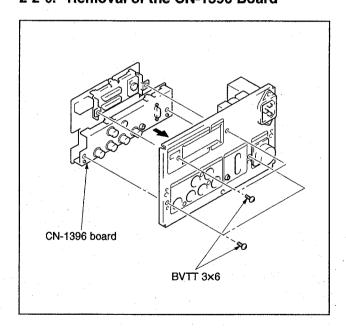
2-2-5. Removal of the Switching Regulator



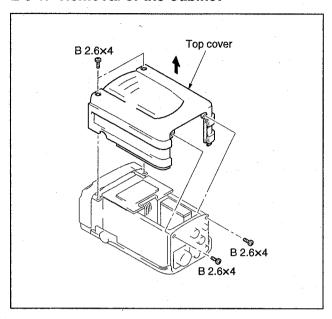
2-2-4. Removal of the MB-725 Board



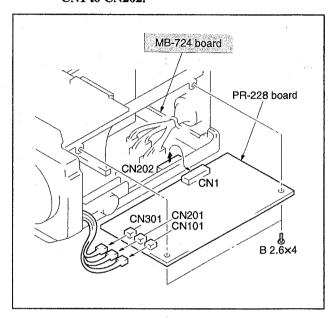
2-2-6. Removal of the CN-1396 Board



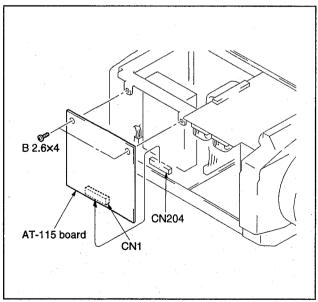
2-3. CAMERA BLOCK DISASSEMBLY 2-3-1. Removal of the Cabinet



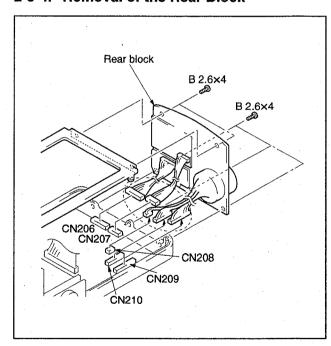
2-3-3. Removal of the PR-228 Board
NOTE: When assembling PR-228 board, connect the
CN1 to CN202.



2-3-2. Removal of the AT-115 Board
NOTE: When assembling AT-115 board, connect the
CN1 to CN204.



2-3-4. Removal of the Rear Block

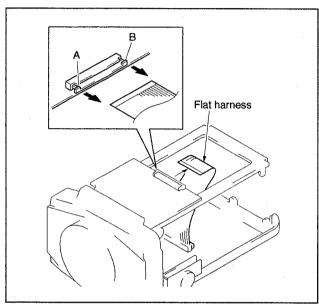


2-3-5. Removal of the Bottom Base Assembly

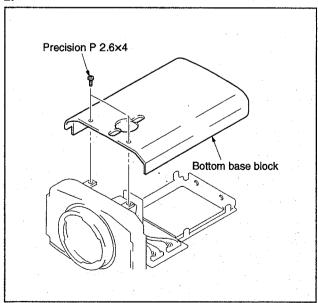
NOTE: When disconnecting the flat harness, pull simultaneously portions A and B in the direction indicated by the arrows and pull out the flat harness.

When connecting the flat harness, firstly, insert the harness into the connector and push simultaneously portions A and B in the opposite direction indicated by the arrows.

1.

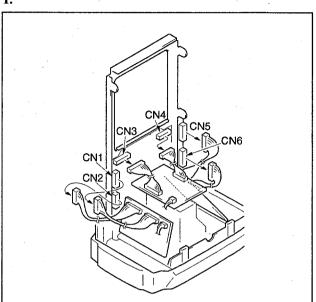


2.

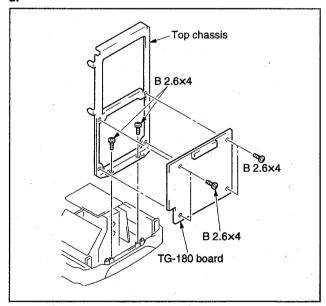


2-3-6. Removal of the TG-180 Board and Top Chassis

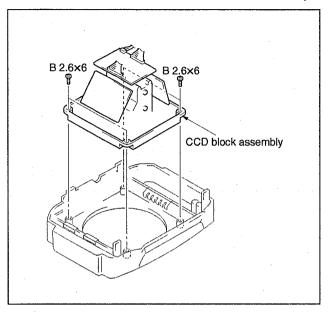
1.



2.



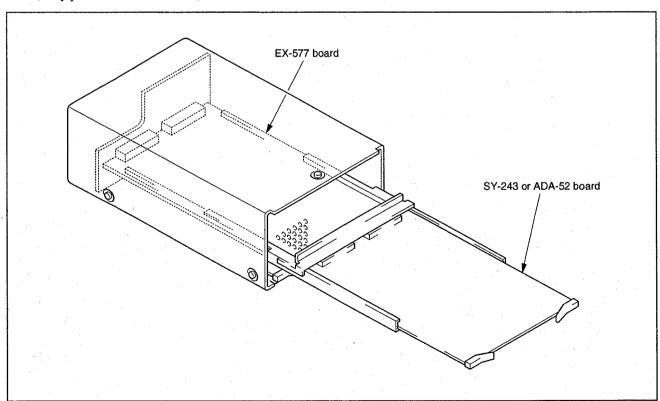
2-3-7. Removal of the CCD Block Assembly



2-4. HOW TO USE EXTENSION BOARD

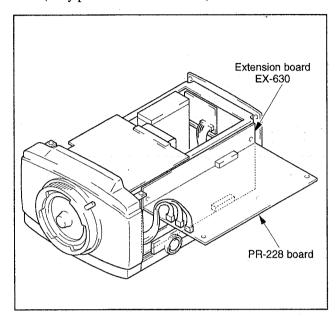
2-4-1. Processor block

• Extention board EX-577 (Sony part No.: J-6276-450-A)

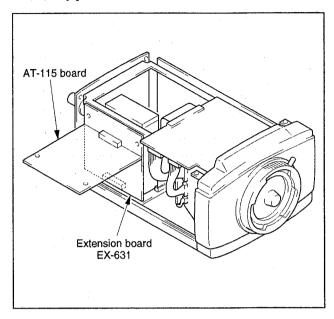


2-4-2. Camera block

• Extention board EX-630 (Sony part No.: J-6276-490-A)



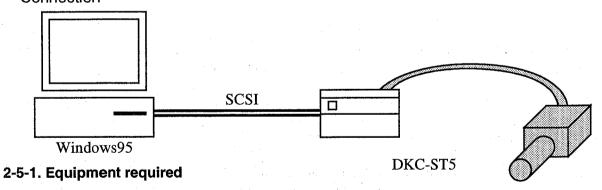
• Extention board EX-631 (Sony part No.: J-6276-500-A)



2-5. AFTER CCD BLOCK IS REPLACED

After replacing CCD block, if photograph would be performed and the picture would be taken in the computer, the noise may be generated. To prevent this, it is needed to memory the defect information of the CCD for the camera.

Connection



(1) Hardware

IBM-PC mutual changeable model

- It has an Intel 80486 or a Pentium processor or it is attached with more than 16M bite main memory.
- SCSI host adapter AHA-1522/2940 series——Adaptec (Connection between DKC-ST5 and the computer with SCSI cable)

(2) Software

- MS-Window 95
- Adaptec EZ-SCSI V4.5J or more (Install the ASPI manager.)
- CCD DATA(E) TOOL J-6276-700-A (SONY part number)

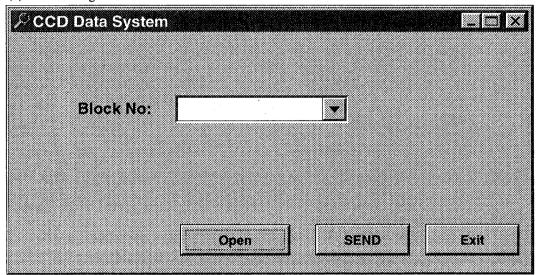
2-5-2. OPERATION DESCRIPTION

(1) Starting of application



If above icon is double-clicked, application is started.

(2) Main dialog box

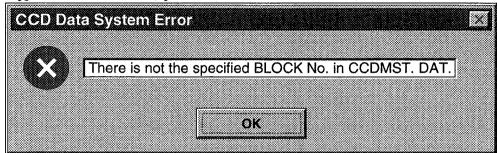


After application started, main dialog box is indicated.

- (3) Selection of file
 - 1 Click the open—button.
 - 2 Dialog box of file selection is indicated.
 - 3 If the file is selected, the block number is registered in reading combo box.
- (4) Input of block number
 - ① Input by combo box
 Push marked ▼ and select the block number by the mouse.
 - ② Input by key
 - Key input mode is made by tab key or mouse clicking of the combo box.
 - Key input is performed by keyboard and push the enter key.

3 Error

When unregistered block number is inputted by key, the following message is appeared and it can not be inputted.

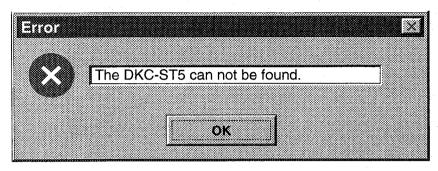


- (5) Transmission of defect data

 Click the send button and then the defect data is sent to the DKC-ST5.
- (6) Finish of application

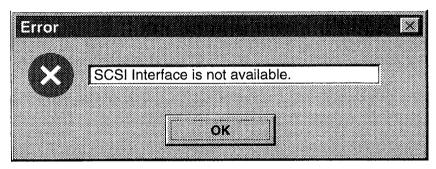
 Click the finish button and then the application is finished.

2-5-3. DKC-ST5 INTERFACE

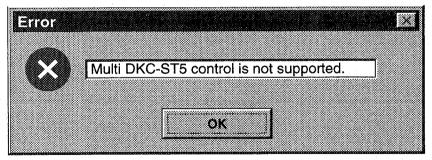


When above message is appeared, DKC-ST5 is not connected to SCSI. Cause:

- It is not the DKC-ST5.
- The cable is not connected.
- There is a mis-setting of SCSI ID.



When above message is appeared, ASPI driver of 32 bit is not there. Reinstall the SCSI manager.



When above message is appeared, some DKC-ST5 sets are connected to SCSI. Connect only one DKC-ST5 to SCSI.

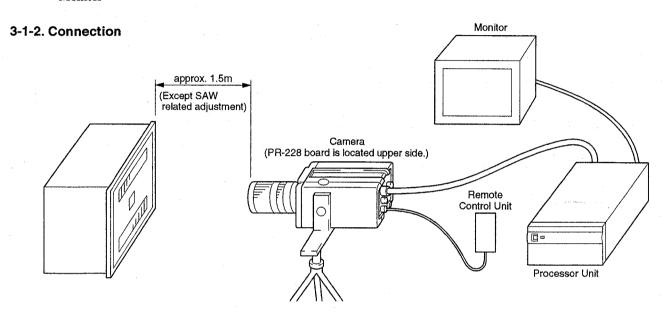
In case, these SCSI interface errors are appeared, SEND button can not be used.

SECTION 3 ALIGNMENT

3-1. PREPARATION

3-1-1. Equipment Required

- · Digital voltmeter
- Oscilloscope (100 MHz or more)
- Monitor



3-1-3. Adjustment mode

Turn on the power and press the ENTER key of the remote control more than 5 seconds. The characters on the monitor are changed to green color. This means that has been entered the adjuntment mode.

[MAIN]
►EVR ADJUST
IRIS ADJUST
SW SETTING
SHUTTER

3-1-4. Iris Setting

Coincide the cursor to IRIS ADJUT by \downarrow key and press the ENTER key. Indication on the monitor is as follows.

[IRIS ADJUST]
CLOSE:
F5.6 : 0
5600K : 0
×3 : 0
▶ □ RETURN

3-2. VSUB ADJUSTMENT (TG-180)

Conditions for adjustment		spec.	Adjustments
CCD imager voltage	TP3 0±0.1V	R-ch	ØRV4
Datenblad	TP5 -0.5±0.1V	G-ch	⊘ RV5
	TP7 0±0.1V	B-ch	ØRV6

3-3. RG DC ADJUSTMENT

Conditions for adjustment		Spec.	Adjustment
clamp pulse voltage	TP2		ØRV1
Midulliter	10±0.1V	R-ch	
Nov-to.1	TP4		⊘ RV2
	10±0.1V	G-ch	
GN)	TP6		⊘ RV3
	10±0.1V	B-ch	

3-4. TERMINOLOGY DEFINITION

SHADING	Select the CLOSE in the IRIS ADJUST or cover the lens with lens cap.
STANDARD	Select the F5.6:-3 in the IRIS ADJUST and shoot the gray scale.
THE THREE TIMES INTENSITY OF RADIATION	Select the x3:0 in the IRIS ADJUST and shoot the gray scale.
ENTIRELY WHITE	Select the F5.6:-3 in the IRIS ADJUST and contact the lens to the pattern box(white). Lens focus(∞), Zoom is end of tele.
OSCILLOSCOPE TRIGGER	When seeing by H synchronization,TP2 When seeing by V synchronization,TP1

3-5. EVR INITIALIZATION

Select the EVR ADJUST and select the INIT.

Change indication to YES by \bigcup and \bigcap keys, and press the ENTER key to initialize the EVR.

3-6. INPUT LEVEL ADJUSTMENT

Conditions for adjustment	spec.	Adjustments
Mode: standard White level (H synchronization)	TP201 115 ± 10 mVp-p	⊘ RV202
	G-ch	
	TP101	ØRV103
	120 ± 10 mVp-p R-ch	
	TP301	⊘ RV302
	60 ± 10 mVp-p	JHV302
	B-ch	
	Specification value	

3-7. DARK ADJUSTMENT

Conditions for adjustment	spec.	Adjustments
Mode: Shading White level (H synchronization)	TP503 Within ± 10 mV G-ch	⊘ EV19
	TP403 Within ± 10 mV(Without noise) R-ch	ØEV7
	TP603 Within ± 10 mV B-ch	⊘ EV31
	signal period OPB pedestal period	
	This step should be become to 0.	

3-8. HSAW ADJUSTMENT

Conditions for adjustment	spec.	Adjustments
Mode: entirely white Difference between white levels (H synchronization)	TP505 Within ± 20 mV(Without noise) G-ch	⊘ EV16
	TP405 Within ± 10 mV(Without noise) R-ch	⊘ EV4
	TP605 Within ± 10 mV(Without noise) B-ch	⊘ EV28
	Should be flat. (Right and left should be balanced.)	

3-9. VSAW ADJUSTMENT

Conditions for adjustment	spec.	Adjustments
Mode: entirely white	TP505	⊘ EV17
Difference between white levels	Within ± 20 mV(Without noise)	and the second second
should be flat	G-ch	
right and left should be balanced.		
(V synchronization)	TP405	ØEV5
	Within ± 10 mV(Without noise)	
	R-ch	
e t	TP605	⊘ EV29
•	Within ± 10 mV(Without noise)	
	B-ch	•
	Should be flat. (Right and left should be balanced.)	

3-10. GAIN ADJUSTMENT

Conditions for adjustment	spec.	Adjustments
Mode: Standard White level (H synchronization)	TP505 550 ± 20 mV G-ch	⊘ EV20
	TP405 550 ± 20 mV(3200K) R-ch	ØEV2
	TP605 550 ± 20 mV(3200K) B-ch	⊘ EV26
	Specification value	

3-11. BLACK 1 ADJUSTMENT

Conditions for adjustment	spec.	Adjustments
Mode: Shading	TP503 ± 10 mV(Without noise) G-ch	⊘ EV18
Step between pedestal and 0PB (H synchronization)	TP403 ± 10 mV(Without noise) R-ch	ØEV6
	TP603 ± 10 mV(Without noise) B-ch	⊘ EV30
	signal period OPB pedestal period This step should be become to 0.	

3-12. PRENEE ADJUSTMENT

Conditions for adjustment	spec.	Adjustments
Mode: The three times intensity of radiation. (H synchronization)	TP505 1130 ± 40 mV G-ch	⊘ EV15
	TP405 1130 ± 40 mV R-ch	⊘ EV3
	TP605 1130 ± 40 mV(Without noise) B-ch	⊘ EV27
	Specification value	

3-13. V SAW ADJUSTMENT

Conditions for adjustment	spec.	Adjustments
Mode: Entirely white (V synchronization)	Difference between TP405 and TP505 Within ± 10 mV(Without noise) R-ch	⊘ EV5
	Difference between TP605 and TP505 Within ± 10 mV(Without noise) B-ch	⊘ EV29
	Should be flat. (Right and left should be balanced.)	

3-14. γ INPUT LEVEL ADJUSTMENT

Conditions for adjustment	spec.	Adjustments
Mode: Standard White level (H synchronization)	TP501 470 ± 10 mVp-p G-ch	⊘ RV501
	TP401 470 ± 10 mVp-p R-ch	⊘ RV401
	TP601 470 ± 10 mVp-p B-ch	⊘ RV601
	Specification value	

3-15. PEDESTAL ADJUSTMENT

Conditions for adjustment	spec.	Adjustments	
Mode: Shading	TP503		⊘ RV506
Optical black DC voltage	2.6 ± 0.1 V		
	G-ch		
	TP403		⊘ RV406
	2.6 ± 0.1 V		
	R-ch		
	TP603		⊘ RV606
	2.6 ± 0.1 V		
	B-ch		
•		-	
	Spe	cification value	
			,
	GND		

3-16. γ ADJUSTMENT

Conditions for adjustment	spec.	Adjustments ©RV504	
Mode: Standard	TP503 cross level Turn the RV505 fully clockwise. 800 ± 40 mV G-ch		
	Specification value		
	The difference between TP403 and TP503. Turn the RV405 fully clockwise. The waveform should be flat. Within ± 20 mV(without noise) R-ch	⊘ RV404	
	The difference between TP603 and TP503. Turn the RV605 fully clockwise. The waveform should be flat. Within ± 20 mV(without noise) B-ch	⊘ RV604	

3-17. OUTPUT LEVEL ADJUSTMENT

Conditions for adjustment	spec.	Adjustments	
Mode: Standard	TP504 White level 2000 ± 40 mVp-p G-ch	⊘ RV508	
	Specification value		
	The difference between TP404 and TP504. Within ± 10 mV R-ch	⊘ RV408	
	The difference between TP604 and TP504. Within ± 10 mV B-ch	⊘ RV608	

3-18. KNEE ADJUSTMENT

Conditions for adjustment	spec.	Adjustments
Mode: The three times intensity of radiation	TP503 White level 2140 ± 100 mV G-ch	⊘ RV507
	Specification value	
	The difference between TP403 and TP503. The waveform should be flat. Within ± 20 mV(without noise) R-ch	⊘ RV407
	The difference between TP403 and TP503. The waveform should be flat. Within ± 20 mV(without noise) B-ch	⊘ RV607
	- - 1000000000000000000000000000000000	

3-19. V SAW ADJUSTMENT (ALL PIXELS)

Conditions for adjustment	spec.	Adjustments	
Mode: Entirely white (V synchronization)	TP505 The difference between white levels Within ± 20 mV(without noise)	⊘ V SAW(G-ch)	
	Should be flat. (Right and left should be balanced.)		
	The difference between TP405 and TP505. Within ± 10 mV(without noise)	⊘ V SAW(R-ch)	
	The difference between TP605 and TP505. Within ± 10 mV(without noise)	⊘V SAW(B-ch)	
	Should be flat. (Right and left should be balanced.)		

3-20. 5600K COLOR TEMPERATURE CONVERSION SETTING

Cover the lens with C14 filter. Select "5600K:0" at IRIS ADJUST. Shoot the gray scale chart.

3-21. GAIN ADJUSTMENT (5600K)

Conditions for adjustment	spec.	Adjustments		
(H synchronization)	The difference between TP403 and TP503. Within ± 10 mV R-ch	5600K(R-ch)		
	The difference between TP603 and TP503. Within ± 10 mV B-ch	5600K(B-ch)		

3-22. SETTING VALUE WRITTING

Conditions for adjustment	spec.	Adjustments
The setting value is written in EEPROM.	Select ALL SAVE at EVR ADJUST and press the ENTER key.	ALL SAVE

SECTION 4 CIRCUIT DESCRIPTION

4-1. PROCESSOR SECTION

[1] OUTLINE

The SY-243 board mainly consists of a system block, image processing block, and FF clock circuit block.

The system block is constituted by 1M-bit EPROM x 2 (IC39 for lower EPROM and IC40 for upper EPROM) for a program; 4M-bit DRAM (IC27) for system working; nonvolatile 64K-bit EEPROM x 2 (IC22 and IC23); 2M-bit flash memory (IC24) required to load an external program in future; a system controller gate array (IC31) with a character generation function that displays characters on the monitor by using an address decoder, PI/O decoder, DMA timing controller, programmable wait generator, 10-bit programmable counter, C-sync detector, and 1M-bit SRAM (IC32); a peripheral circuit of CPU (IC19) such as IC29 for a real-time clock, IC25 for an RS-232C controller, and an RS-232C driver (IC21); and CPU (IC19) that controls the peripheral circuit.

The image processing block is constituted by a gate array (IC500), incorporating a lookup table (LUT), that can write the image data in VRAMs (IC509 to IC514) for monitor output and that can write and read the input image in and from frame memory (16M-bit DRAM x 3 (IC506, IC507, and IC508))(DMA transfer); gate arrays (IC501 and IC502) that interpolate the slantly shifted pixel image and have a contrast and sharpness function; a gate array (IC503) that can read the image data from VRAMs (IC509 to IC514) for monitor output, that can write and read the data from 4M-bit DRAMs (IC515, IC516, and IC517), and that incorporates a masking function and monitoring lookup table (LUT); SPC (IC504) for SCSI; and an active terminator (IC505). The FF clock circuit block is constituted by IC100, digital delay lines (IC101, IC102, and IC103), and a sync separator (IC104), so as to generate the WCK clock (20 MHz) locked to the WHD clock from a camera head.

[2] SYSTEM BLOCK

(1) Address decoder, PI/O decoder, and PI/O

The memory map and I/O map consist of system controller gate array IC31, and IC1, IC2, IC3, IC4, and IC5 (refer to pages 4-3 to 4-5).

The $\overline{OUT1}$ to $\overline{OUT5}$ output signals of IC31 are input to the CK and input pins of IC28 and IC7, and the CK pins of IC37, IC30, IC529, and IC38, respectively. The $\overline{IN1}$ to $\overline{IN4}$ output signals of IC31 are input to the \overline{IG} and $\overline{2G}$ pins of IC26, IC36, IC35, and IC34, respectively to obtain the PI/O configuration shown on page 4-6 and control the system.

(2) Character generator

The character generator consists of system controller gate array IC31, 1M-bit SRAM IC32, and IC33.

The characters and lines such as menu, headline, chin line, and print frames displayed on the monitor are written in IC32 from CPU (IC19)(1-bit 768 pixels x 682 lines x 2 are provided as a character image) and output from the CR KEY, CR R, CR G, and CR B pins. The CR KEY signal corresponds to the CR R + CR G + CR B signals.

These four signals are superimposed on a picture signal in the former stage of a D/A converter on the ADA-52 board and output from the monitor.

(3) Interrupt controller

The interrupt controller consists of IC26, IC28, IC5, IC6, IC7, IC10, and IC11.

The INTP01 input signal of IC19 is set low and recognized as an interrupt when the HK INT signal at the falling edge in IC501 or the FM INT signal of IC500 is set low or when the LM INT signal of IC503 is set low. The type of the above three interrupts that occurred can be recognized by reading IN1 data. (Refer to page 4-6)

(4) Real-time clock block

The real-time clock block consists of IC29, BH1, D2, Q1, and Q2.

The power of IC29 is backed up by BH1 during the power-off sequence. In addition to a timer function, IC29 also has internal SRAM. Therefore, IC29 memorizes the zoom, focus, and iris values of a lens.

[3] FF CLOCK CIRCUIT BLOCK

This block consists of two 20-MHz crystal oscillators; IC100; digital delay lines (IC101 and IC102), and IC105, IC108, and IC107; IC103 and IC106; a sync separator (IC104); and IC105, IC111, IC114, IC110, IC108, IC109, and IC107.

IC100 has a function that gate-delays a clock of 20 MHz, generates a multi-phase clock, compares the clock with the WHD signal from the camera head in phase, and selects a clock with the nearest phase. The digital delay lines (IC101 and IC102), and IC105, IC108, and IC107 are used to eliminate the noise in an output clock of 20 MHz. IC103 and IC106 are used to adjust the phase level for an input picture signal. The sync separator (IC104) detects the WHD signal from the camera head. The WEXT output signal of IC31 is set high if a WHD signal exists (that is, the connection with the camera head is performed using a cable) when the output signal of IC104 is input to the WCSNC pin of IC31. The WEXT output signal is set low if no WHD signal exists (that is, the connection with the camera head is not performed using a cable). However, when connection is done using the WEXT output a signal, WEN and XHD signals are used as WEN and WHD signals from the camera head, and a clock of 20 MHz is synchronized at the falling edge of a WHD signal. When no connection is done using the WEXT output signal, a WEN signal is generated by the RFLD signal from the ADA-52 board and an XHD signal is used as an RHD signal. A clock of 20 MHz is selected by IC105, IC111, IC114, IC110, IC108, IC109, and IC107 so that it is synchronized at the falling edge of an RHD signal.

In addition to WEN and WHD (reference signal of IC500) signals, a WVD signal (write reference signal of IC19) is output as a sync signal from the camera head. Moreover, a flash OUT signal that controls the solid state relay (IC1) on the MB-725 board, and TXD and RXD signals that are used for communication with the camera head CPU are output. The TXD and RXD signals are a differential signal. They conform to the RS-422 standard. These signals exchange each data via differential driver IC17.

Address	Device	Block No	CS	Wait	Access byte
FF800000h to FFFFFFFh (Actually, FFFC0000h to FFFFFFFh)	EPROM (2M) IC39, IC40	3	CS3	·	Upper (D15 to 8) Lower (D7 to 0)
FF0000000 to FF7FFFFH (Actually, FF7C00000 to FF7FFFFH)	Flash Mem (2M) IC24	3	CS3		Upper Lower
3F000000h to 3FFFFFFH (Actually, 3FF80000h to 3FFFFFFFh)	DRAM (4M) IC27	0	DRAM MODE	3	Upper Lower
7F000000н to 7FFFFFFн (Actually, 7FFFC000н to 7FFFFFFFн)	EEPROM (128k) IC22, IC23	1	CS1		Upper Lower

Note: Addresses A24 through A29 are not used.

Table 4-1 Memory Map

Address	Device	Block No	CS	Wait	Access byte	
С0000000н to С00000FFн	Inside of internal peripheral I/O register IC19	3	·	Makes one or two waits forcibly	Upper Lower	
BF800000н to BF807FFFн	LUT1 (for input) IC500 (incorporating FMC G/A)	2	FWCS	External WAT	Lower	
BF800000H to BF8007FFH BF800800H to BF800FFFH BF801000H to BF8017FFH	R-ch LUT1 G-ch LUT1 B-ch LUT1					SYC G/A
BF808000н to BF80FFFFн	LUT2 (for monitor) IC503 (incorporating LM G/A)	. 2	LWCS	External WAT	Lower	EECS
BF808000H to BF8081FFH BF808200H to BF8083FFH BF808400H to BF8085FFH	R-ch LUT2 G-ch LUT2 B-ch LUT2			· · · · · · ·		
BF880000H to BF88FFFFH	PI/O and SYC G/A register IC31	2			Upper	
BF980000H to BF98007FH	FMC G/A register IC500	2	FMCS	External WAT	Lower	SYC G/A FMCS
BF980080н to BF9800FFн	LM G/A register IC503	2	LMCS	External WAT	Lower	SYC G/A ESCS
BF980100н to BF98013Fн	HK1 G/A register IC501	2	HKICS		Lower	
BF980140 _H to BF98014F _H	HK2 G/A register IC502	2	HK2CS		Lower	SYC G/A MSCS
BF980150н to BF98015Fн	serial controller IC25	2	SCCS		Lower	Massa
BF980160n to BF98017Fn	SPC IC504	2	SPCCS		Lower	
BFA00000h to BFA07FFFh	Flame Memory IC500 window	2	FMWCS	External WAT	Lower	SYC G/A FWCS
BFA00000h to BFA00FFFh	R-ch FM window		-,			
BFA01000h to BFA01FFFh	G-ch					
BFA02000H to BFA02FFFH	FM window B-ch FM window					

(To next page)

Address	Device	Block No	CS	Wait	Access byte	
BFA08000H to BFA0FFFFH	Background frame memory window IC503	2	LMWCS	External WAT	Lower	SYC G/A FWCS
BFA08000H to BFA087FFH	R-ch B.G.M. window					
BFA08800h to BFA08FFFh	G-ch B.G.M. window					
BFA09000h to BFA097FFh	B-ch B.G.M. window				•	
BFB00000h to BFBFFFFH (Actually, BFB00000h to BFB3FFFFH)	Character RAM IC32	2	CRAMCS		Upper	SYC G/A CRAMCS

Table 4-2 I/O Map

Notes: 1. Addresses A24 through A29 are not used.
2. For only an internal periphral I/O register, addresses A8 through A29 are not used.

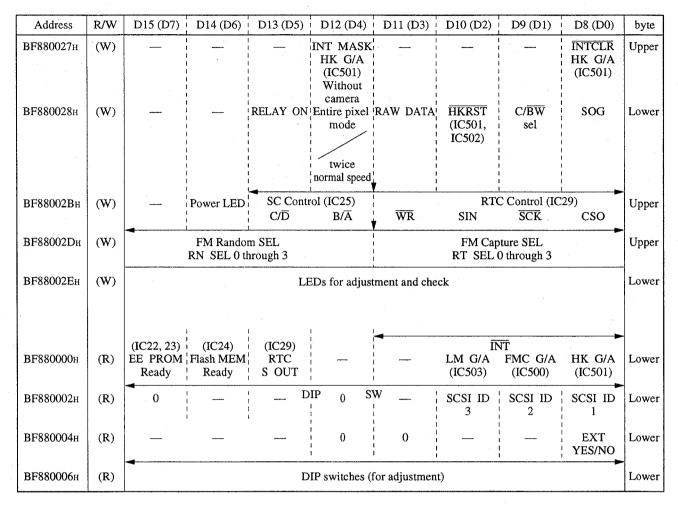


Table 4-3 P I/O

Notes:

- 1. The C/BW SEL signal selects whether to output RET video, video OUT, and S OUT signals as a color or black-and-white signal. The C/BW SEL signal is set high during normal operation.
- 2. The SOG signal selects whether to superimpose a C-SYNC signal on the G OUT signal of R, G, and B OUT signals.
- 3. The RT SEL 0 through 3 signals change when a WEN signal is synchronized.
- 4. The RN SEL 0 through 3 signals change when an HD signal is synchronized.
- 5. Addresses BF880027H through BF88002FH are set to 0 after POC.
- 6. The raw data signal is set to 1 when raw data is transferred by SCSI. For other operations, this signal is set to 0.
- 7. EXT YES/NO indicates whether the camera is connected. Camera connected = High (1)
- 8. The RELAY ON signal is set to 1 when relay RY1 on the MB-725 board is turned on. It is set to 0 when relay RY1 is turned off.
- 9. RT SEL 3 through 0:0, 1, 2 through 9, A through F
 RN SEL 3 through 0:0, 1, 2 through 9, A through F
 Normal Service Option NO sel
 state FM FM

[4] IMAGE PROCESSING BLOCK

(1) Live image display

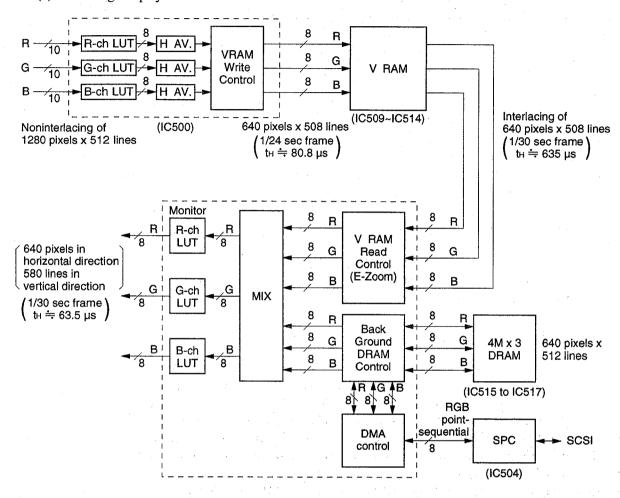


Fig. 4-1 Flow of Image Data in Live State

In the DKC-ST5, the entire pixel read mode (effective pixel of 1280 pixels x 1024 lines) and the twice normal speed mode (effective pixel of 1280 pixels x 512 lines) in which the sum of two lines is output are provided as the CCD read mode. In the live state, data is read in the twice normal speed mode.

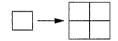
Two-line average analog R, G, and B signals are output from the camera head, passed through an analog circuit such as a differential amplifier, cable compensation circuit, sample-and-hold circuit on the processor side, and converted from analog to digital using an A/D converter (the processing described above is performed on the ADA-52 board). The resultant signals are sent to the SY-243 board, latched using a clock of 20 MHz by IC518, IC519, IC520, and IC521, and input to IC500. In IC500, these signals are then passed through a 10-bit lookup table and converted into eight-bit R, G, and B signals, respectively. The converted signals are equalized in the horizontal direction by two pixels and alternately written in VRAMs (IC509 through IC514) for every line in the order of odd, even, and odd.

The picture size is 640 pixels x 508 lines, 1/24 sec frame (WEN period), and $t_H = 80.8$ usec (XHD (WHD) period). The throughput rate is 20 MHz.

For configuration of VRAM, IC509 is used for an R-channel odd field, IC510 for an R-channel even field, IC511 for a G-channel odd field, IC512 for a G-channel even field, IC513 for a B-channel odd field, and IC514 for a B-channel even field.

The image from VRAM is read by IC503. The normal live image is input from MR0 through 7, MG0 through 7, and MB0 through 7 pins to IC503 and output from MR00 through 7, MG00 through 7, and MB00 through 7 pins through R-, G-, and B-channel lookup tables for a monitor. The image data is then passed through a buffer consisting of IC537, IC538, and IC539 and character-superimposed on the ADA-52 board. After that, the image data is displayed on the monitor through a D/A converter, RGB encoder, and 75Ω driver. The image from VRAM is read in picture size of 640 pixels x 508 lines, 1/30 sec frame (double period of \overline{RVD}), and $t_H = 63.5$ usec (\overline{RHD} period), and at a throughput rate of 12.283 MHz. For RFLD = 0, the image is read from the odd memory of VRAM by interlacing. For RFLD = 1, it is read from the even memory of VRAM by interlacing. The image from VRAM is written and read independently. In other words, VRAMs (IC509 through IC514) are used as buffer memory for rate conversion.

The center of the monitor screen becomes double when the E-zoom key on the remote control unit is pressed. In this case, one-pixel data is simply expanded in the horizontal and vertical directions by two pixels. The expansion is performed in IC503 during read operation.



A background image is described below.

The background image memory consists of three 4M-bit DRAMs (IC515 for R channel, IC516 for G channel, and IC517 for B channel). The image memory is input through SPC (SCSI protocol controller) IC504 in a processor to the ID0 to ID7 pins of IC503 when an image of 640 pixels x 512 lines x R.G.B is point-sequentially transferred from a computer by SCSI. IC504 and IC505 (point-sequential DMA) are subject to handshaking by DREQ, DACK, and DBWR control signals. The background image data sent to IC503 is written in IC515 through IC517 described above.

The written image data is synchronized with the <u>read</u> timing of VRAM when the fame ON/OFF key on the remote control unit is pressed (\overline{RVD} synchronization in the vertical direction, \overline{RHD} synchronization in the horizontal direction, and throughput rate of 12.283 MHz). In this case, even lines (0, 2, 4, etc.) for RFLD = 0 or odd lines (1, 3, 5, etc.) for RFLD = 1 are read from DRAMs (IC515 through IC517) to the DR0 through 7, DG0 through 7, and DB0 through 7 pins in the high-speed page mode (interlacing).

In IC503, the average data (mix data) for VRAM image data is passed through a lookup table for monitor and output from the MRO0 through 7, MGO0 through 7, and MBO0 through 7 pins.

(2) Memory image display and capture

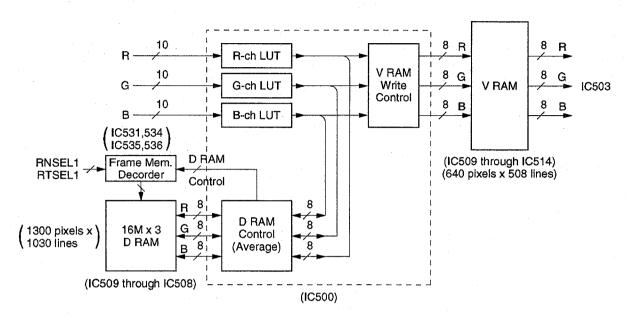


Fig. 4-2 Flow of Image Data during Memory Image Display and Capture

The CCD read mode of the camera head is put into the entire pixel read state to transfer only one frame when the release key on the remote control unit is pressed. When RT SEL0 to 3 signals are set to 1 (refer to page 4-6) at that time, the entire pixel data is sent from the ADR0 through 9, ADG0 through 9, and ADB0 through 9 input pins to IC500 and passed through R-, G-, and B-channel lookup tables. In synchronization with WEN, XHD (WHD), and ADCK (20 MHz clock) signals, the pixel data is then written in IC506 (R channel), IC507 (G channel), and IC508 (B channel) in the high-speed page mode. The RNSEL0 through 3 and RTSEL0 through 3 data that latch the DRAM control signals, output from IC500, (XRTC (set low during image memory capture or clear operation), XRFC (set low during image memory refresh), RAS, CAS, OE, WER, WEG, WEB, and DIR (determines the data bus direction of image memory)) by IC529 and IC530 are decoded (by IC531 and IC532). Moreover, RNSEL1 and RTSEL1 signals are generated, and the DRAM control signal described above is decoded by the RNSEL1 and RTSEL1 signals. As a result, image memory consisting of IC506, IC507, and IC508 is controlled (by a frame memory decoder consisting of IC531, IC534, IC535, and IC536 shown in Fig. 4-2). During write operation, the RT-SEL1 signal is set to 0, and the RNSEL1 signal to 1 (the RNSEL1 and RTSEL1 signals are set to 1, respectively during normal operation). The size of the written image is 1300 pixels x 1030 lines.

The memory image display is described next.

For review during capture operation, the entire pixel data from the camera head is decimated as shown in Fig. 4-3 and written (alternately for every odd and even line of VRAM) in VRAM simultaneously with when it is written in image memory consisting of IC506, IC507, and IC508.

When the live/memory key on the remote control unit is pressed and memory 1 is selected, RNSEL0 through 3 signals are set to 1 (refer to page 4-5) and the RNSEL1 signal shown in Fig. 4-2 is set low (the RTSEL1 signal is set high). At that time, the image data (with image size of 1280 pixels x 1016 lines) of IC506 through IC508 are read and input to IC500. In IC500, 2 x 2 pixel data items are equalized as shown in Fig. 4-4 and alternately written in VRAM for every odd and even memory line.

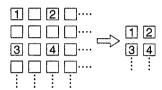


Fig. 4-3 Decimated Data during Review

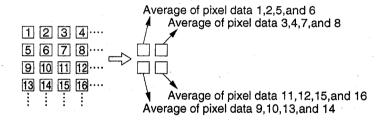


Fig. 4-4 Average Date during Memory Image Display

(3) DMA data transfer

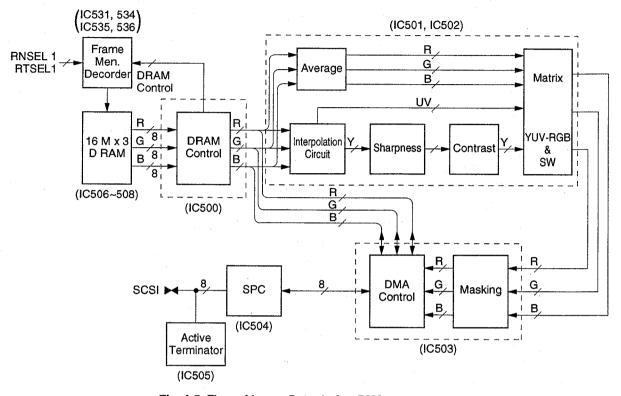


Fig. 4-5 Flow of Image Data during DMA

1) Full image transfer (2560 pixels x 2048 lines x R.G.B)

Nine image memories (including the eight image memories on the ADA-52 board) are provided. RTSEL0 through 3 signals are set to 1 (refer to page 4-6) when the image memory on the SY-243 board is selected. DRAM control signals (XRFC, XRTC, RAS, CAS, OE, WER, WEG, WEB, and DIR) are decoded using a frame memory decoder consisting of IC531, IC534, IC535, and IC536 so as to select and control IC506, IC507, and IC508. The R, G, and B data (1300 pixels x 1030 lines) of image memory 1 are passed through IC500 and transferred in the handshaking (asynchronous) state by IC501, REQ and ACK signals. The eight-bit R, G, and B image data items input to IC501 are first interpolated in IC501. In the camera head, the R, G, and B CCDs are fixed with the R and B channels obliquely shifted for a G channel by a half pixel.

By interpolating the eight-bit R, G, and B image data, however, the image size is made double in the vertical and horizontal directions from 1300 pixels x 1030 lines to 2600 pixels x 2060 lines. As a result, the resolution is improved (1400 TV lines).

For the interpolation block processing in Fig. 4-5, the R, G, and B signals are converted into luminance signal Y and color-difference signals (R-Y) and (B-Y) so as to interpolate the Y signal. The interpolated Y signal is processed in sharpness and contrast blocks, converted into R, G, and B signals, and output from IC501.

To avoid the influence of FIR filter in IC501, the image is cut in the upper and lower, and right and left portions. The size of the obtained image is 2580×2056 . The sharpness and contrast blocks have parameters that enable control operation. (In the menu, the sharpness can be set in the range of off, low, mid, and high, and the contrast can be selected in the range of -5 to +5.)

The R, G, and B image data output from IC501 are transferred for IC503 in the handshaking and asynchronous states by \overline{REQ} and \overline{ACK} signals and input to IC503. In IC503, masking (in which hue, saturation, and brightness can be adjusted) is performed. The masking block has parameters that enable control operation (in the menu, the masking can be selected in the range of off, low, mid, and high). The R, G, and B output signals are converted into R, G, and B point-sequential data by a DMA control block and transferred in the handshaking and asynchronous states between the DAM control block and SPC (IC504) by REQ and \overline{ACK} signals. Moreover, R, G, and B point-sequential image data items (2560 pixels x 2048 lines) are transferred from IC504 to the computer between a processor and computer via an SCSI bus.

In IC503, 2560 pixels x 2048 lines are clipped from the image size of 2580 x 2056 for data transfer.

2) Index image transfer (320 pixels x 256 lines x R.G.B)

The index image transfer is basically the same as a full image transfer except that, in IC501, an index image is output through an average circuit. For 320 pixels x 256 lines, the average data of 16 pixels in all (4 pixels each in the horizontal and vertical directions) is output by one pixel. The image size is 1280 pixels x 1024 lines in the output block of IC500, 320 pixels x 256 lines in the output block of IC501, and 320 pixels x 256 lines in the output block of IC501 and IC503 during full image transfer is not carried out during index image transfer. No sharpness and contrast processing are also performed.

3) Raw image transfer (1300 pixels x 1030 lines x R.G.B)

During raw image transfer, the raw image captured by image memory is directly output to SCSI without sharpness, contrast, and masking processing.

The raw image skips IC500 to IC501 (IC502) as a pass and is directly transferred to IC503. In the same way as described previously, it is transferred in the handshaking and asynchronous states by REQ and ACK signals. Both read and write operations are performed only during raw image transfer, that is, the raw image is transferred in two ways.

For the asynchronous handshaking transfer using REQ and ACK signals, the REQ signal is output from the data transmission side, and the ACK signal is output from the data reception side. However, between IC504 and IC503, the REQ signal is output from IC504, and the ACK signal is output from IC503.

[5] CIRCUIT DESCRIPTION OF ADA-52

(1) Outline

The ADA-52 board mainly consists of an A/D block, D/A block, memory block, and PLL block

The A/D block is constituted by a floating video amplifier circuit, cable compensation amplifier circuit, bias amplifier (CXA1399), DC level shift circuit, and A/D converter (SPT7855).

The D/A block is constituted by a D/A converter, low-pass filter, RGB encoder (CXA1645), video amplifier, and 75Ω driver.

The memory block is constituted by 16M-bit DRAM for eight image memories and a frame memory decoder.

The PLL block is constituted by a sync signal generator (CXD1159) that generates a timing pulse and clock (12.288461 MHz) for monitor display, phase comparator (TC8051), TL082 for a low-pass filter amplifier, and voltage-controlled crystal oscillator (VCO)(24.576 MHz).

4-13

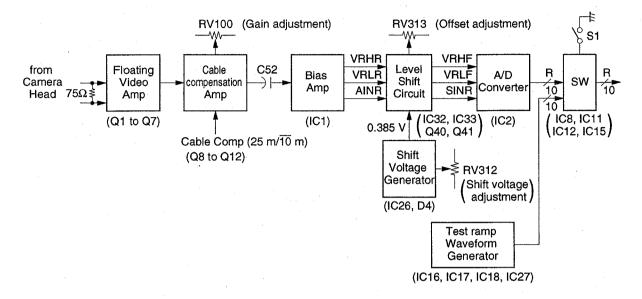


Fig. 4-6 Block Diagram of A/D Block

The A/D block is described based on an R-channel circuit below. (G- and B-channel circuits are the same as for the R-channel circuit.)

The video amplifier in the initial stage has a floating structure so as to cancel the potential difference of ground (GND) from a signal line and cancel the in-phase noise between the signal line and GND when a potential difference occurs between the camera head of DKC-ST5 and the processor.

The cable compensation amplifier in the second stage contains a low-pass filter in the feedback block. The attenuation occurring when the camera cable between the camera head and processor is 10 m or 25 m is compensated so that it is flat in the input block of a bias amplifier (IC1) by switching the effect of the low-pass filter using FET (Q12).

The level of a signal input to the A/D bias amplifier (IC1) is also gain-adjusted by RV100. Bias amplifier IC1 samples and holds a signal, amplifies a signal to about three times as high, and generates a reference voltage for an A/D converter.

To match the DC characteristics of the reference voltage between the bias amplifier (IC1) output and A/D converter (IC1) input, the level shift circuit and shift voltage generator generate a voltage of 0.385 V (adjusted by RV312), decrease the DC voltage level by 0.385 V, and input the voltage to the A/D converter.

Moreover, offset adjustment is performed for the DC level fine-adjustment of a signal by RV313.

A ramp waveform generator is mounted for adjustment on the ADA-52 board. The image signal from the A/D converter and the test ramp waveform can be switched using switch S1 on the front end of the board.

A 10-bit sampling signal of 20 MHz is output from the A/D converter.

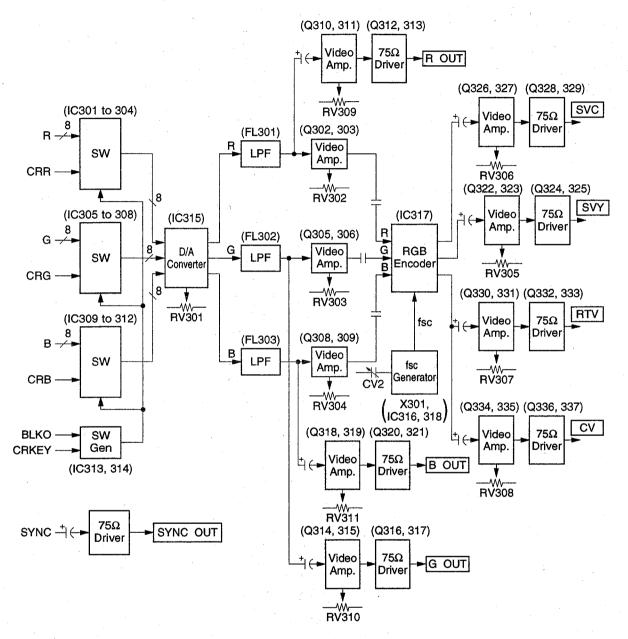


Fig. 4-7 Block Diagram of D/A Block

The D/A block is described next.

The eight-bit R, G, and B signals on the left of Fig. 4-7 are the display image data (live and memory images) from the SY-243 board. CRR, CRG, and CRB are one-bit data for menu display (e.g., character display). BLKO is a blanking signal indicating whether it is a video period. CRKEY is a character signal indicating whether character data exists. In the switch generation block, the switch block is controlled, and the CRR, CRG, and CRB signals are selected when the BLKO and CRKEY are used. When BLKO and CRKEY signals are used, R, G, and B signals are selected and input to the D/A converter. RV301 is used to adjust the maximum amplitude value of a D/A output signal. The resultant signal is converted from digital to analog using the D/A converter. The converted analog R, G, and B signals are input to low-pass filters FL301, FL302, and FL303 to eliminate the reflected components. The cut-off frequency is 6 MHz.

The output signal of the low-pass filters is cut in a DC component and output through a video amplifier and 75Ω driver to the RGB monitor. For a SYNC OUT signal, the composite sync signal generated in the PLL block is cut in a DC component, and the resultant signal is output through a 75Ω driver. RV309, RV310, and RV311 are used to adjust the gains of R, G, and B output signals.

Moreover, the output signal of the low-pass filters is passed through a video amplifier, cut in a DC component, and input to RGB encoder IC317. RV302, RV303, and RV304 are used to adjust the gains of the R, G and B signals that are input to the RGB encoder.

RGB encoder IC317 generates and inputs a subcarrier of 3.579545 MHz using an fsc generator. The fsc frequency is adjusted using trimmer capacitor CV2. An S-video Y signal, C signal, and composite video signal are then output as an output signal of the RGB encoder. Each signal is output as a monitor signal of the S-video Y signal, C signal, and composite video signal and a return video signal to the camera head through a video amplifier and 75Ω driver. RV305, RV306, RV307, and RV308 are used to adjust the gains of S-video Y, S-video C, return video, and composite video signals.

(4) Memory block

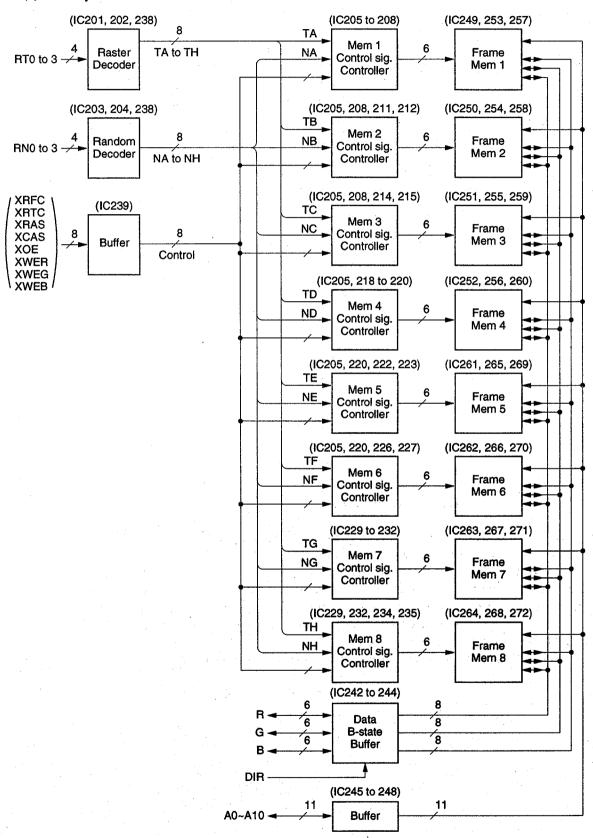


Fig. 4-8 Block Diagram of Memory Block

The memory block is described based on Fig. 4-8.

The memory block uses three 16M-bit DRAMs (R, G, and B) and constitutes eight frame memories.

The RT0 through 3 signals on the left of Fig. 4-8 are used to select the type of memory in which the image signal from the camera head is memorized. RN0 through 3 signals are used to select the type of memory to be displayed, the contents of memory to be transferred to a computer, and the type of memory in which image data is written from a computer.

The actual memory selection is shown in the table below.

RN3 RT3	RN2 RT2	RN1 RT1	RN0 RT0	Descripsion
0	0	0	0	stand-by
0	0	0	1	SY-243 Memory
0	0	1	0	Frame Mem. 1
0	0	1	1	Frame Mem. 2
0	1	0	0	Frame Mem. 3
0	1	0	1	Frame Mem. 4
0	1	1	0	Frame Mem. 5
0	1	1	1	Frame Mem. 6
1 .	0	0	0 -	Frame Mem. 7
1	0	0	1	Frame Mem. 8
1	0	1	0	Non
1	. 1	≀ 1	1	NOP

Table 4-4 Memory Selection

RT0 through 3 and RN0 through 3 signals are decoded by a raster decoder and random decoder and input to memory 1 through 8 control signal controllers, respectively. XRFC, XRTC, XRAS, XCAS, XOE, XWER, XWEG, and XWEB signals are passed through a buffer as memory control signal and input to the memory 1 through 8 control signal controllers described above. In the eight controller blocks, frame memory 1 through 8 control signals (XRAS, XCAS, XOE, XWER, XWEG, and XWEB) are generated by the output signals of the above two decoders, and XRFC and XTRC signals and input to each memory.

The data bus and address bus are used in common for frame memories 1 through 8.

(5) PLL block

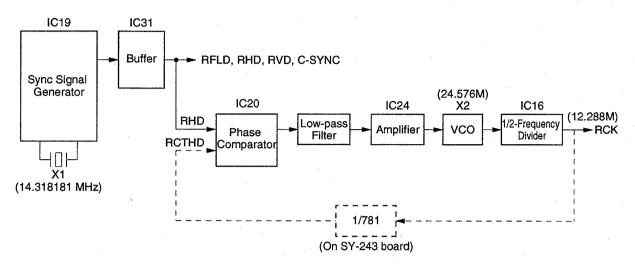


Fig. 4-9 Block Diagram of PLL Block

In the PLL block, the aspect ratio on the monitor is 4 to 3 because the CCD has a square grid. Therefore, the image size is 486 lines in the vertical direction and 648 pixels in the horizontal direction. The PLL block generates an RCK clock (12.288 MHz) that samples the video period of an NTSC signal by 648.

The RFLD, RHD, RVD, and C-sync signals generated using sync signal generator IC19 are passed through buffer IC31 to produce a sync signal for display.

The RCT HD signal generated when an RCK clock is frequency-divided by 781 and the output RHD signal of sync signal generator IC19 is phase-compared using phase comparator IC20 to generate an RCK clock (12.288 MHz). The obtained error signal is passed through a low-pass filter consisting of C and R and input from amplifier IC24 to voltage-controlled oscillator (VCO) X2 (24.576 MHz). The output clock of the VCO is frequency-divided into 1/2 by IC16 to produce an RCK clock of 12.288 MHz. The frequency division of 1/781 is performed on the SY-243 board.

4-2. Camera Section

[1] Description of the imager

The DKC-ST5 uses the 2/3 inch CCD imager ICX085-FL2 that has 1,400,000 pixels.

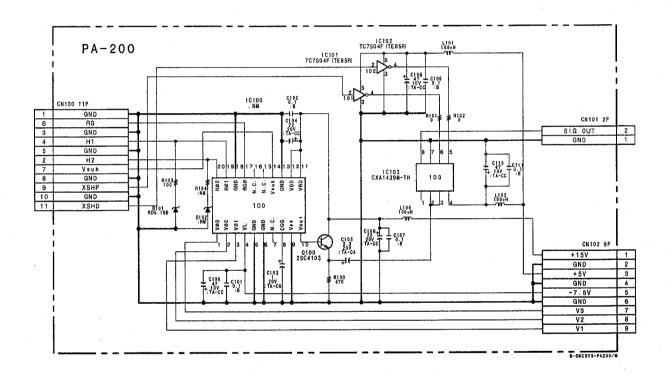
- Picture element 1280×1024
- When taking the still image, entire pixels are read out independently. (Frame frequency 12Hz, This following indication is entire pixel reading out.)
- The line numbers are decreased to 1/2 by 2 lines addition reading out in live picture mode. The reading out time is decreased to 1/2 time.(frame frequency is 24Hz, This following indication is twice normal speed reading out.)
- Electronic shutter
- Square grid unit cell

In live image, the video signal reading from CCD imager with frame frequency 24Hz is sent to the processor, and in the processor, the signal is converted analog to digital, and is sent to the memory once. The signal reading from the memory with the frame frequency 30Hz is produced the video signal with NTSC timing equivalent. It is sent to the MONITOR terminal.

4-20

[2] PA-200,PA-201 and PA-202 Board

PA board is for attaching to the CCD imager. Power supply to CCD and supply of driving pulse are performed. CDS(CORRELATED DOUBLE SAMPLING) is composed. PA-200, PA-201 and PA-202 boards are same circuit composition. Therefore, PA-200 is only explained. The driving pulse (H1, H2, RG, V1, V2, V3) and V sub supplied from TG-180 board are sent to CCD imager IC100. The video signal from pin 10 of IC100 is passed through Q100 and inputted to pins 2, 3 of IC103. IC103 is the CDS circuit. Pulse for CDS is supplied from TG-180 board as XSHP and XSHD. They passed through inverter IC101 and IC102 respectively and inputted to IC103. The video signal outputted from pin 8 of IC103 is sent to the PR-228 board.



4-21

[3] PR-228 Board

R, G and B signals from PA-200 (B) / 201 (G) / 202 (R) are inputted to the PR-228 board and passed through the 20MHz trap circuit. They are entered to the video amplifier (IC102, 202, 302). The following processes are performed at this amplifier.

- · White shading
- · Gain control
- · Blanking clean
- · White balance
- · Pre knee

After that, The signals are passed through the gamma correction circuit, knee circuit and drive circuit, and inputted to the processor via MB-724 board.

R-ch, G-ch and B-ch are the same circuit composition. Therefore, R-ch is explained as an example.

(1) Video amplifier from trap filter

The signal passed through CDS circuit on the PR board is inputted to the PR-228 board. This signal (CN101-2) includes CCD driving pulse of 20MHz and coupling noise of sample hold. This signal is passed through 20 MHz trap filter (FL101) to prevent bad condition at the following signal process circuit. After this signal is passed through buffer Q103 and is adjusted the level by RV103. After that, it is inputted to pin 18 of VA IC (IC102: CXA1486Q). The serial data from microcomputer on the AT-115 board are passed through pin 20 (SCK), pin 18 (S DATA) and pin 16 (LD1) of CN1 and inputted to the D/A converter IC105. The analog signal outputted from the IC105 controls the all sections on the PR-228 board. This control voltage becomes approx. 2.3 V when the data from AT-115 board is 80H, approx. 4.6 V as maximum when the data is FFH, and 0V as minimum when the data is 00H.

(2) Video amplifier

Video amplifier IC (IC102) is explained. IC102 is composed following circuit.

- · Black set
- Clamp
- · Video amplifier gain switching circuit
- · Blanking clean
- White balance circuit
- · Pre knee correction
- · Shading correction

[Black set]

The black level is decided by optical black level as standard. In case there is the difference between black levels video and optical on the video amplifier gain switching circuit, the black level is fluctuated by switching the gain and the black level component may be added a color. Therefore, the black set signal is added to main line at VA IC (IC102) input. This black set signal cancels the difference. This black set correction signal is made as following. The DARK control signal of pin 6 of IC105 is divided the voltage by R149 and R133. DARK control signal is 2.3 V by standard. R133 becomes approximately 0 V to divide the voltage since it is connected to -2.5 V. The signal is cleaned the GND level by BLK phase at IC103 (1/3). This signal is attenuated by R110 and R111 and inputted to BLACK SHADING control terminal.

When switching the gain, black level change is canceled by adjusting the DARK output with control data from AT-115 board. This adjustment is performed on adjustment mode. In ABB operation mode, The microcomputer on the AT-115 board automatically adjusts it. The OFF-SET of optical black level and blanking level are loosen by adjusting pin 5 (BLACK1) of IC105 on the adjustment mode.

[Clamp]

The signal is clamped to stabilize the DC voltage of video waveform.

[Video amplifier gain switching circuit]

It is available to set the gain from -3 dB to 30 dB as IC. In DKC-ST5, the gain setting is used from 6 dB to 30 dB in LIVE mode according to level diagram and dynamic range. And in STILL mode, 6 dB/12 dB/18 dB/24 dB are used as ISO20/40/80/160 equivalent. The gain is selected by controlling the voltages of pin 19 (GS2) and pin 20 (GS1) with pin 9 (GS1) and pin 12 (GS2) of IC105 output.

The gain is decided by selecting three values(0 V, 2.3 V, 4.6 V) combination.

			GS1	97
		4.6 V	2.3 V	οV
	4.6 V	18 dB	-3 dB	24 dB
GS2	2.3 V	12 dB	3 dB	30 dB
	0 V	0 dB	6 dB	9 dB

[BLANKING clean]

The black level of video signal is fixed at GND level in the blanking period. Like this, blanking cleaning is performed. In this stage, pre-blanking is performed by P BLK pulse.

[White balance circuit]

The output level of IC102 is pin 26 (G-CTRL) input voltage. This is variable approximately more than ±6dB continuously. The microcomputer on the AT-115 board controls the output level of IC102. The white balance is adjusted by losing white level difference between G-ch and R-ch, B-ch.

[Pre knee correction]

Pre knee is that the signal is degreased the amplifier gain in more than some level, is compressed and is limited the maximum amplitude to coincide the signal for dynamic range of signal process circuit on rear stage. KNEE-PT control signal of pin 2 output of IC105 is passed through R147, attenuated at R122 and inputted to pin 9 (KNEE-PT) of IC102. Pin 2 output (KNEE-PT) of IC205 is master KNEE point adjustment signal. It is passed through R252 and controls the IC102.

[Shading correction]

The shading may be appeared by the optical system and image pick up devises. In the DKC-ST5, it is performed to correct the white shading. The amplitude and polarity of H SAW signal output of IC103 (3/4) are controlled by the pin 3 (H SAW) output of IC105. The amplitude and polarity of V SAW signal output of IC103 (4/4) are controlled by pin 4 (V SAW) output of IC105. These two signals are mixed at R140 and R141, and is preblanking cleaned at IC101 (2/3) and is inputted to the white shading correction input terminal (pin 2 of IC102). The output of IC102 is controlled the gain by this saw tooth waveform. As a result, white shading is corrected.

Saw tooth waveform appearance of R-ch H SAW is explained as an example.

The H SAW control signal is divided the voltage by R134 and R136, and inputted to the pin 5 of IC103. When pin 9 of IC104 (2/3) is turned ON by CLP2, IC103 becomes voltage follower. Therefore, the pin 7 is the same output as pin 5.

The IC103 is operational amplifier. The pin 5 and pin 6 are the same voltage since input of positive and negative are imaginary shorted. The difference voltage between the pin 5 and H SAW control signal is applied to the R135. And the respond current flows. This current flows between pin 6 and pin 7 as it is since input impedance of pin 6 is infinity. When CLP2 becomes L, pins 3 and 4 of IC104 becomes open, C126 is started to charge by the above current. The charging current is decided by H SAW control voltage. Therefore, the voltage is fixed and voltage change by time is also fixed. The signal that voltage is increased or decreased by fixed ratio is outputted from pin 7. This is the saw tooth waveform. The amplitude and polarity of H SAW waveform is controlled by changing H SAW control voltage since the current is changed by applied voltage to R135.

The V SAW control voltage is switched by the reading out the signal since V period is changed by whether entire pixel reading out or twice normal speed reading out of CCD reading.

(3) Gamma correction circuit

The video signal outputted from pin 7 of IC102 is adjusted the level by RV401, and is passed through Q401 buffer and the amplifier Q402 through Q404. That output is inputted to gamma amplifier input, the base of Q409. It is attached to load resistor R452 and diode D401 to the collector of Q411. D401 has four diodes. Their cathodes are connected to the voltages which are divided by R456, R461, R458, R455, R454 and RV404 through R453, R457, R460, R463. When signal is nothing, the diode is OFF since the cathode voltage of D401 is low. Therefore, the signal is not passed. When the signal is gradually bigger, the diode is conducted from lower voltage one gradually and the signal is outputted. This signal is negative fed-back to Q412 via RV405. When the signal becomes bigger, the quantity of feedback through the diode become bigger. Therefore, the output gain becomes smaller than signal becomes bigger. The collector signal of Q411 is entered to base of Q413, is attenuated at R467,R465 and R466, is passed through RV405 and is negative fed-back to O412. The Q413 gamma amplifier output is sample-held by Q414 in the CLP2 period and is fedback by the operation amplifier IC402 to the emitter of Q410. The voltage of RV406 (R PED) is passed through the voltage follower of IC402 (1/2) to become low impedance. And is entered to minus input of pin 2 of IC402. R PED control signal of pin 8 of IC105 and the master PED control signal of pin 8 of IC205 are mixed at R472, R477 and R478 and entered to the minus input of pin 2 of IC402. The feedback works to coincide this mixed voltage and the voltage of black level that is sample-held by pin 3 of IC402. As a result, the black level is fixed by gamma circuit. Stabilization of gamma process is planned.

(4) Knee

Gamma correction circuit output of Q413 is passed through the buffer Q415 (1/2). This voltage is compared with the voltage that is set by RV407 at Q415 (1/2 and 2/2). When the signal is bigger than setting voltage, Q415 (2/2) becomes ON, the signal is attenuated at R496 and R497. By this mean, the signal of over setting voltage can be attenuated. After that, this signal is passed through buffer Q416, sent to AT-115 board via MB-724 board. And the other side, the signal is sent to 75 Ω driver circuit.

(5) 75 Ω driver circuit

The signal from Q416 emitter is passed through C422, and the buffer Q419 (1/2 and 2/2), entered to base of Q420 (1/2). The output from that collector is sent to the driver circuit that is composed by Q421 through Q424. The output of this driver circuit is fed-back to the base of Q420 (2/2). The output of driver circuit is sent to the MB-724 board and passed through 75 Ω resistor , is sent to the processor.

[4] TG-180

TG-180 includes following circuits.

- Timing generator (TG) (IC5 : CXD2437TQ)
- Sync generator (SG) (IC101 : CXD8843R)
- H driver (IC2/3/6/7/10/11:74AC04SJ)
- V driver (IC4/8/12 μPD16502GS)
- High speed electronic shutter pulse generation (IC9: μPD16502GS)
- V sub generation
- RG clamp

(1) Timing generator

IC5 (CXD2437TQ) is timing generator (TG) that is developed for CCD imager ICX085. The oscillator is CP1. 40MHz clock is inputted to pin 64 (CKI) of IC5. Some kind of pulses are generated by the clock and HD and VD that are generated by SG. 1/2 frequency divided clock of the oscillator is outputted to pin 51 (CL) and is supplied to SG. Setting of the electronic shutter is performed by serial signals (SDATA, SCLCK, STRBTG) outputted from the microcomputer on the AT-115 board. When the still image is taking, the start timing of reading out is decided by TRIG signal output from the microcomputer on the AT-115 board. The entire pixel reading out or twice normal speed reading out is switched by RM signal outputted from the microcomputer on the AT-115 board. (RM= "H" -Entire pixel reading, RM= "L" -twice normal speed reading out.)

(2) Sync generator

The IC101 (CXD8843R) is programmable SG. The timing of some kinds of pulses outputted from this SG are decided by serial signals (SDATA, SCK, STBSG) with the microcomputer on the AT-115 board. This IC is operated as twice normal speed reading out equivalent. The VD (pin 37 of IC101) is sent to IC104 (SN74HC00), when twice normal speed reading out, as it is, when entire pixel reading out, is masked by 1/2 and is outputted. This signal is sent to TG as VD. The VD of twice normal speed reading out is sent to the AT-115 board and the processor via MB-724 board as communication timing between camera and the processor. The output of pin35 (V WINDOW) and pin 36 (H WINDW) of IC101 are composed at IC102. The WINDOW pulse is made. This pulse indicates center section of screen. This signal is sent to the AT-115 board via MB-724 board. This signal is used for exposure detection when operating auto white balance and METERING is PEAK setting.

(3) H driver

The output of pin 25 (XH1) of IC5 is sent to IC3 / 7 / 11 (74AC04SJ). The output of pin 26 (XH2) of IC5 is sent to IC2 / 6 / 10. ICs that are signals sent is H driver. These ICs includes 6 inverters. The large current drive can be controlled by three ICs of them are connected in parallel. The one of these ICs are connected to the H1 or H2 of one of CCD imagers. The output of IC2 / 3 is sent to the PA-202 board, The output of IC6 / 7 is sent to the PA-201 board and the output of IC10 / 11 is sent to the PA-200 board.

(4) V driver

The outputs of pin 23 (XV1), pin 22 (XV2), pin 21 (XV3) and pin 18 (XSG) are sent to the IC4 / 8 / 12 (uPD16502GS). The IC4 / 8 / 12 are V driver. This IC generates V1 (value 2) from XV1, V2 (value 3) from XV2 and XSG and V3 (value 3) from XV3 and XSG. The signal of value 2 is -7.5 V / 0 V and the signal of value 3 is -7.5 V / 0 V / 15 V.

The output of IC4 is sent to the PA-202 board, the output of IC8 is sent to the PA-201 board and the output of IC12 is sent to the PA-200 board.

(5) High speed electronic shutter pulse generating

Pin 19 (XSUB) of IC5 outputs the electric charge cleaning pulse for CCD imager. High speed electronic shutter of 1 / 24 seconds or less at twice speed reading out, and 1 / 12 seconds or less at all pixels reading out is realized by setting the output period of cleaning pulse in the 1V. This signal is sent to the IC9 and converted -7.5 V / 15 V pulse (22.5 V at peak to peak). The pin 18 of IC9 is that output. That lower side is clamped at 15 V by C49 and D3. Those signals are added to each V sub via C8, C35 and C59.

(6) V sub generation

Board voltage of CCD imager is decided by each imager. R ch circuit is explained as an example. The setting voltage by RV4 is inputted to the base of Q2. Q2 is differential amplifier. The opposite side of the base becomes the same voltage. The voltage of TP3 is (R9+R10) / R10 times of the previous voltage since the base current is minuteness. This voltage is sent to the CCD imager board. As described before, the electronic shutter pulse is also added via C8.

(7) RG clamp

The signals outputted from pin 27 (RG) of IC5 are passed through each clamp capacitor C2, C29 and C53 and lower clamped. They are sent to the CCD imagers.

5. MB-724 board

MB-724 board connects between each circuit boards as mother board.

Other circuits are receiving return video signal from processor and the driver circuit. RET VIDEO (pin 7 of CN209) is main signal. RETV GND (pin 8 of CN209) is GND. The RET VIDEO sent to the emitter of Q203 via R203,R244 and C220. The RETV GND is sent to the base of Q203 via Q201. The frequency response compensation of connection cable is performed at this process. Q202 through Q207 are the feedback amplifier. The emitter output of Q207 is divided by two. One is inputted to IC201 via Q208 and Q209. The IC201 is 75Ω driver of video signal. The output of pin 12 of IC201 is inputted to pin 4 of CN207 and is sent to the BNC connector via CN-1395 board as MONITOR output. Another is passed through Q210, Q211 and pin 1 of CN207 and inputted to the pin 8 of DIN connector via CN-1395 board as view finder output. This board composes DC-DC converter that supplies each converted voltages from processor +12 V power to each board.

4-28 DKC-ST5 (UC)

[6] AT-115 board

AT-115 board is composed following circuits.

- · One chip microcomputer
- Microcomputer reset circuit
- EEPROM
- · Video signal detection circuit
- · Lens control circuit for video camera
- · Communication buffer with the processor
- · Detection sensor for camera setting direction

(1) One chip microcomputer

IC10 is one chip microcomputer uPD78P218AGC. This microcomputer performs camera block control, communication with the computer of processor side, communication (control) with exclusive lens, control of lens for video camera and reading of remote controller switch mode.

(2) Microcomputer reset circuit

The IC8 (TL7705ACPS) resets microcomputer of IC10 when turning power on.

(3) EEPROM

IC12 (M6M80021) is EEPROM that stores each setting values of D/A converter on PR-228 board and the damage information of CCD imager. The damage information of CCD imager is sent to the processor, and when video data is transmitted, damage correction is performed.

(4) Video signal detection circuit

The Q1 through Q20, IC1 through IC6 and IC14 are video signal detection circuit. This circuit performs detection of white balance and black balance and brightness detection of live picture mode.

(5) Lens control circuit for video camera

IC11 (M62352GP) is D/A converter of 8 bit 12 ch. The microcomputer controls the iris of lens for video camera through this IC. This D/A converter outputs 0 V at 00 (hexadecimal), 5 V at FF(hexadecimal).

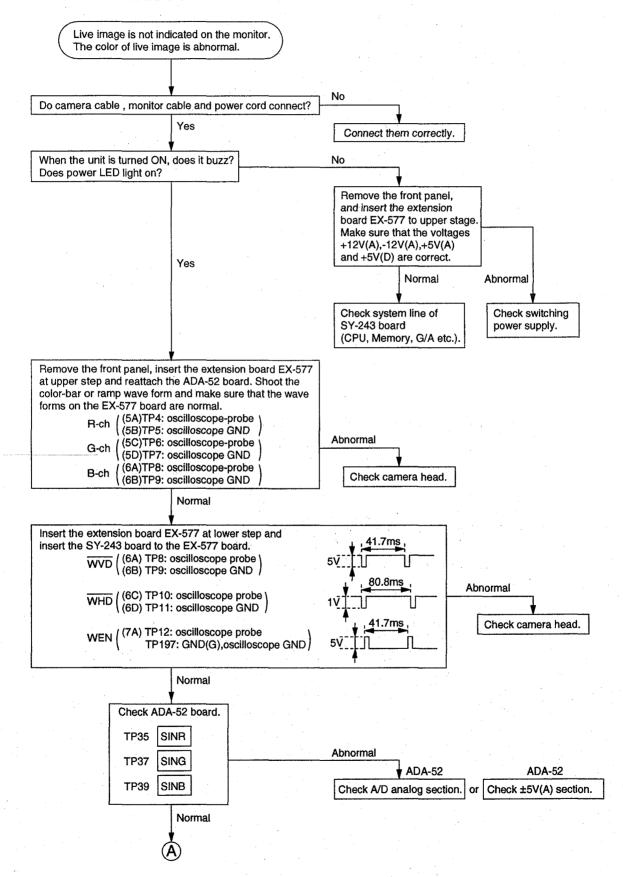
The iris value of lens is set by output of pin 18 of IC11. This signal is converted the level by IC7 and outputted to pin 48 of CN1. It is passed through the MB-724 board and CN-1462 board. It is sent to the lens. The servo of lens is switched by the output of pin 19 of IC11. This signal is driven by Q21 and Q22 and is sent to the lens in the same way above. The iris operation of lens is changed at REMOTE or LOCAL by switching output pin 2 of IC11 with two values $5 \ V / 0 \ V$.

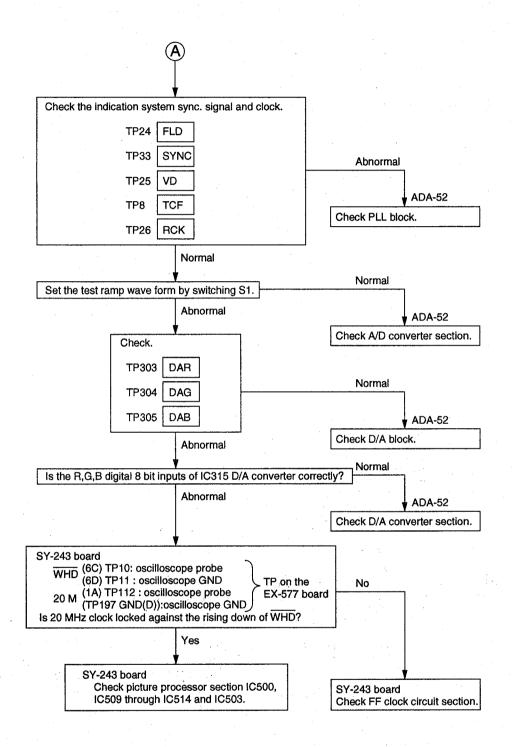
- (6) Communication buffer with the processor
 - IC13 is the buffer between the processor and communication line. The output of pin 37 (TXD) is sent to the pin 3 of IC10, and converted the differential digital signal. It is outputted to pin 5 (TXD+) and pin 6 (TXD-) and sent to the processor via MB-724 board. Conversely, the differential digital signal at pin 8 (RXD+) and pin 7 (RXD-) of IC13 is converted to the digital signal 5 V / 0 V. It is sent from pin 2 of IC13 to pin 36 (RXD) of IC10.
- (7) Detection sensor for camera setting direction

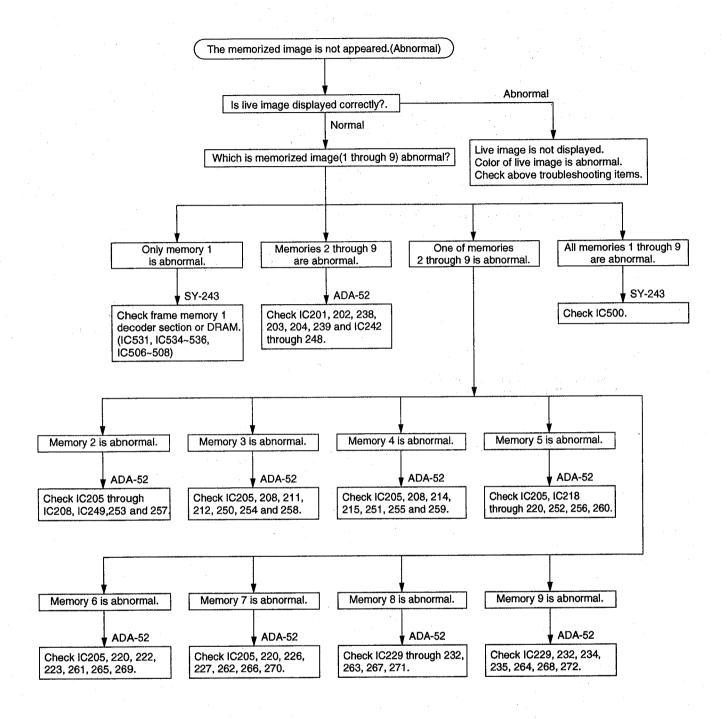
The SE1 (RIP-1020) detects setting direction of camera by slanting sensor. The pin 3 of SE1 is output. When regular setting, output is "L", when vertical setting, output is "H". This output is inputted to the computer pin 33 (TILT) of IC10. Its information is sent to the computer of the processor and decided the direction of character indicating on the picture screen.

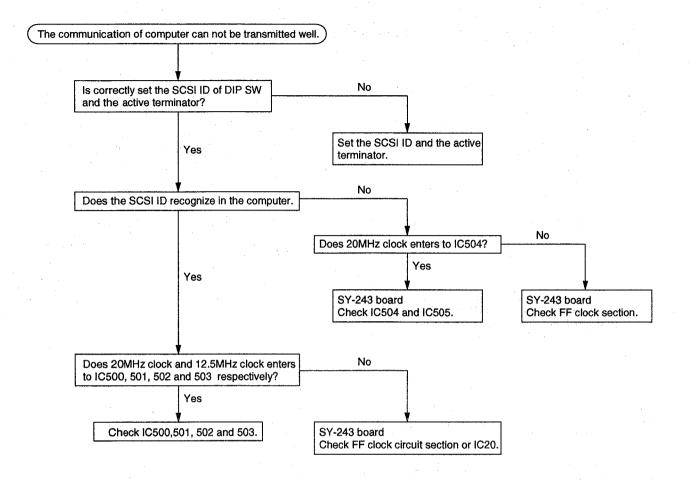
SECTION 5 TROUBLE SHOOTING

5-1. PROCESSOR BLOCK





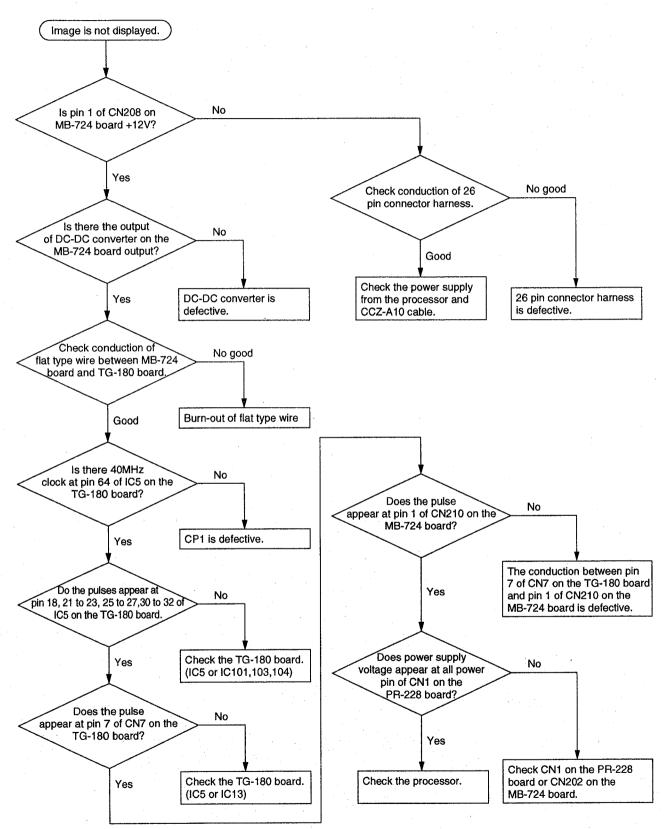




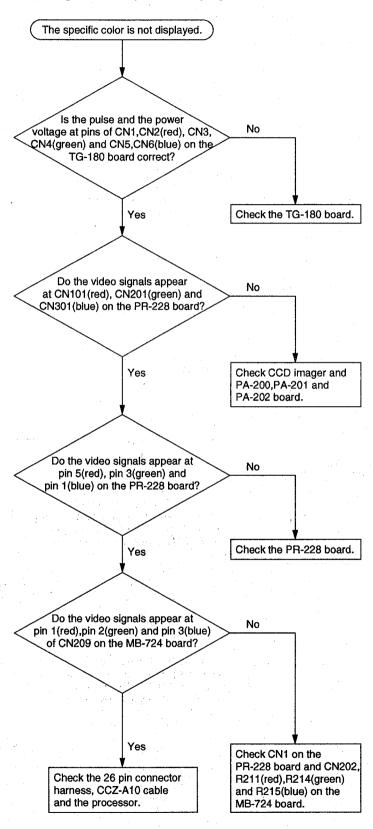
5-2. CAMERA BLOCK

- (1) Picture is not indicated.
- (2) The specific color is not indicated.
- (3) The remote control can not be worked.
- (4) Zoom and focus of special lens can not be worked.
- (5) Flashlight does not flash.
- (6) White balance and black balance can not be adjusted.

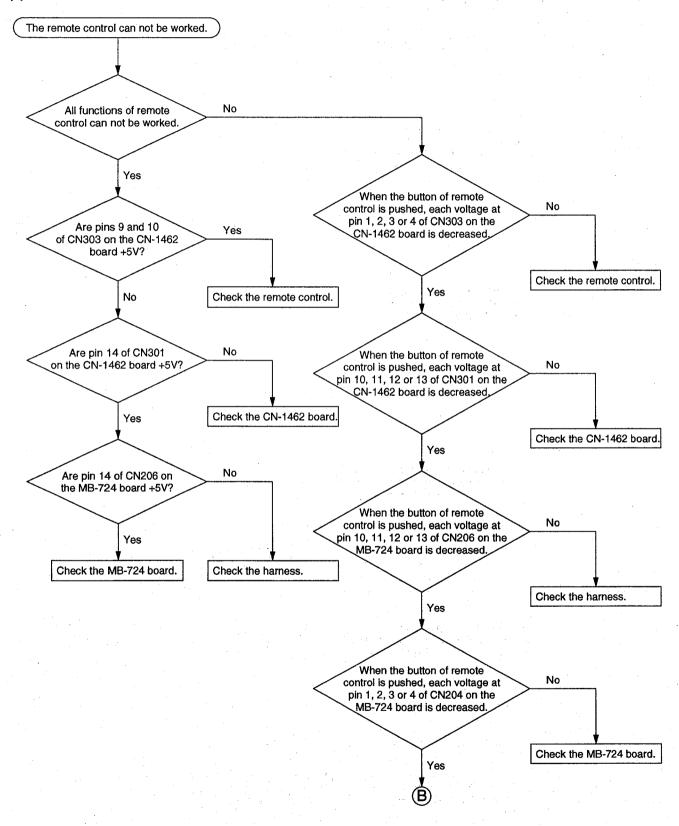
(1) Image is not displayed.

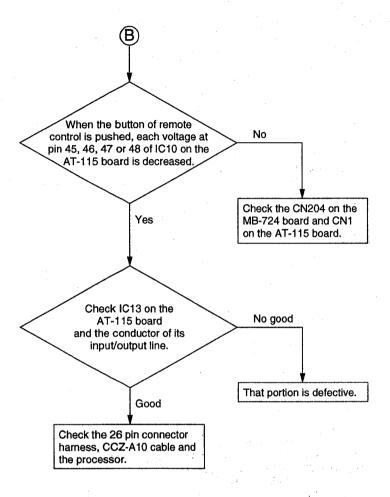


(2) The specific color(red, green, blue) is not displayed.

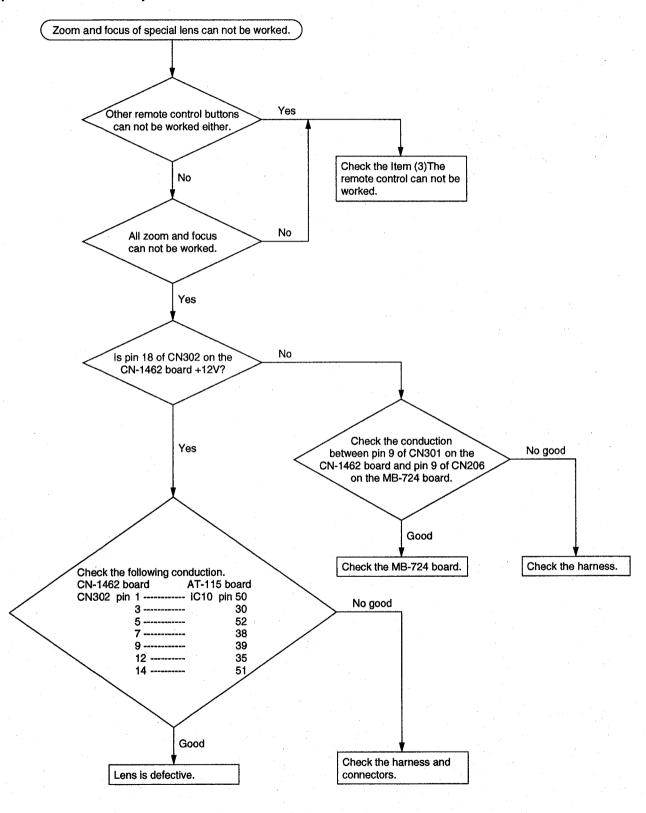


(3) The remote control can not be worked.

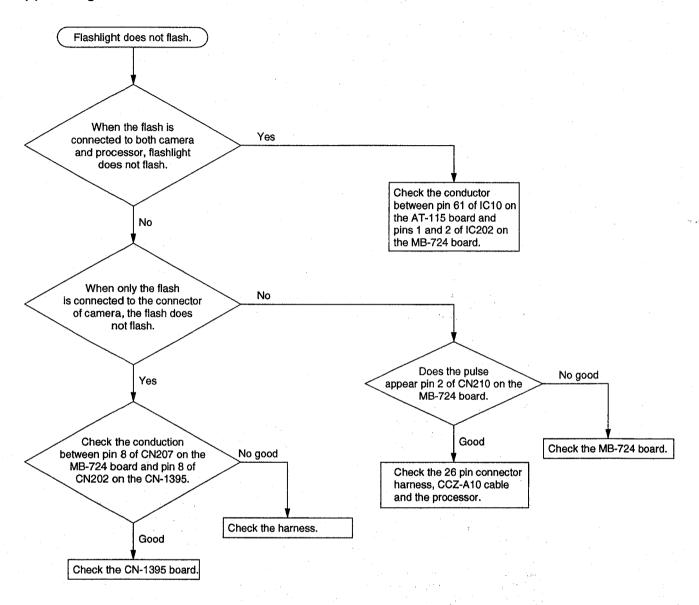




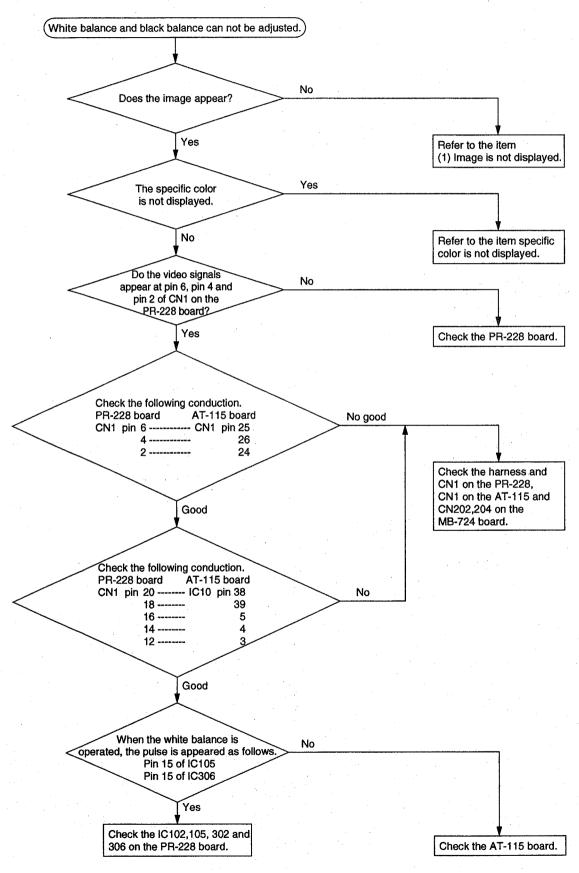
(4) Zoom and focus of special lens can not be worked.



(5) Flashlight does not flash.



(6) White balance and black balance can not be adjusted.



SECTION 6 SEMICONDUCTOR PIN ASSIGNMENTS

Semiconductors of which functions are equivalent are described here. For parts replacement, refer to the section of Spare Parts in this manual. The circuit diagram of each IC is obtained from the IC data book published by the manufacturer.

DIODE	PAGE	TRANSISTOR	PAGE	TRANSISTOR PAGE	IC PAGE
1SS123-T1	6-3	2SA1162G	6-4	XN6501 6-4	74AC04SJ-T5R 6-19
1SS226	6-3	2SA1162G-TE85L .	6-4	XN6501-TW 6-4	
1\$S300-TE85L	6-3	2SA1213Y-TE12L	6-4	XN6534 6-4	AM29F200T-120SC 6-5
		2SA1226-T1E3	6-4	XN6534-TW 6-4	AM29F200T-120SC/T 6-5
2GWJ42	6-3	2SA1576A-T106-R	6-4	XP1401-TX 6-4	AN7805 6-21
	*	2SA1576-A-T106-Q		XP1501-TXE 6-4	Programme and the second
FMP1	6-3	2SB1295-UL6	6-4	XP4601-TXE 6-4	BH9595FP-Y-E2 6-5
FMP1-T-148	6-3	2SB798-DL	6-4	XP6501-TXE 6-4	
		2SB815B6B7-TB	6-4	•	CX20095A-TH 6-6
LN1251C-TR	6-3	2SC1623	6-4		CXA1399Q 6-6
		2SC2712-GL-TE85I	6-4		CXA1439M6-7
RD15SB	6-3	2SC2714-O	6-4		CXA1439M-TH 6-7
RD15SB-T1		2SC2714O-TE85L .	6-4		CXA1486Q-TH6-7
RD3.6M-B1		2SC2873Y-TE12L	6-4		CXA1645M 6-8
RD3.6M-T1B		2SC4081T106R	6-4		CXA1645M-T6 6-8
RD4.3M-B	6-3	2SC4081T106S			CXD1159Q6-9
RD4.3M-T1B		2SC4103-Q	6-4		CXD1159Q-TH 6-9
RD5.1SB-T1		2SC4103T106-Q	6-4		CXD1178Q 6-8
RD5.1SB-T2		2SC4176-B34	6-4		CXD207-109Q 6-10
RD5.6M-B2		2SC4176T1B33B34			CXD209-107Q 6-10
RD5.6M-T1B		2SD1048-X7	6-4		CXD210-108Q 6-11
RD5.6SB		2SD1048X7-TB	6-4		CXD2437TQ 6-12
RD5.6SB-T1	6-3	2SD1623-S	6-4		CXD8843R 6-11
RD9.1M-B1	6-3	2SK853-K5	6-4	and the second s	CXD8932Q 6-11
RD9.1M-T1B		2SK853-T1K5			CXK581000AM-70LL 6-13
		2SK94-T1X2	6-4		CXK581000AM-70LL-TL 6-13
SLP281C-51	6-3	2SK94-T1X2X3X4 .	6-4	$(\mathbf{v}_{\mathbf{k}}, \mathbf{v}_{\mathbf{k}}) = (\mathbf{v}_{\mathbf{k}}, \mathbf{v}_{\mathbf{k}}, \mathbf{v}_{\mathbf{k}},$	
		2SK94-X2X3X4	6-4		DS1000Z-50 6-12
TLUG163	6-3	2SK94-X4	6-4		DS1000Z-50(TE2) 6-12
					DS1000Z-75(TE2) 6-12
		DTA114EUA-T106 .	6-4		
		DTC114EU	6-4		HM5117800BJ-7EL 6-13
		DTC114EUA-T106	6-4		HM514260CJ7-Z 6-14
		DTC114YKA-T146	6-4	•	HM514800CJ7Z6-14
		DTC143TKA-T146	6-4		HM530281-20 6-15
		DTC144EUA-T106	6-4	·	HM530281RTT-20 6-15
					HN58C66FP-25 6-15
		XN1501	6-4		
		XN1501-TX	6-4		LM1881M 6-15
		XN2401	6-4		LM6361M 6-16
		XN2401-TX			LM6361M-T1 6-16
		XN2501			
		XN2501-TX	6-4		M27C1001-10F1 6-16
		XN4501	6-4		M62352GP 6-16
		XN4501-TW			M62352GP-75ED 6-16
		XN4601			M6M80021FP 6-17
* *		XN4601-TW			M6M80021FP-T3 6-17
		XN6401			MC145407F 6-17
		XN6401-TW	6-4		MC145407F-ML2 6-17

IC	PAGE	IC		PAGE
NJM2233BM	6-17	TC74VHC	11F(EL)	6-23
NJM2233BM(TE2)			125F	
NJM2903M			125F(EL)	
NJM2903M-TE2			138F(EL)	
THOMESOOM TEE	0-17		161F(EL)	
OP293-S	6 17		163F	
Or 290°0	0-17			
RPI-1020	0.40		163F(EL)	
			20F(EL)	
RTC-4553B			244F	
RTC-4553B-L2	6-18		244F(EL)	
0.00.000.0			27F(EL)	
S16MD01			32F(EL)	
SC7S04F			374F	
SN74HC00ANS			374F(EL)	
SN74HC00ANS-E05	6-18	TC74VHC	T245F(EL)	6-25
SN74HC02ANS		TC74VHC	74F(EL)	6-20
SN74HC02ANS-E05	6-18	TC7S04F(TE85R)	6-18
SN74HC04ANS	6-19	TC7S08F		6-25
SN74HC04ANS-E05	6-19	TC7S08F(TE85R)	6-25
SN74HC08ANS	6-19		•••••	
SN74HC08ANS-E05	6-19		(TE12R)	
SN74HC14ANS	6-19		· V	
SN74HC14ANS-E05	6-19		V-E05	
SN74HC164ANS			N	
SN74HC164ANS-E05			V-E05	
SN74HC244ANS				
SN74HC244ANS-E05 .			S-E20	
SN74HC273ANS-E05 .			PS	
SN74HC74ANS			PS-E05	
SN74HC74ANS-E05			PS-B	
SN74HCU04ANS-E05			S-B-E20	
SN74HCU04ANS-E20.		IL//USCF	'3-B-EZU	0-25
SN75179BPS		1100701.00		0.00
			3T-E1	
SN75179BPS-E05			2GS(1)	
SPT7855SCT			2GS(1)-E2	
SYM53CF92A-64QFP.			GN-056-LMU	
			GD-047-LML	
TA7805S			IGC-25-7EA .	
TA79005S			IGC-11-3B6	
TC4053BFS		UPD78P2	18AGC-AB8	6-29
TC4053BFS-EL				
TC4S71F				
TC4S71F(TE85R)				
TC4W53FU				
TC4W53FU(TE12R)				
TC5081AP	6-22			
TC74AC04F-EL	6-19			
TC74HC4053AFS	6-22			
TC74HC4053AFS-EL	6-22			
TC74HC4072AF	6-23			
TC74VHC00F	6-18			
TC74VHC00F(EL)	6-18			
TC74VHC02F				
TC74VHC02F(EL)				
TC74VHC04F				
TC74VHC04F(EL)				
TC74VHC08F(EL)				
TC74VHC11F				

6-2

DIODE

-TOP VIEW-

1SS226 1SS123-T1



FMP1 FMP1-T-148

RD3.6M-B1 RD4.3M-B RD5.6M-B2 RD9.1M-B1 RD3.6M-T1B RD4.3M-T1B RD5.6M-T1B RD9.1M-T1B

-TOP VIEW-

1SS300-TE85L





LN1251C-TR;RED



RD5.1SB-T2 RD5.1SB-T1



-TOP VIEW-



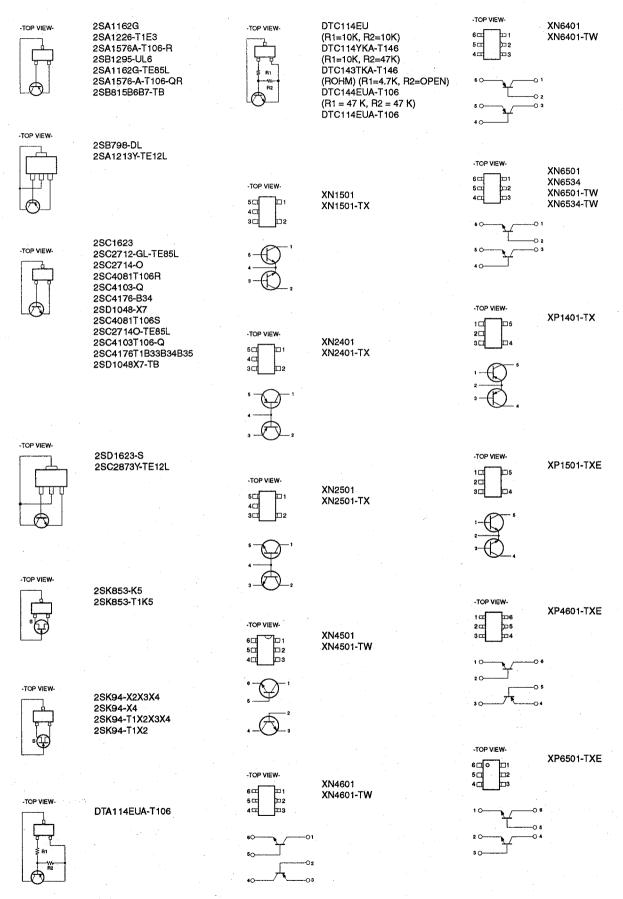
RD15SB RD5.6SB RD15SB-T1 RD5.6SB-T1



SLP281C-51 ;GREEN TLUG163

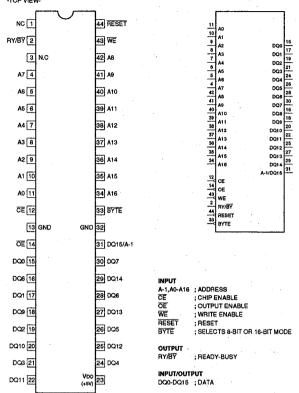
TRANSISTOR

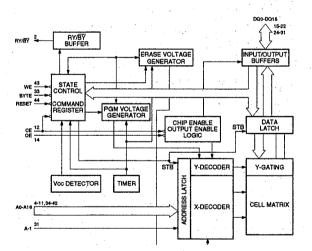
TRANSISTOR



AM29F200T-120SC (AMD) AM29F200T-120SC/T

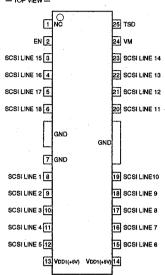
C-MOS FLASH MEMORY



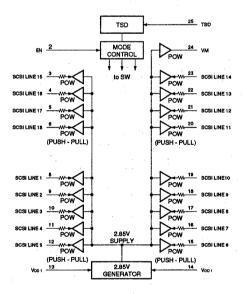


BH9595FP-Y-E2 (ROHM)

SCSI ACTIVE TERMINATOR — TOP VIEW —

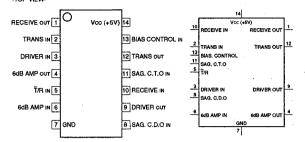


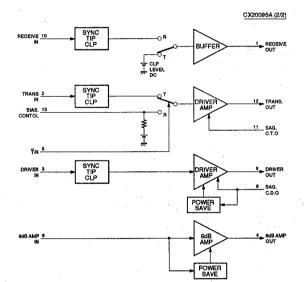
EN (PIN 2)	MODE
0	SCSI Line are High Impedance.
1	SCSI Line are connected with 2.85V through resistors.
	0 ; LOW LEVEL 1 : HIGH LEVE



CX20095A-TH (SONY)FLAT PACKAGE

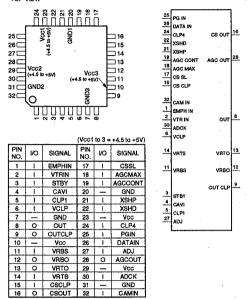
VIDEO LINE DRIVER AND RECEIVER





CXA1399Q (SONY)FLAT PACKAGE

HEAD AMPLIFIER FOR DIGITAL CCD CAMERA



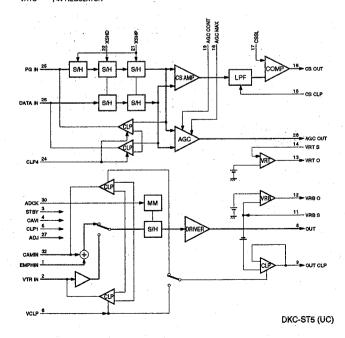
; S/H TRIGGER PULSE INPUT FOR A/D
; INTERNAL SH SAMPLING WIDTH ADJUSTMENT
; AGC AMPLIFIER GAIN ADJUSTMENT
; AGC AMPLIFIER GAIN ADJUSTMENT
; AGC OUT (FIN-29) COULFING DEPENDING ON CAPACITY
; CAMERA MODE (CHIPHINCAMIN -> OUT) AND
VTR MODE (VITRIN -> OUT) SWITCHING AT INPUT STAGE
; CLAMP PULSE CLP1 : CAMERA
; CLAMP PULSE CLP1 : CAMERA
; CLAMP PULSE CLP1 : CAMERA
; CLAMP FULSE CLP1 : COMERA
; AFRICAN SUPPRESSION
; CCD SIGNAL INPUT
; CCD SIGNAL INPUT
; CCD SIGNAL INPUT
; ATSIGN FOWER SAVE MODE SWITCHING (NORMALLY GND)
; CLAMP FULSE VCLP : VIDEO
; INTERNAL OPERATION AMPLIFIER INVERTED INPUT IN 2V REGULATOR
; INTERNAL OPERATION AMPLIFIER INVERTED INPUT IN 4V REGULATOR
; VIDEO PLASECK SIGNAL
; HIGH SPEED PULSE INPUT FOR S/H CLP1 CLP4 CSCLP CSSL DATAIN EMPHIN PGIN STBY VCLP VRBS VRTS VTRIN XSHD XSHP

OUTPUT

; AGC OUTPUT
; FOR CHROMA SUPPRESSION AND FOR SELECTING SM CIRCUIT OPERATION FOR AID
; DRIVER OUTPUT STANDARD D RANGE : 2.2V
; CLAMPS BLACK LEVEL OF OUT (PIN-8)
; 2V REQULATOR
; 4V REQULATOR

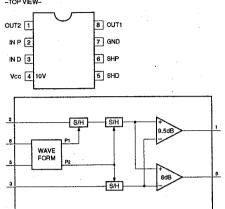
OUT

VRBO VRTO



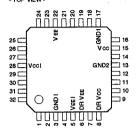
CXA1439M (SONY)FLAT PACKAGE CXA1439M-TH

CORRELATED DOUBLE SAMPLING

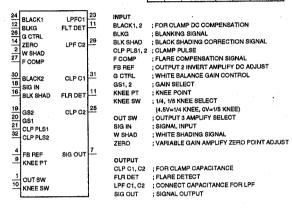


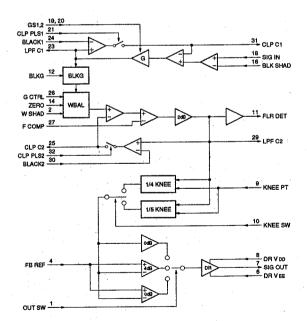
CXA1486Q-TH (SONY)

VIDEO AMPLIFIER FOR VIDEO CAMERA - TOP VIEW -

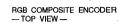


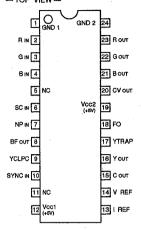
	VCC, VCCI, DR VCC, = (+5V) VEE, VEEI, DR VEE, = (-2.5 to -5.5V											
PIN No.	VO	SIGNAL	PIN No.	1/0	SIGNAL							
1	. 1	OUTSW	17	-	GND1							
2	T	WISHAD	18	1	SIG IN							
3	-	GNDI	19	1	GS2							
4	1	FBREF	20	- 1	GS1							
5	-	V EE I	21	1	CLP PLS1							
6		DRVEE	22	-	VEE							
7	0	SIG OUT	23	0	LPF C1							
8	-	DRVcc	24		BLACK1							
9	1	KNEE PT	25	0	CLP C2							
10	1	KNEE SW	26	1	G CTRL							
11	0	FLR DET	27	1	F COMP							
12	T	BLKG	28	-	Vccl							
13	_	GND2	29	0	LPF C2							
14	1	ZERO	30	1	BLACK2							
15	_	Vcc	31	0	CLP C1							
16	1	BLK SHAD	32	1	CLP PLS2							

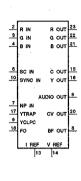




CXA1645M (SONY)FLAT PACKAGE CXA1645M-T6







; ANALOG B

B IN G IN NP IN R IN B IN ; ANALOG B
G IN ; ANALOG G
NP IN ; NTSC/PAL MODE SELECT
FI IN ; ANALOG FI
SC IN ; SUB-CARRIER
SYNC IN; COMPOSITE SYNC SIGNAL

OUTPUT

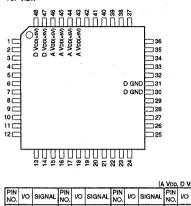
OUTPUT
B OUT ; ANALOG B
BF OUT ; BF PULSE FOR MONITOR
C OUT ; CHROMA SIGNAL
CV OUT ; COMPOSITE VIDEO
G OUT ; ANALOG G
R OUT ; ANALOG R
Y OUT ; Y SIGNAL

OTHER

TO ADJUST FOR INNER FILTER
IREF ; REFFERENCE CURRENT
VREF ; REFFERENCE VOLTAGE
YCLPC ; Y SIGNAL CLAMP CAPACITOR
YTRAP ; Y SIGNAL CROSS-COLOR TRAP

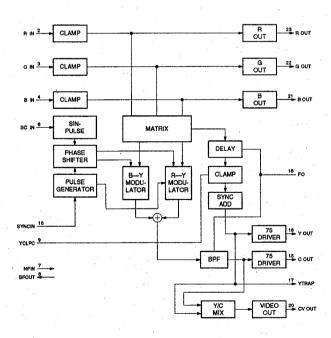
CXD1178Q (SONY)

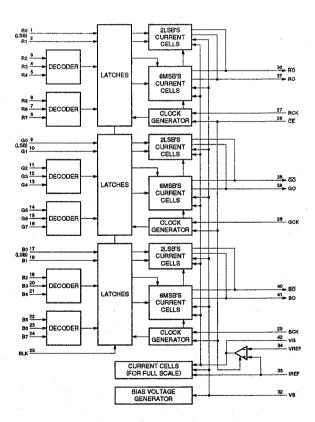
C-MOS 3CH 8-BIT 40MHz D / A CONVERTER



			_
	1 2	RO RO	
	3	R2	1
	4	R3 GC	38.
	-	R4 GC	
	6	R6	1
	_7	R6 B0	40
	8	R7 BC	41
	9	1	42
	10	Go V	100
	11	G1 VI	3
	12	G2	1
	13	G3	
		G4	1
	15	G5	1
	16	G6	1
	_	G7	
	17	Во	
	18	B1	-
DD = +5V)	19	B2	l
SIGNAL	20	83	
1	21	B4	1
RO	22	B5	1
GO	23	86	
GO	24	87	ı
BO			1
ВО	27	RCK	
VG	20	GCK	
A VDD	28	BCK	
A VDD	34	VREF	1
A VDD	35	IREF	
A VDD			1
D Voo		BLK CE	
I n ADD		25 26	

1	1	Ro(LSB)	13		G4	25	1	BLK	37	0	RO
2	-	R1	14	1.	G5	26	1	CE	38	0	GO
ß	- 1	R2	15	-	G6	27	1	RCK	39	0	GO
4	Ī	R3	16	-	G7	28	1	GCK	40	0	BO
5	1	R4	17	1	BO(LSB)	29	1	BCK	41	0	ВО
6	.1	R5	18	- 1	81	30	-	D GND	42	1	VG
7	1	R6	19	1	B2	31	-	D GND	43	_	A VDD
8	1	R7	20	1	B3	32	1	VB	44	-	A Vod
9	1	GO(LSB)	21	1	B4	33	-	A GND	45		A Vod
10	1	G1	22	1	B5	34	-	VREF	46	-	A VDD
11	T	G2	23	1	B6	35	1	IREF	47	-	D Voo
12	1	G3	24	1	B7	36	0	RO	48		D Voo

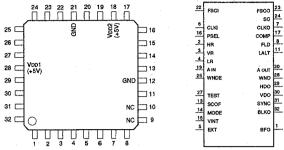




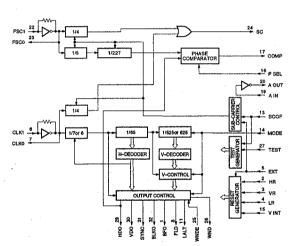
CXD1159Q (SONY) CXD1159Q-TH

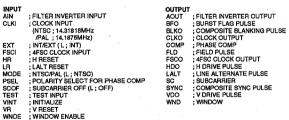
C-MOS SYNC GENERATOR

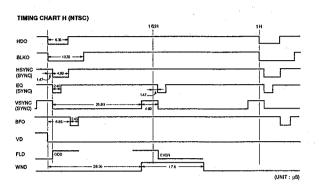
-TOP VIEW

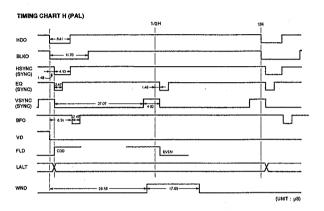


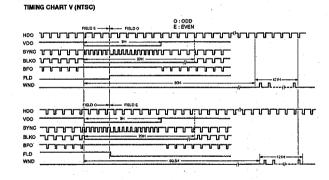
PIN NO.	1/0	SIGNAL	PIN NO.	1/0	SIGNAL	PIN NO.	1/0	SIGNAL	PIN NO.	1/0	SIGNAL
1	0	BFO	9	1	NC	17	.0	COMP	25	1	WNDE
2	T	HR	10	1	NC	18		VDD2(+5V)	26	0	WND
3		VR	11	0	LALT	19	ī	AIN	27	ī	TEST
4	T	LR	12	_	GND	20	0	TUOA	28	_	VDD1(+5V)
5	1	EXT	13	Π.	SCOF	21		GND	29	0	HDO
6		CLKI	14	1	MODE	22	1	FSCI	30	0	VDO
7	0	CLKO	15	1	VINT	23	0	FSCO	31	0	SYNC
8	0	FLD	16	1	PSEL	24	0	SC	32	0	BLKO

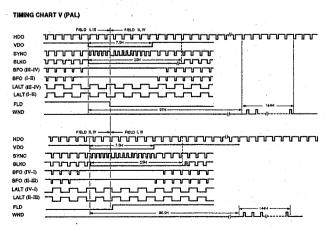






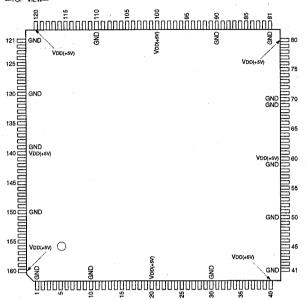






CXD207-109Q (SONY)

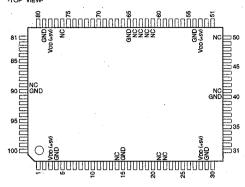
C-MOS GATE ARRAY



											(Vob = +5 V)
PIN No.	1/0	SIGNAL									
1	=	GND	41	-	GND	81	_	GND	121	_	GND
2	Τ,	D9	42	_	M RQ	82	}	CAS	122	0	CA5
3		D10	43	0	M AK	83	1	RAS	123	0	CA6
4	ī	D11	44	0	M WE	84	0	CRKEY	124	0	CA7
5	I	D12	45	0	MRE	85	0	CRR	125	0	CA8
6	1	D13	46	0	TLR	86	0	CRG	126	0	CA9
7	1	D14	47	0	TLW	87	0	CRB	127	0	CA10
8	-	D15	48	ı	J RQ	88	0	IN 1	128	0	CA11
9	_	A23	49	0	J AK	89	o	IN 2	129	0	CA12
10	1	GND	50	_	GND	90	_	GND	130	-	GND
11	1	A22	51	0	JWE	91	0	IN 3	131	0	CA13
12	1	A21	52	0	JRE	92	0	IN 4	132	0	CA14
13	1	A20	53		FRQ	93	0	OUT1	133	0	CA15
14	1	A19	54	0	F.AK	94	0	OUT2	134	0	CA16
15	-	A18	65	0	F WE	95	0	OUT3	135	1/0	CDO
16	1	A17	56		FDTAK	96	0	OUT4	136	NO.	CD1
17	1	A 16	57	0	DMDIR	97	0	OUT5	137	1/0	CD2
18	1	A15	58	0	LDEN1	98	0	OUT6	138	1/0	CD3
19	T	A14	59	_	GND	99	0	IN 5	139	-	GND
20		Voo	60	_	VDD	100	_	Voo	140	1	Voo
21	1	A13	61	0	UDEN1	101	-	RST	141	1/0	CD4
22	i i	A12	62	0	LDEN2	102	1	TST1	142	1/0	CD5
23	1	A11	63	ō	UDEN2	103	0	PRCS	143	1/0	CD6
24	Ħ	A10	64	T	RHD	104	0	EECS	144	1/0	CD7
25	T	A9	65	1	RVD	105	0	JPCS	145	0	DTACK
26	T	A8	66	1	RFLD	106	0	FMCS	146	T	RD
27	1	A7	67	0	BLKO	107	0	ESCS	147	T	LDS
28	Ť	A6	68	T	RCK	108	0	MSCS	148	T	UDS
29	i	A5	69	_	GND	109	0	FWCS	149	1	AS
30	_	GND	70	_	GND	110	_	GND	150	=	GND
31	1	A4	71	T	WVD	111	0	CRDG	151	1	SYCK(16M)
32	T	A3	72	1	WFLD	112	0	COE	152	1	TST2
33	Ť	A2	73	1	WCK	113	0	CWE	153	0	DR00
34	Η	Ai	74	0	VINT	114	0	ccs	154	0	DR01
35	i.	TCK	75	ō	ADAEN	115	0	CAO	155	0	DR02
36	Ħ	TDI	76	Ť	WCSNC	116	0	CA1	156	1	DAKO
37	i	TENA1	77	0	W EXT	117	.0	CA2	157	T	DAK1
38	0	TDO	78	ō	W CTHD	118	0	CA3	158	1	DAK2
39	T	VST	79	0	R CTHD	119	0	CA4	159	1	D8
40	1=	VDD	80	-	Vpp	120		VDD	160	-	Von

CXD209-107Q (SONY)

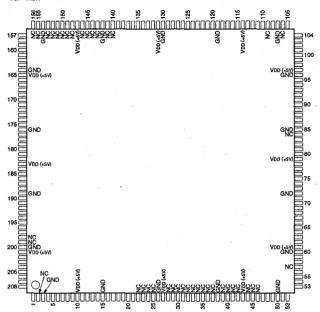
C-MOS GATE ARRAY



PIN No.	1/0	SIGNAL												
1	_	DA0	21	_	NC	41		NC	61	ł	NC	81	_	GI2
2	Ĺ	DA1	22	-	NC	42	0	GO0	62		NC	82	1	GI3
3	-	VDD	23	_	TCK	43	0	GO1	63	-	NC	83	L	Gl4
4	_	GND	24	_	TDI	44	0	GO2	64	1	NC	84	1	GI5
5	-	DA2	25		TENA1	45	0	- GO3	65		GND	86	1	GI6
6	1	DA3	26	_	TD0	46	0	GO4	66	- 1	BIO	86	1.	GI7
7	1	DA4	27	_	VDT	47	0	GO5	67	-	BI1	87	1	GI8
8	1	DA5	28		Voo	48	0	GO6	68	T	Bl2	88	1	GI9
9	1	AD0	29	_	GND	49	0	GO7	69	F	BIS	89	_	NC
10	_	AD1	30		CK	50	1	NC	70	1	BI4	90		GND
11	1	AD3	31	1	RST	51	0	RO0	71	1	Bi5	91		RI0
12	1	CS	32	1	BO0	52	0	RO1	72	1	B16	92	1	RII
13	T	WR	33	0	BO1	53	_	Voo	73	1	BI7	93	-	RI2
14	_	NC	34	0	BO2	54	_	GND	74	-	B18	94	1	RI3
15	_	GND	35	0	BO3	55	0	RO2	75	1	B19	95	1	RI4
16	1	MX0	36	0	BO4	56	0	RO3	76	-	NC	96	1	RI5
17	1	MX1	37	0	BO5	57	0	RO4	77	1	GI0	97	1	RI6
18	1	UV	38	0	BO6	58	0	RO5	78		VDD	98	1	RI7
19	1	EHK	39	0	807	59	0	RO6	79	_	GND	99	1	RI8
20	1	P4	40	0	GND	60	0	RO7	80	-	GI1	100	1	RI9

CXD210-108Q (SONY)

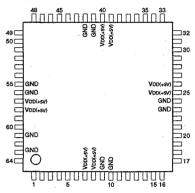
C-MOS GATE ARRAY



														(VDD = +5V)
PIN No.	1/0	SIGNAL	PJN No.	1/0	SIGNAL	PIN No.	1/0	SIGNAL	PIN No.	1/0	SIGNAL	PIN No.	i/O	SIGNAL
1	1	IRC17	43	_	Voo	85		GND	127	1/0	GI2	169	0	OGCO0
2	_	NC	44	-	NC	86	1	IADRS0	128	1/0	GI1	170	0	OGCO1
3		GND	45		NC	87	1	IADRS1	129	1/0	GIO	171	0	OGCO2
4	-1	IGCIO	46	-	NC	88	ī	IADRS2	130	1	GND	172	0	OGCO3
5	Ш	IGC 1	47		TCK	89	Ι.	IADRS3	131	1	Voo	173	Ó	OGCO4
6		IGCI2	48	_	TDI	90	_	IADRS4	132	10	817	174	0	OGCO5
7		IGCI3	49		TENA1	91	_	ICEX	133	10	Bl6	175	0	OGCO6
8		IGCI4	50		GND	92	_	IREADX	134	1/0	815	176	_	GND
9	I	IGCI5	51	0	TDO	93		IWRITEX	135	1/0	Bl4	177	0	OGCO7
10		Voo	52	1	VST	94	0	OINTX -	136	10	BI3	178	0	OGCO8
11		IGC16	53	1/0	800	95	1	IRESETX	137	1/0	Bl2	179	0	OGCO9
12	1	IGC17	54	1/0	BO1	96	-	GND	138	10	BII	180	0	08000
13	_	IBC10	55	1/0	BO2	97		VDD	139	1/0	B10	181	0	OBCO1
14	1	: IBCI1	56	0	ВОЗ	98	1/0	DB0	140	-	NC	182	O	OBCO2
15	-	GND	57	-	NC	99	1/0	DB1	141		NC .	183	_	VDD
16	1	IBC12	58	1/0	BO4	100	5	DB2	142	-	GND	184	0	OBCO3
17	-1	IBCI3	59	1/0	BO5	101	5	DB3	143	Ţ	NC	185	o	OBCO4
18		IBC14	60		GND	102	5	DB4	144	-	NC	186	0	OBCO5
19	_	IBCI5	61	-	VDD	103	5	DB5	145	_	NC	187	0	OBCO6
20	1	IBCI6	62	1/0	BO6	104	1/0	DB6	146		NC	188	0	OBCO7
21	1	IBCI7	63	1/0	BO7	105	VO	DB7	147	-	Voo	189	_	GND
22		NC.	64	1/0	GO0	106	_	NC	148		NC	190	0	OBCO8
23		NC	65	1/0	GO1	107	_	GND	149	_	NC	191	0	OBCO9
24		NC	66	1/0	GO2	108	1	ICLK	150	_	NC	192	0	OEXEHKN
25	-	NC	67	1/0	GO3	109	-	NC	151		NC	193	0	OP4
26	-	GND	68	1/0	GO4	110	1/0	IIREQX	152	Ţ	NC.	194	0	OMTX0
27	-	VDD	69	I/O	GO5	111	1/0	I1ACKX	153	_	NC	195	0	OMTX1
28		NC	70	1/0	GO6	112	WO	RI7	154		GND.	196	0	OUV
29	-	NC	71	1/0	G07	113	1/0	RI6	155		NC	197	0	OCLK
30	-	NC	72	_	GND	114	_	Vpo	156	_	NC	198	=	NC
31	-	NC .	73	1/0	RO0	115	1/0	R15	157	0	ORCO0	199	_	NC
32		NC	74	1/0	801	116	1/0	RI4	158	0	ORCO1	200		GND
33		NC	75	1/0	RO2	117	1/0	RI3	159	0	ORCO2	201	-	VDD
34		NC	76	1/0	RO3	118	1/0	RI2	160	0	ORCO3	202	1	IRC10
35		NC	77	1/0	RO4	119	_	GND	161	0	ORCO4	203	1	IRC11
36		NC	78	1/0	RO5	120	1/0	RII	162	0	ORCO5	204		IRCI2
37		NC	79	_	VDD	121	1/0	RIO	163	0	ORCO6	205	-	IRCI3
38	-	GND	80	1/0	RO6	122	1/0	GI7	164	_	GND	206	T	IRCI4
39	-	NC	81	1/0	RO7	123	1/0	G16 ·	165	-	VDD	207	ī	IRCI5
40	-	NC	82	1/0	OIREQX	124	1/0	GI5	166	0	ORCO7	208	T	IRC16
41	=	NC	83	1/0	OIACKX	125	1/0	GI4	167	0	ORCO8			
42		NC	84		NC	126	1/0	GI3	168	0	ORCO9			

CXD8843R (SONY)

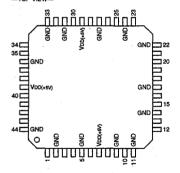
C-MOS GATE ARRAY



											(VDD = +5V)
PIN NO.	1/0	SIGNAL	PIN NO.	МО	SIGNAL	PIN NO.	1/0	SIGNAL	PIN NO.	1/0	SIGNAL
1	0	XPLLH	17	ı	XTRIG	33		SIBLK	49		CLPGATE
2	1	PPLL	18	1	VSTBY	34	0	WDISPLAY	50	0	CLP1
3		PREF	19		cs	35	0	VWINDOW	51	0	CLP2
4	0	EXTH	20	1	SI .	36	0	HWINDOW	52	0	CLP3
5	1	RSTIN	21	_	SCK	37	0	VD	53	0	OBPCLP
6	_	EXTSYNC	22	1	STB	38	0	HD.	54	0	PBLK
7	_	Voo	23	-	GND	39	-	VDD	55	_	GND
8		VDD	24	-	GND	40	-	VDD	.56		GND
9		GND	25	_	Voo	41	_	GND	57	-	Voo
10	_	GND .	26	_	Voo	42	_	GND	58	_	Voo
11		EXTHD	27	0	EXTMODE	43	0	FLD	59	0	SYSCLK
12		EXTVD	28	0	HVMODE	44	0	VBLK	60	0	CKQ -
13	0	HR.	29	1	TEST0	45	0	CMPBLK	61	1	GND
14		HRI	30	1	TEST1	46	0	HBLK	62		CKIN
15	0	VR	31	1	TEST2	47	0	SYNC1	63	_	GND
16	l I	. VRI	32		SBKRVS	48	0	SYNC2	64	0	PCO

CXD8932Q (SONY)

C-MOS GATE ARRAY

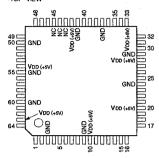


43 42 41	D4 D3	CKO1A CKO1B	23
40	D2 D1 D0	CKO2A CKO2B	26
3 25	CKIA CKIB	AOOH BOOM	6 28
30	HDSLA HOSLB	XTQA . XTQB	15 37
10 32	HDA HDB	XT2A	12
13 35	XT1A XT1B		
		100	,

											(VDD = +5V)
PIN No.	1/0	SIGNAL	PIN No.	Ю	SIGNAL	PIN No.	1/0	SIGNAL	PIN No.	ИΟ	SIGNAL
1	0	CKO1A	12	0	XT2A	23	0	CKO1B	34	=	NC
2		GND	13	_	XT1A	24	ļ	GND	35		XT1B
3		CKIA	14	-	GND	25	1	CKIB	36	-	GND
4	0	CKO2A	15	0	XTOA	26	0	CKO2B	37	0	XT0B
5		GND	16	_	NC	27	_	GND	38	1	D0
6	0	HDOA	17		GND	28	Ο.	HDOB	39	-	Voo
7	-	VDD	18	_	NC	29	_	Voo	40	1	D1
8	1	HDSLA	19	-	NC	30		HDSLB	41	Т	DS
9	-	GND	20	_	NC NC	31	-	GND	42	1	D3
10		HDA	21	_	NC	32	ı	HDB	43	1	D4
11	_	GND	22	_	GND	33	_	GND	44		GND

CXD2437TQ (SONY)

C-MOS TIMING GENERATOR FOR PROGRESSIVE SCAN READOUT SYSTEM CCD IMAGE SENSOR TOP VIEW-



											(VDD = +5V)
PIN No.	1/0	SIGNAL									
1	0	osco	17		СН	33		Voo	49	_	TEST8
2	_	OSCI	18	0	XSG	34	0	XCPOB	50	_	GND
3	-	GND	19	0	XSUB	35	0	XCPDM	51	0	CL
4	Ï.	INT	20	-	VDD	36	0	PBLK	52	0	CLD
5	1	TEST1	21	0	XV3	37	_	GND	53	0	СКО
6	1	STRB	22	0	XV2	38		TEST5	54		Voo
7	1	DCLK	23	0	XV1	39	-	TEST6	55		GND
8	_	DATA	24		GND	40	_	TEST7	56	1	XGRST
9	_	GND	25	0	XH1	41	_	GND	57	1	STDBY
10	_	VDD	26	0	XH2	42	-	Voo	58	1	TRIG
11	-	RM	27	0	RG	43		NC	59		ESG
12	1	TEST2	28	_	Voo	44	_	NC	60	_	GND
13	1	FSE	29	_	GND	45	_	NC	61	1	HD
14		SMDE	30	0	XSHP	46	0	ID	62	1	VD
15	1	TEST3	31	0	XSHD	47	0	WEN	63		VDD
16		TEST4	32	0	XRS	48	0	BUSY	64	1	CKI

INPUT CKI DATA

; CLOCK ; SHUTTER SPEED SETTING DCLK ESG FSE

SHUTTER SPEED SETTING
SHUTTER SPEED SETTING
SHUTTER SPEED SETTING
EXTERNAL TRIGGER SWEEP THROW AWAY SELECT
(H: HIGH SPEED SWEEP THROW AWAY EFFECTIVE/
L: HIGH SPEED SWEEP THROW AWAY INVALID)
HORIZONTAL SYNC
OSCILLATOR INVERTER
INTERNAL OSCILLATOR SELECT
(H: INTERNAL OSCILLATOR SELECT
(H: INTERNAL OSCILLATOR SELECT
STANDBY (H: NORMAL READN: TWICE SPEED READ)
READ TIMING SELECT (H: NORMAL READN: TWICE SPEED READ)
STANDBY (H: NORMAL: INTERNAL CLOCK SUPPLY STOP)
SHUTTER SPEED SETTING
TEST
STANDBY (H: NORMAL: INTERNAL CLOCK SUPPLY STOP)
SHUTTER SPEED SETTING
TEST
EXTERNAL TRIGGER
VERTICAL SYNC
INTERNAL FR RESET (L: RESET)

HD OSCI INT

RM

SMDE

STDBY STRB TEST1 - TEST8

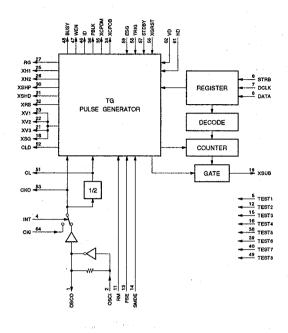
TRIG VD XGRST

OUTPUT

; 3232H CLOCK
; 1616H CLOCK
; 1616H CLOCK
; AD CONVERSION PULSE
; TRIGGER MODE FLAG
; LINE DETECT
; OSCILLATOR INVERTER
; BLANKING CLEANING PULSE
; RESET GATE PULSE
; WRITE ENABLE
; CLAMP PULSE
; CLAMP PULSE
; CCO HORIZONTAL DRIVE CLOCK
SAMPLE HOLD PULSE CKO
CL
CLD
BUSY
ID
OSCO
PBLK
RG
WEN
XCPDM
XCPOB
XH1, XH2

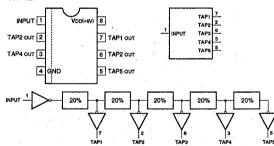
XRS XSG XSHD XSHP ; SAMPLE HOLD PULSE ; CCD SENSOR ELECTRIC READ OUT PULSE ; SAMPLE HOLD PULSE ; SAMPLE HOLD PULSE

XSUB XV1 - XV3 CCD ELECTRIC SWEEP THROW AWAY PULSE CCD VERTICAL DRIVE CLOCK



DS1000Z-50 (DALLAS SEMICONDUCTOR) DS1000Z-75(TE2) (DALLAS SEMICONDUCTOR) DS1000Z-50(TE2)

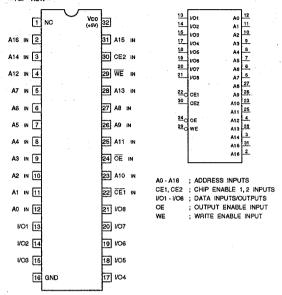
C-MOS DELAY LINE

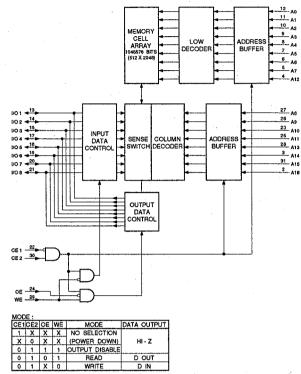


TVDE NO	DELAY TIME (ns)							
TYPE, NO.	TAP1	TAP2	TAP3	TAP4	TAP5			
DS1000M-50	10	20	30	40	50			
DS1000M-60	12	. 24	36	48	60			
DS1000M-75	15	30	45	60	75			
DS1000M-100	20	40	60	. 80	100			
DS1000M-125	25	50	75	100	125			
DS1000M-150	30	60	90	120	150			
DS1000M-175	35	. 70	105	140	175			
DS1000M-200	40	80	120	160	200			
DS1000M-250	50	100	150	200	250			
DS1000M-500	100	200	300	400	500			
DS1000Z-25	5	10	15	20	25			
DS1000Z-100	20	40	60	80	100			

CXK581000AM-70LL (SONY)FLAT PACKAGE CXK581000AM-70LL-TL

C-MOS 1M (131,072 x 8) -BIT STATIC RAM



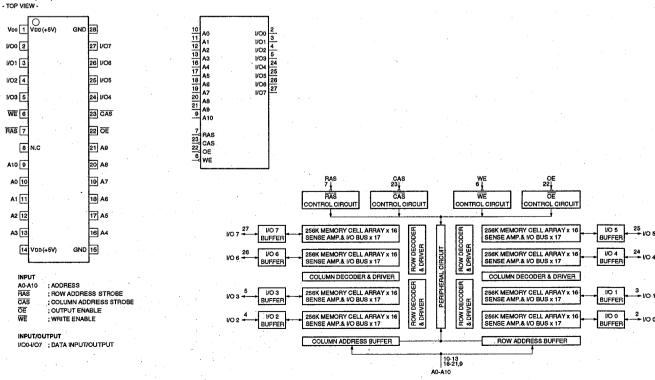


: LOW LEVEL ; HIGH LEVEL ; DON'T CARE

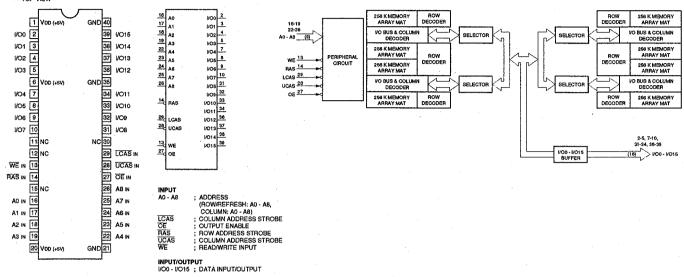
HI-Z ; HIGH IMPEDANCE

HM5117800BJ-7EL (HITACHI)

C-MOS 2,097,152-WORD x 8-BIT DYNAMIC RAM

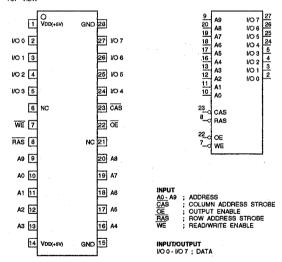


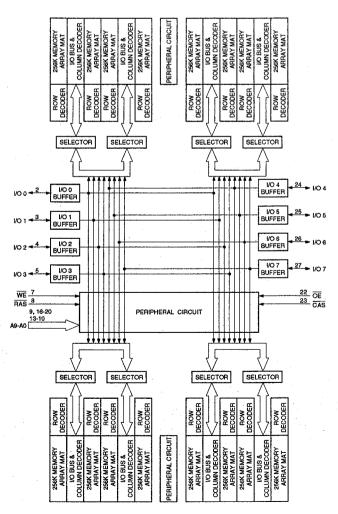
HM514260CJ7-Z (HITACHI)



HM514800CJ7Z (HITACHI) FLAT PACKAGE

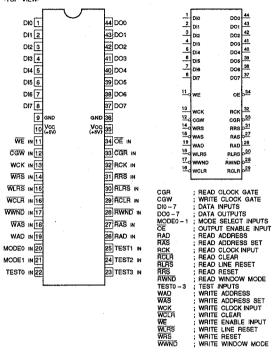
C-MOS 524288-WORD x 8-BIT DYNAMIC RAM

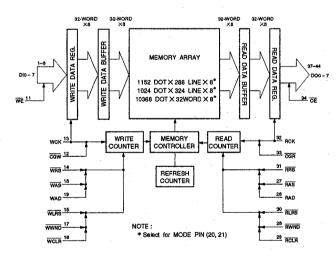




HM530281-20 (HITACHI)FLAT PACKAGE HM530281RTT-20

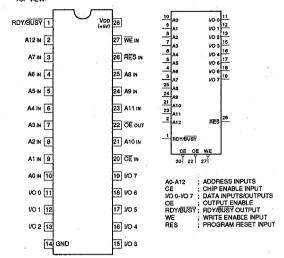
331,776WORDX8-BIT FRAME MEMORY





HN58C66FP-25 (HITACHI)

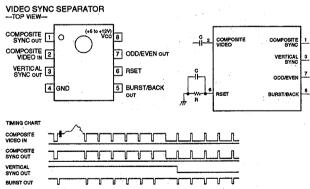
C-MOS 64K (8192 × 8)-BIT EEPROM -TOP VIEW-



CE	Œ	WE	ADY/BUSY	RES	I/O TERMINAL	FUNCTION
0	0	1	HI-Z	1	Dout	READ
1	×	X	HI-Z	×	HI-Z	STANDBY
0	1	0	HI-Z→LOW	1	Din	WRITE
0	1	1	HI-Z	1	HI-ZL	DESELECT
×	×	1	HI-Z	×		WRITE INH
×	0	X	HI-Z	×		WRITE INH
0	0	1	LOW	1	DATA OUT (1/07)	DATA POLLING
×	×	×	HI-Z	0	HI-Z	PROGRAM RESE

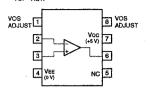
0 ; LOW LEVEL
1 ; HIGH LEVEL
X ; DON'T CARE
HI-Z ; HIGH IMPEDANCE

LM1881M (NS)FLAT PACKAGE



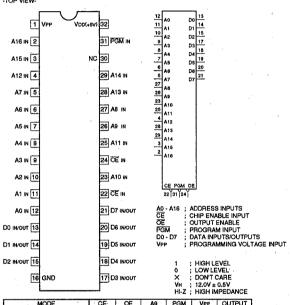
LM6361M (NEC) LM6361M-T1

HIGH SPEED OPERATIONAL AMPLIFIER —TOP VIEW—

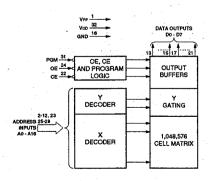


M27C1001-10F1 (SGS)

C-MOS 1M (128k X 8)-BIT UV EPROM -TOP VIEW-

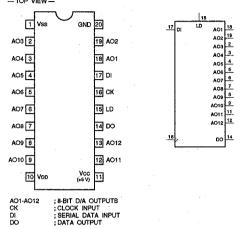


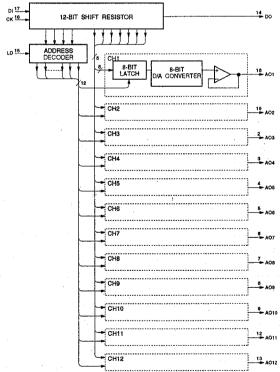
MODE	CE	0E	A9	PGM	VPP	OUTPUT
READ	0	0	×	×	×	Dout
OUTPUT DISABLE	0	1	×	×	×	HI-Z
STANDBY	1	×	×	×	×	HI-Z
PROGRAM	0	1	×	0	VPP	Din
PROGRAM VERIFY	· 0 ·	0	×	1	Vep	Dout
PROGRAM INHIBIT	.1	×	×	×	VPP	HI-Z
ELECTRONIC SIGNATURE	0	0	Vн	1	Vpp	CODE



M62352GP (MITSUBISHI)FLAT PACKAGE M62352GP-75ED

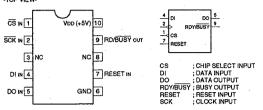
C-MOS 8-BITx12 CHANNEL D/A CONVERTER (WITH BUFFER OPERATIONAL AMPLIFIER)

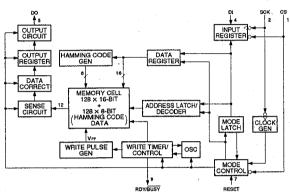




M6M80021FP (MITSUBISHI)FLAT PACKAGE M6M80021FP-T3

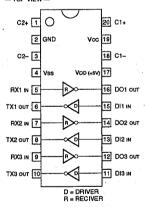
C-MOS 2K (128 × 16) BIT ERASABLE PROM -TOP VIEW-





MC145407F (MOTOROLA)FLAT PACKAGE MC145407F-ML2

C-MOS RS-232C DRIVER/RECEIVER -- TOP VIEW --



INPUT DI1 - DI3 RX1 - RX3

; RS-232C DRIVER INPUTS ; RECEIVER INPUTS

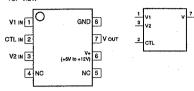
OUTPUT DO1 - DO3 TX1 - TX3

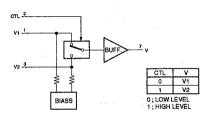
; RS-232C DRIVER OUTPUTS ; TRANSCEIVER OUTPUTS

OTHER
C1+, C1-, C2+, C2-; EXTERNAL CAPACITORS
VDD ; POSITIVE SUPPLY
Vss ; NEGATIVE SUPPLY

NJM2233BM (JRC)FLAT PACKAGE NJM2233BM(TE2)

2-INPUT VIDEO SIGNAL SWITCH -TOP VIEW-





NJM2903M (JRC)FLAT PACKAGE NJM2903M-TE2

DUAL VOLTAGE COMPARATORS



OP293-S (ANALOG DEVICES)

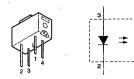
DUAL OPERATIONAL AMPLIFIERS (SINGLE-SUPPLY TYPE) -TOP VIEW-

1	$\frac{O}{\lambda}$	Vcc	8
2	 	Ţ	7
3		4	6
4	VEE		5

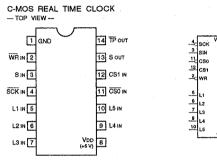
Vcc - VEE
+5 to +36V
+2.5 to +36V
+3 to +24V
+4 to +32V
+3 to +10V
+4 to +14V
+4 to +32V
+3 to +16V
+3 to +10V
+3 to +44V
+3 to +36V

RPI-1020 (ROHM)

OPTICAL DETECTION SENSOR THAT HAS DETECTION ANGLE



RTC-4553B (EPSON) RTC-4553B-L2

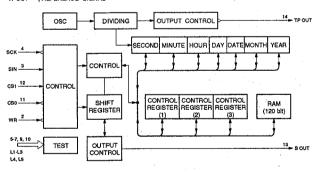


INPUT CS0 CS1

; CHIP SELECT (L: ACCESS ENABLE, H: SOUT HIGH Z); POWER DOWN DETECTION; TEST IN; SERIAL SYNC SIGNAL; SERIAL ADDRESS/DATA; WRITING SELECT (L: WRITING, H: READING)

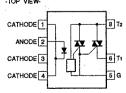
OUTPUT

SOUT SERIAL ADDRESS/DATA



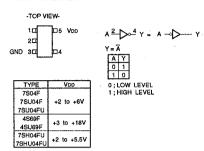
S16MD01 (SHARP)

SOLID STATE RELAY



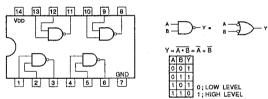
SC7S04F (MOTOROLA)CHIP PACKAGE TC7S04F(TE85R)

C-MOS INVERTER



SN74HC00ANS (TI)FLAT PACKAGE TC74VHC00F (TOSHIBA)FLAT PACKAGE SN74HC00ANS-E05 TC74VHC00F(EL)

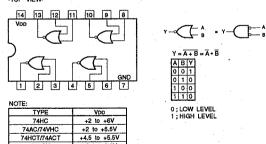
C-MOS QUAD 2-INPUT NAND GATES



TYPE	VDD
74AC/74VHC	+2 to +5.5V
74ACT/74HCT/74VHCT	+4.5 to +5.5V
LCX	+2 to +3.6V
OTHER TYPES	+2 to +6V

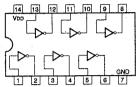
SN74HC02ANS (TI)FLAT PACKAGE TC74VHC02F (TOSHIBA)FLAT PACKAGE SN74HC02ANS-E05 TC74VHC02F(EL)

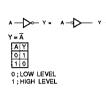
C-MOS QUAD 2-INPUT NOR GATES



SN74HC04ANS (TI)FLAT PACKAGE SN74HCU04ANS-E20 (TI)FLAT PACKAGE TC74AC04F-EL (TOSHIBA)FLAT PACKAGE TC74VHC04F (TOSHIBA)FLAT PACKAGE SN74HC04ANS-E05 SN74HCU04ANS-E05 74AC04SJ-T5R TC74VHC04F(EL)

C-MOS HEX INVERTERS

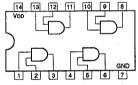


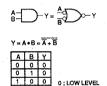


NOTE:	
TYPE	VDD
74AC/74VHC/74VHCT	+2 to +5.5V
74ACT/74HCT	+4.5 to +5.5V
74LCX	+2 to +3.6V
OTHER TYPE	+2 to +6V

SN74HC08ANS (TI)FLAT PACKAGE TC74VHC08F(EL) (TOSHIBA) SN74HC08ANS-E05

C-MOS QUAD 2-INPUT AND GATE

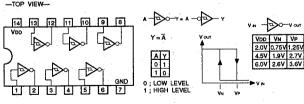




	Vop
TYPE	
74AC	+2 to +5.5V
40H	+2 to +8V
74ACT/74HCT/74VHCT	+4.5 to +5.5V
74LCX	+2 to +3.6V
OTHER TYPES	+2 to +6V

SN74HC14ANS (TI)FLAT PACKAGE SN74HC14ANS-E05

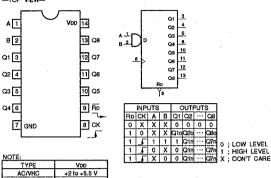
C-MOS HEX SCHMITT TRIGGER INVERTERS --TOP VIEW--

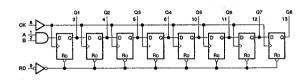


+2V to +5.5V
+2V to +6V

SN74HC164ANS (TI)FLAT PACKAGE SN74HC164ANS-E05

C-MOS 8-BIT SERIAL-IN/PARALLEL-OUT SHIFT REGISTER --TOP VIEW-

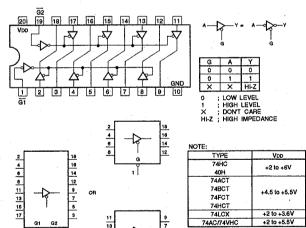




SN74HC244ANS (TI)FLAT PACKAGE TC74VHC244F (TOSHIBA)FLAT PACKAGE SN74HC244ANS-E05 TC74VHC244F(EL)

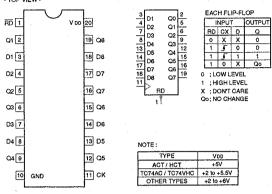
+2 to +6 V

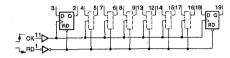
C-MOS BUS BUFFER WITH 3-STATE OUTPUTS -TOP VIEW-



SN74HC273ANS-E05 (TI)FLAT PACKAGE

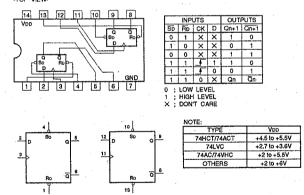
C-MOS OCTAL D-TYPE FLIP-FLOPS WITH RESET





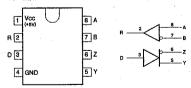
SN74HC74ANS (TI)FLAT PACKAGE TC74VHC74F(EL) SN74HC74ANS-E05

C-MOS DUAL D-TYPE FLIP-FLOPS WITH DIRECT SET/RESET



SN75179BPS (TI)FLAT PACKAGE SN75179BPS-E05

DIFFERENTIAL BUS DRIVER/RECEIVER



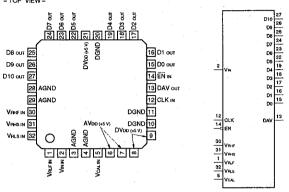
-DRIVER	-	•	
INPUT	OUTP	UTS	
ם	Y	Z	
Н	н	L	
L	L	н	

ECEIVER-	
DIFFERENTIAL INPUTS A-B	OUTPUTS R
Vip≥ 0.2V	н
-0.2V < VID < 0.2V	. ?
ViD≤-0.2V	L

H = HIGH LEVEL L = LOW LEVEL ? = INDETERMINATE

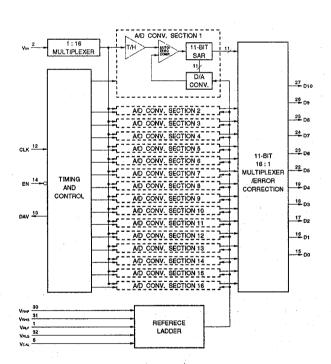
SPT7855SCT (SIGNAL PROCESSING TECHNOLOGIES)FLAT PACKAGE

C-MOS 10-BIT A/D CONVERTER - TOP VIEW -



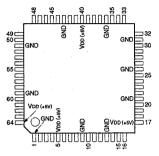
OUTPUT
D0-D9 ; DIGITAL DATA
D10 ; OVERRANGE
DAV ; DATA VALID
OTHER

OTHER
AGND ; ANALOG GROUND
AV00 ; ANALOG POWER SUPPLY
DGND ; DIGITAL GROUND
DV00 ; DIGITAL POWER SUPPLY



SYM53CF92A-64QFP (SYMBIOS LOGIC)

C-MOS SCSI PROTOCOL CONTROLLER



														(VDU = +5 V/
PIN No.	1/0	SIGNAL	PIN No.	1/0	SIGNAL	PIN No.	1/0	SIGNAL	PIN No.	1/0	SIGNAL	PIN No.	1/0	SIGNAL
1	-	GND	14	I/O	DB6	27	1/0	SD7	40	\equiv	Vod	53	1	A3-ALE
2	0	DREQ	15	1/0	DB7	28	0	SDP	41	1	MODE	54	1	TESTIN
3		DACK	16	5	DBP	29	1/0	ATN	42	0	INT	55	1/0	PAD0
4		DBWR	17	-	VDD	30	1/0	BSY	43	_	GND	56	1/0	PAD1
5	-	VDD	18	1/0	SD0	31	_	GND	44		RESET	57	1/0	PAD2
6	1/0	DB0	19	5	SD1	32	1/0	ACK	45	1	WR	58	1/0	PAD3
7	1/0	DB1	20	5	SD2	33	1/0	RST	46	1	RD	59	_	GND
8	1/0	DB2	21	-	GND	34	1/0	MSG	47	1	ČŠ	60	1/0	PAD4
9	-	GND	22	5	SD3	35	1/0	SEL	48	L	CLK	61	1/0	PAD5
10	1/0	DB3	23	9	SD4	36		GND	49	1	A0	62	1/0	PAD6
11	1/0	DB4	24	1/0	SD5	37	1/0	ČD	50	ī	A1	63	1/0	PAD7
12	1/0	DB5	25	1/0	SD6	38	1/0	REQ	51	_	GND	64	E	VDD
13	-	GND	26	I —	GND	39	1/0	ĪŌ	52		A2-DBRD			1

(Van - .6V/)

INPUT

ADDRESS ADDRESS/READ SIGNAL FOR THE DMA DATA BUS ADDRESS

A0, A1 A2-DBRD A3-ALE CLK CS DACK DBWR MODE RD CLOCK

: CLOCK
: CHIP SELECT
: DMA ACKNOWLEDGE
: DMA WRITE SIGNAL
: MODE SELECT (PAD BUS/ADDRESS CONTROL BUS)
: REGISTER READ SIGNAL
: CHIP RESET
: TEST
: REGISTER WRITE SIGNAL

RESET TESTIN WR

OUTPUT

; DMA REQUEST SIGNAL ; OPEN-DRAIN INTERRUPT SIGNAL

INPUT/OUTPUT
ACK : SC
ATN : OF
ESY : OF
CD : SC
DB0-DB7 : DF
DBP : OF
CO : SC
MSG : SC
MSG : SC
PADOLPADY : PF PUT

; SCSI I/O
; OPEN-DRAIN CUTPUT, SCHMITT TRIGGER INPUT
; OPEN-DRAIN SCSI I/O
; SCSI PHASE SIGNAL
; DMA DATA BUS
; ODD PARITY FOR DBO-DB7
; SCSI PHASE SIGNAL
; SCSI PHASE SIGNAL
; PROCESSOR ADDRESS-DATA BUS
; SCSI II AS
; SCSI I/O
; OPEN-DRAIN SCSI I/O
; SCSI DATA BUS

PADO-PAD7 REQ RST

SDO-SD7 SDP SEL

SCSI DATA BUS SCSI DATA/PARITY OUTPUT BUS OPEN-DRAIN SCSI I/O

PARITY GENERATOR AND CHECKER STEERING LOGIC 24-bit TRANSFER COUNT 24-bit TRANSFER COUNTER 53-58, PADO- 60-63 PAD7 -----STATUS REGISTER DESTINATION ID SEQUENCER INTERRUPT REGISTER SCSI CONTROL SEQUENCE STEP SYNC PERIOD TIME SEQUENCE MODE CLK 48

WR 45

WR 45

CS 47

DACK 3

A3-ALE 53

A2-DBRD 52

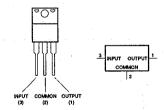
A1 49

DBWR 41 READ/WRITE MODE 41
RESET 44
TESTIN 54

TA7805S (TOSHIBA)+5V(1 A) AN7805

FIFO(16X9-bit)

POSITIVE VOLTAGE REGULATOR --FRONT VIEW-



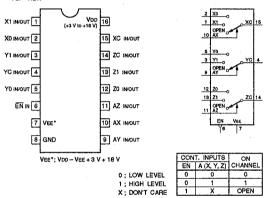
TA79005S (TOSHIBA)--5V

NEGATIVE VOLTAGE REGULATOR (500mA)



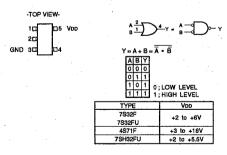
TC4053BFS (TOSHIBA)FLAT PACKAGE TC4053BFS-EL

C-MOS TRIPLE 2-CHANNEL ANALOG MULTIPLEXERS/DEMULTIPLEXERS - TOP VIEW -



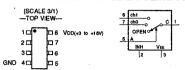
TC4S71F (TOSHIBA)CHIP PACKAGE TC4S71F(TE85R)

C-MOS 2-INPUT OR GATE



TC4W53FU (TOSHIBA)CHIP PACKAGE TC4W53FU(TE12R)

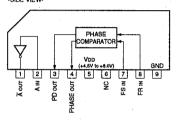
C-MOS 2-CHANNEL MULTIPLEXER / DEMULTIPLEXER

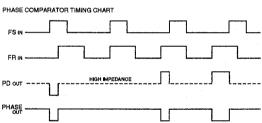


	CONT.	INPUT	ON
	INH	Α	CHANNEL
0;LOW LEVEL	0	0	ch0
1 : HIGH LEVEL	0	1	dh1
X: DON'T CARE	1	х	OPEN

TC5081AP (TOSHIBA)

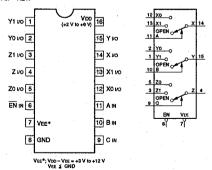
C-MOS PHASE COMPARATOR -SIDE VIEW-





TC74HC4053AFS (TOSHIBA)FLAT PACKAGE TC74HC4053AFS-EL

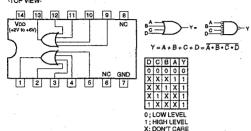
C-MOS TRIPLE 2-CHANNEL ANALOG MULTIPLEXER/DEMULTIPLEXER -TOP VIEW-



							1
CO	CONTROL INPUTS						
	. 8	ELEC	T	ON	CHAN	INEL.	
EN	С	В	A	l			
0	0	0	0	ZO	YO	X0	
0	٥	0	1	ZO	YO	X1	
0	0	1	0	ZO	Y1	X0	
0	0	1	1	ZO	Υ1	X1	
0	1	0	0	Z1	Yo	Xo	
0	1	0	1	Z1	Y0	X1	
0	1	1	0	Z1	Y1	XO	0; LOW LEVEL
0	1	1	1	Z1	Y1	X1	1 ; HIGH LEVEL
	×	X	×		OPEN		X; DON'T CARE

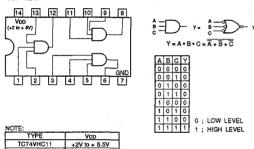
TC74HC4072AF (TOSHIBA)FLAT PACKAGE

C-MOS 4-INPUT OR GATE



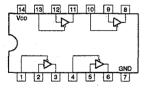
TC74VHC11F (TOSHIBA)FLAT PACKAGE TC74VHC11F(EL)

C-MOS 3-INPUT POSITIVE-AND GATE



TC74VHC125F (TOSHIBA)FLAT PACKAGE TC74VHC125F(EL)

C-MOS BUS BUFFER GATES WITH 3-STATE OUTPUT -TOP VIEW-

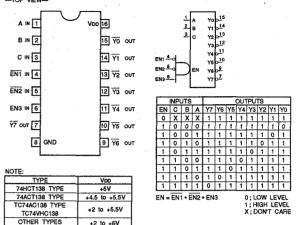




NOTE:	
TYPE	VDD
74AC/ 74VHC	+2 to +5.5V
74ACT/74HCT	+4.5 to +5.5V
74LCX	+2 to +3.6V
74LVT/74LVC	+2.7 to +3.6V
OTHER TYPES	+2 to +6V

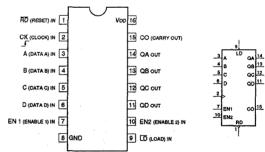
TC74VHC138F(EL) (TOSHIBA)

C-MOS 3-TO-8 LINE DECODER / DEMULTIPLEXER



TC74VHC161F(EL) (TOSHIBA)

C-MOS SYNCHRONOUS PRESETTABLE 4-BIT BINARY COUNTER-TOP VIEW-

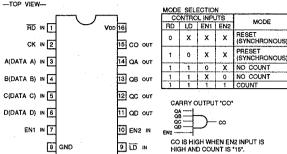


		CTION		<u> </u>		COUNTS	E CACLE	OUT	UTS	
RD	LD	EN1	EN2	MODE		COUNT	QD	QC	QB	QA
0	х	х	X	RESET (ASYNCHRONOUS)	-	0	0	0	0	0
_				PRESET		2	0	0	+	-
1	0	X	×	(SYNCHRONOUS)		3	0	0	1	1
1	1	0	Х	NO COUNT		4	0	1	0	0
1	1	Х	0	NO COUNT		5	0	1	0	1
1	1	1	1	COUNT		6	0	1	1	a
	V LEV					7	0	1.	1	.1
	HLE					8	1	0	0	0
;00	N'T C	4HE				.9	1	0	0	1
						10	1	0	-	٥
	ARR	Y OUT	PUT *	or.		11	1	0	1	1
	QA					12	1	1	0	0
	QB	-				13	1	1	0	1
	QC	1)		•		14	1	1	1	.0
EN:		\boldsymbol{T}				15	1	1	1	1

NOTE:	1
TYPE	Voo
74ACT	+5 V
TC40H	+2 to +8 V
OTHERS	+2 to +6 V

TC74VHC163F (TOSHIBA)FLAT PACKAGE TC74VHC163F(EL)

C-MOS PRESETTABLE SYNCHRONOUS 4-BIT BINARY COUNTER
--TOP VIEW-



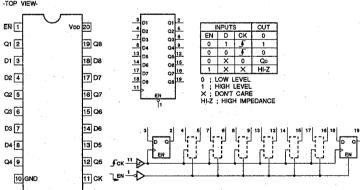
NOTE:		· .
TYPE		VDD
HC		+2 to +6V
AC/VHC		+2 to +5.5V
HCT/ACT/FCT		+5V
	3 A B 5 C 6 D 2 2 EN1 10 EN2	

COUNT SEQUENCE									
COUNT		OUTPUTS							
COUNT	QD	CC	QB	QA ·					
0	0	0	0	0					
1	0	0	0	1					
2	0	0	1	0					
3	0	- 0	1	1					
4	0	1	0	0					
5	0	1	0	1					
6	0	1	1	. 0					
7	0	1	1	1					
8	1	0	0	0					
9	1	0	0	1					
10	1	0	1	0					
11	1	0	1	1					
12	1	1	0	0					
13	1	1	0	1					
14	1	1	1	0					
15	1	1	1	1					

C-MOS 3-STATE OCTAL D-TYPE FLIP-FLOP

TC74VHC374F(EL)

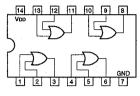
TC74VHC374F (TOSHIBA)FLAT PACKAGE



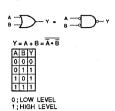
TYPE	Vod
74AC/74HC	+2 to +6V
4ACT/74BCT/74FCT	+5V
/74HCT	+54
74VHC	+2 to +5.5V

TC74VHC32F(EL) (TOSHIBA)

C-MOS QUAD 2-INPUT OR GATES

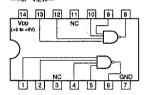


TYPE	VDD
74AC/74VHC	+2 to +5.5V
74HC	+2 to +6V
74HCT	+4.5 to +5.5V

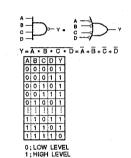


TC74VHC20F(EL) (TOSHIBA)FLAT PACKAGE

C-MOS 4-INPUT POSITIVE-NAND GATE

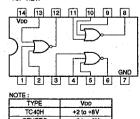


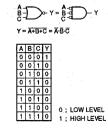
TE:	
TYPE	VDD
AC/VHC	+2 to +5.5\
HC	+2 to +6V



TC74VHC27F(EL) (TOSHIBA)

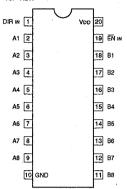
C-MOS 3-LINE POSITIVE-NOR GATE -TOP VIEW-

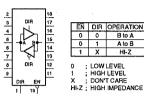




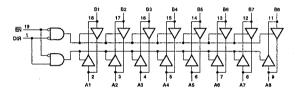
TC74VHCT245F(EL) (TOSHIBA)FLAT PACKAGE

C-MOS BILATERAL BUS TRANSCEIVERS WITH 3-STATE OUTPUTS -TOP VIEW-





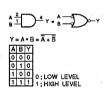
NOTE:	
TYPE	VDO
74HC	+2 to +6V
74A8T	
74ACT	451-551
74BCT	+4.5 to +5.5V
74HCT	
74AC	T
74VHC	+2 to +5.5V
74LCX	+2 to +3.6V
74LVT	+2.7 to +3.6V



TC7S08F (TOSHIBA)CHIP PACKAGE TC7S08F(TE85R)

C-MOS 2-INPUT AND GATE





TYPE	VDD				
7S08F	+2 to +6V				
7S08FU	+2 to +6V				
4581F	+3 to +18V				
14S81F	+310+184				
7SH08FU					
7SH08F	+2 to +5.5V				

TC7W08F (TOSHIBA)CHIP PACKAGE TC7W08F(TE12R)

C-MOS 2-INPUT AND GATE



1	0	ō	0	
	0	1	0	
	-	0	0	0:LOW LEVEL
	-	1	1	0; LOW LEVEL 1; HIGH LEVEL

TL062CPW (TI)FLAT PACKAGE TL082M (TI)FLAT PACKAGE TL062CPW-EO5 TL082CPS-E20

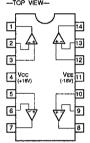
DUAL OPERATIONAL AMPLIFIERS (DUAL-SUPPLY TYPE) -TOP VIEW-



TYPE	V cc	VEE
062/072/082/4556A/		
M5218/BA15218/	+2 to +16V	-2 to -16V
33178/34182 TYPES		
4580 TYPE	+2 to +18V	-2 to -18V
5532 TYPE	+3 to +20V	-3 to -20V
CXA1297 TYPE	+5 to +12V	-5 to -12V
M5219/M5220 TYPES	+5 to +22.5V	-5 to -22.5V
NJM2100 TYPE	+1 to +3.5V	-1 to -3.5V
OP-297 TYPE	+2 to +20V	-2 to -20V
OTHERS	+5 to +16V	-5 to -16V

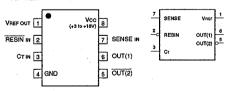
TL064CPW (TI)FLAT PACKAGE TL064CPW-E05

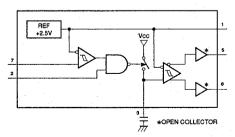
OPERATIONAL AMPLIFIER (J FET INPUT) —TOP VIEW—



TL7705ACPS (TI)FLAT PACKAGE TL7705CPS-B (TI)FLAT PACKAGE TL7705ACPS-E05 TL7705CPS-B-E20

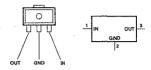
POWER VOLTAGE SUPERVISOR



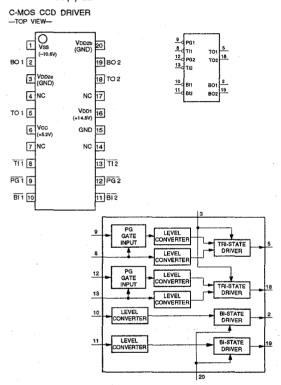


UPC78L08T-E1 (NEC)+8V

POSITIVE VOLTAGE REGULATOR -TOP VIEW-

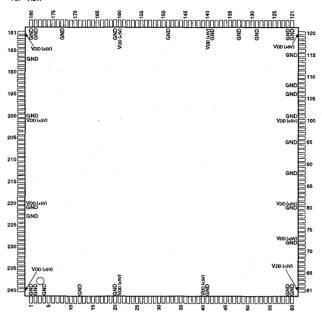


UPD16502GS(1) (NEC)FLAT PACKAGE UPD16502GS(1)-E2



UPD65808GN-056-LMU (NEC)

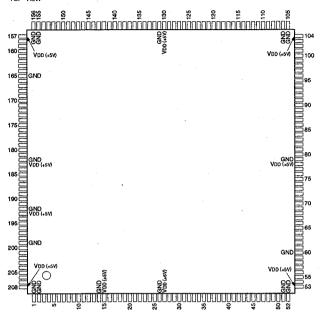
C-MOS GATE ARRAY



PIN	1/0	SIGNAL	PIN	1/0	SIGNAL	PIN	1/0	SIGNAL	PIN	1/0	SIGNAL	PIN	1/0	SIGNAL
No.			No.			No.			No,	<u> </u>		No.		
1		GND	49	Ш	MB7	97	1/0	ID1	145	0	AD9	193	1	XLUTCS
2	-	GND	50	0	XOEO	98	1/0	ID2	146	1/0	DB1	194	1.1	XWE
3	1/0	XACK2I	51	0	XCGRO	99	1/0	ID3	147	1/0	DB0	195	1	XRE
4	_	GND	52	0	XOEE	100	-	Voo	148	1/0	DG7	196	0	XWAT
5	1/0	XRQ2I	53	0	XCGRE	101		GND	149	_	GND	197	0	XINT
6	1/0	B217	54	0	XRRS	102	1/0	ID4	150	1/0	DG6	198	1/0	CPD3
7_	1/0	B216	55	0	XRLRS	103	1/0	1D5	151	1/0	DG5	199	1/0	CPD2
8	1/0	B215	56	1/0	G216	104	1/0	ID6	152	1/0	DG4	200	_	GND
9	0	MROO	57	1/0	G215	105	1/0	ID7	153	11	CATI	201	-	Voo
10	0	MRO	58	1/0	G214	106		GND	154	1	CA10	202	1/0	CPD1
11	0	MRO2	59	ı	GND	107	T	REGO	155	1	CA9	203	1/0	CPD0
12	_	GND	60	_	GND	108	<u> </u>	GND	156	П.	CA8	204		B117
13	0	MRO3	61	_	Voo	109	0	XACKO	157	П	CA7	205	-	B116
14	0	MRO4	62	1/0	G2i3	110	0	XDMWR	158	ı	CA6	206	1.	B115
15	0	MRO5	63	1/0	G212	111	0	XDMRE	159	1	CA5	207	ī	B114
16	vo.	B2I4	64	1/0	G211	112	1/0	DB7	160	_	Voo	208	1	B1 3
17	1/0	B213	65	0	RAD	113	1/0	DB6	161	_	GND	209		8112
18	1/0	B2I2	66	0	XRWND	114	1/0	DB5	162	1	CLK	210	$\overline{}$	8111
19	0	RCKO	67	0	XRASV	115	_	GND	163	1	TST	211		B110
20	_	GND	68	ō	XRCLR	116	1	XRHD	164	ΙĖ	XRST	212	Ť	G117
21		VDD	69	ō	MGO4	117	i	XRVD	165	ΙĖ	CA4	213	i	G116
22	1/0	B2I1	70	ŏ	MGO5	118	i	RFLD	166	Ϊ́Τ	CA3	214	÷	G115
23	1/0	B210	71	ō	MGO6	119	Ť	RCK!	167	ΙŤ	CA2	215	i	G114
24	10	G217	72	Ÿ	GND	120		VDD	168	1	CAI	216	÷	G113
25	1	MR7	73		VDD	121	-	GND	169	1	CAO	217	H	G112
26	i i	MG0	74	0	MGO7	122	-	GND	170	1/0	DG3	218	H	G1I1
27	i	MG1	75	ö	MBO0	123	0	XCAS	171	1/0	DG2	219	H	G1IO
28	H	MG2	76	0	MBO1	124	0	XWER	172	1/0	DG1	220		VDD
29	+	MG2	77	0	G210	125	0	XWEG	-		GND	_	-	GND
30	+	MG4	78	1/0	R217		-	XWEB	173	=		221	=	
_	H		-			126	0		174	1/0	DG0	222	1	XRQ1I
31		MG5	79	1/0	R216	127	ŏ	XOE	175	1/0	DR7	223	=	GND
32	4	MG6	80		GND	128	0	XRAS	176	1/0	DR6	224	ċ	XACK11
33	1	MG7	81	=	VDD	129	_	GND	177	1/0	CPD7	225	1	- R117
34	-	MB0	82	VO	R2I5	130	1/0	DB4	178	1/0	CPD6	226		R116
35	1	MB1	83	1/0	R214	131	1/0	DB3	179	_	GND	227	\equiv	R115
36		MB2	84	10	R213	132	1/0	DB2	180	=	GND	228	Ш	R114
37	0	MRO6	85	0	MBO2	133		GND	181	-	VDD	229	ш	R1I3
38	0	MRO7	86	0	MBO3	134	0	AD0	182		CPD5	230	ш	R1I2
39	0	MGC0	87	0	MBO4	135	0	AD1	183	1/0	CPD4	231	-	Rill
40	-1	VDD	88		GND	136	0	AD2	184	1/0	DR5	232	_	R110
41	_	GND	89	0	MBO5	137	0	AD3	185	1/0	DR4	233		MRO
42	Ó	MGO1	90	0	MBO6	138	0	AD4	186	1/0	DR3	234	1	MR1
43	0	MGO2	91	0	MBO7	139		GND	187		GND	235	-	MR2
44	0	MGO3	92	9	R212	140	_	VDD	188	1/0	DR2	236	-	MR3
45	1	мва	93	10	R2I1	141	0	AD5	189	1/0	DR1	237		MR4
46	П	MB4	94	10	R210	142	0	AD6	190	1/0	DR0	238	1	MR5
47		MB5	95	_	GND	143	0	AD7	191	1	xcs	239	T	MR6
48		MB6	96	1/0	1D0	144	0	AD8	192	1	XLWCS	240		VDD

UPD65810GD-047-LML (NEC)



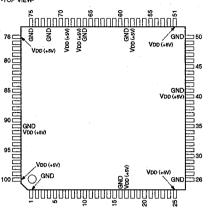


(VDD = +5V)

PIN No.	1/0	SIGNAL	PIN No.	1/0	SIGNAL	PIN No.	I/O	SIGNAL	PIN No.	0	SIGNAL	PIN No.	1/0	SIGNAL
1	=	GND	43	0	XCGWE4	85		ADR4	127	_	XLUTCS	169	0	DAD2
2		GND	44	0	XWCLR	86		ADR5	128	_	XFWCS	170	0	DAD1
3	1/0	DR5	45	0	MDG6	87		ADR6	129	-	XCS	171	0	DAD0
4	1/0	DR4	46	0	MDG5	88	i i	ADR7	130	-	VDD	172	1	CA12
5	1/0	DR3	47	0	MDG4	89		ADRB	131	-	GND	173	_	- CA11
6	1/0	CPD1	48	1/0	HVR6	90		ADR9	132	1/0	XREQ	174		CA10
7	1/0	CPD0	49	1/0	HVR7	91		ADG0	133	-	TST	175	_	CA9
8	0	MDR4	50	1/0	HVG0	92	1	ADG1	134	1/0	XACK	176		CA8
9	0	MDR3	51	_	GND	93	1	ADG2	135	1	XRST	177		CA7
10	0	MDR2	52	_	GND	94	1	ADG3	136	(OI	HVB5	178		CA6
11	1/0	HVR0	53		VDD	95	T	ADG4	137	1/0	HVB6	179	1	CA5
12	1/0	HVR1	54	1/0	HVG1	96		ADG5	138	1/0	HVB7	180	T	CA4
13	1/0	HVR2	55	NO	HVG2	97	1	ADG6	139	0	DIR	181	1	CA3
14	_	GND	56	1/0	HVG3	98	1	ADG7	140	0	XWEB	182	-	GND
15		VDD	57	0	MDG3	99	1	ADG8	141	0	XWEG	183		Vpp
16	1/0	HVR3	58	0	MDG2	100	1	ADG9	142	0	XWER	184	- 1	CA2
17	1/0	HVR4	59	0	MDG1	101	0	MDB6	143	0	XOE	185	1	CA1
18	1/0	HVR5	60	-	GND	102	0	MDB4	144	0	XCAS	186	. 1	CA0
19	10	DR2	61	0	MDG0	103	0	MDB3	145	0	XRAS	187	1/0	CPD7
20	1/0	DR1	62	0	MDB7	104	Ξ	VDD	146	0	XRFC	188	1/0	CPD6
21	1/0	DRO	63	0	MDB6	105		GND	147	0	XRTC	189	1/0	DG6
22	0	MDR1	64	0	XWRS	106	=	GND	148	1/0	DB7	190	1/0	DG5
23	0	MDR0	65.	0	XWLRS	107	0	MDB2	149	1/0	DB6	191	1/0	DG4
24	0	MDG7	66	0	XWWND	108	0	MDB1	150	1/0	085	192	=	GND
25	0	WCK	67	0	XWAS	109	0	MDB0	151	0	DAD10	193	=	Voo
26	-	GND	68	0	WAD	110	1/0	HVB2	152	0	DAD9	194	1/0	DG3
27	_	Voo	.69	1/0	HVG4	111	1/0	HVB3	153	0	DAD8	195	1/0	DG2
28	0	XWEO1	70	1/0	HVG5	112	1/0	HVB4	154	0	DAD7	196	1/0	DG1
29	0	XCGW01	71	1/0	HVG6	113	T	ADB0	155	-	GND	197	1/0	CPD5
30	0	XWEE1	72	ī	ADR0	114	1	ADB1	156		GND	198	1/0	CPD4
31	0	XCGWE1	73	T	ADR1	115	1	ADB2	157	-	VDD	199	_	GND
32	0	XWEO2	74	0	CLP	116	1	ADB3	158	0	DAD6	200	1/0	CPD3
33	0	XCGW02	75	T	XHD	117	1	ADB4	159	0	DAD5	201	1/0	CPD2
34	o	XWEE2	76	I	WEN	118	ı	ADB5	160	0.	DAD4	202	1/0	DG0
35	o	XCGWE2	77	1	ADCK	119	- 1	ADB6	161	0	DAD3	203	1/0	DR7
36	0	XWEO3	78	-	VDD	120	T	ADB7	162	1/0	DB4	204	1/0	DR6
37	0	XCGW03	79	T-	GND	121	ī	ADB8	163	1/0	DB3	205	0	MDR7
38	0	XWEE3	80	1/0	HVG7	122	1	ADB9	164	1/0	DB2	206	0	MDR6
39	0	XCGWE3	81	1/0	HVB0	123	0	XINT	165		GND	207	0	MDR5
40	0	XWEO4	82	1/0	HVB1	124	0	XWAT	166	1/0	DB1	208	-	VDD
41	0	XCGWO4	83	T	ADR2	125	1	XRE	167	1/0	DB0	Γ_		
	-		1	_	1000	400		10.00	400	1/0	507	1	_	

UPD70741GC-25-7EA (NEC)

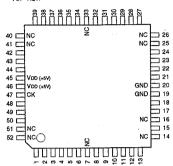
C-MOS GATE ARRAY



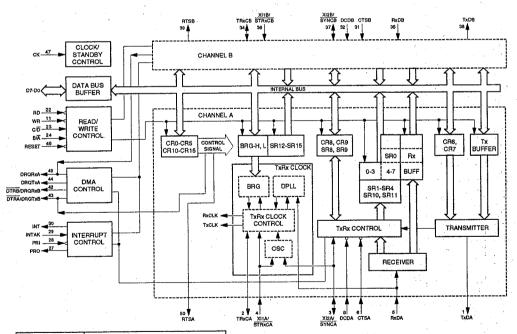
				,			•	N 0	4					$(VDD \approx +5V)$
PIN No.	1/0	SIGNAL	PIN No.	1/0	SIGNAL	PIN No.	1/0	SIGNAL	PIN No.	1/0	SIGNAL	PIN No.	1/0	SIGNAL
1	-	GND	21	I/O	NTP02/TC01	41		GND	61	0	A7	81	1	READY
2	0	IORD	22	J.	INTP01	42	1/0	D8	62	0	A8	82	0	CS0/REFRQ
3	0	IOWR	23	1/0	NTP00/TC00	43	1/0	D9	63	0	A9	83	0	CS1
4	1	NMI	24	ī	INTP13/TI	44	1/0	D10	64	0	A10	84	0	ČS2
5	T	HLDRQ	25	_	Vop	45	1/0	D11	65	0	A11	85	0	CS3
6	0	HLDAK	26	_	GND	46	1/0	D12	66	_	GND	86	0	A12
7	1/0	RXD/P09/TC	27	1	INTP12	47	1/0	D13	67	=	Voo	87	0	A13
8	1/0	TXDP08UBE	28	T	INTP11	48	1/0	D14	68	0	CLKOUT	88	0	A14
9	1/0	SCLK/P07	29		INTP10	49	1/0	D15	69	-	VDD	89	0	A15
10	1/0	SO/P06	30	I	RESET	50		GND	70	1	X2	90	0	A16
11	1/0	SI/P05	31		IC	51	-	Voo	71	I	X 1	91	ı	GND
12	1/0	DACK1/P04	32	1/0	D0	52	0	A0	72	-	GND	92	H	Voo
13	1/0	DREQ1/P03	33	NO	D1	53	0	A1	73	0	UCAS	93	0	A17
14	1/0	DACKO/P02	34	1/0	D2	54	0	A2	74	0	LCAS	94	0	A18
15	1/0	DREQ0/P01	35	1/0	D3	55	0	A3	75	_	GND	95	0	A19
16	-	GND	36	1/0	D4	56	0	A4	76	_	VDD	96	0	A20
17	Γ=	VDD	37	1/0	D5	57	0	A5	77	0	RAS	97	0	A21
18	VO.	TCLR/P00	38	1/0	D6	58	-	Voo	78	0	UMWR	98	o	A22
19	0	BLOCK	39	1/0	D7	59	_	GND	79	0	CWWH/WE	99	0	A23
20	1	INTPOS	40	_	Von	60	0	A6	80	0	MRD	100	1	Vpp

UPD72001GC-11-3B6 (NEC)

C-MOS ADVANCED MULTI-PROTOCOL SERIAL CONTROLLER —TOP VIEW—



								(VDD = +5V)
PIN NO.	1/0	SIGNAL	PIN NO.	1/0	SIGNAL	PIN NO.	I/O	SIGNAL
1	0	TxDA	19		GND	37	1/0	X12B/SYNCB
2	1/0	TRxCA	20		GND	38	0	TxDB
3	1/0	XI2A/SYNCA	21	1	WR ·	39	0	RTSB
4	1	XI1A/STRXCA	22	1	RD	40	<u> </u>	NC
5		RxDA	23	1	C/D	41		NC
6		CTSA	24		B/Ā	42	0	DTRB/DRQRxB
7	_	NC(OPEN)	25	_	NC	43	0	DTRA/DRQTxB
8		DCDA	26		NC	44	0	DRQTxA
9	1/0	D7	27	0	PRO	45	<u> </u>	VDD(+5V)
10	1/0	D6	28	-	PRI	46	-	VDD(+5V)
11	1/0	D5	29		INTAK	47	1	CK
12	1/0	D4	30	0	INT	48	J.	RESET
13	1/0	D3	31	T	CTSB	49	0	DRQRxA
14	_	NC	32	1	DCDB	50	0	RTSA
15	1/0	D2	33		NC(OPEN)	51		NC
16	_	NC	34	1/0	TRxCB	52		NC .
17	1/0	D1	35	1	RxDB			
18	1/0	D0	36	1	XI1B/STRxCB	L		



5	AxDA	TxDA	1
6	CTSA	XIZA/SYNCA	3
3.0	DCDA	TRXCA	2.
4	XI1A/STRXCA		50
35	AxDB	TxDB	38
<u>31</u> c	CTSB .	XI2B/SYNCB	37
32	DCDB	TRXCB	34
36	XI1B/STRXCE	- ATSB	39
47		Do	18
	4, 5, 5,	D1	17
22 _C	RD .	D2	15
21 _C	WR	Da	13
23	c/D	D4	12
24	B/Ā	DS	11
48 _C	RESET	De	10
		D7	8
	1.00		
		DRQRxA	49
		DRQTxA	44
	ŀ	DTRB/DRQAxB	42
		DTRA/DRQTx8	43
			١
29	INTAK	INT	<u>30</u>
. <u>28</u> c	PRI	PRO	3 <u>27</u>
			ı
٠.,			

	INP	UTS		FUNCTION							
WR	RD	B/A	C/D		TOROTION						
0	1	0	0	CHANNEL A CHANNEL B	WRITE (TxD)						
- 1	0	0	0	CHANNEL A CHANNEL B	READ (RxD)						
0	1	0	1	CHANNEL A	WRITE (CONTROL REGISTER)						
1	0	0	1	CHANNEL A CHANNEL B	READ (STATUS REGISTER)						
1	1	X	Х	HIGH-IMPEDA	NCE						
0	0	X	Χ.	INHIBIT							

0 ; LOW LEVEL 1 ; HIGH LEVEL X ; DON'T CARE.

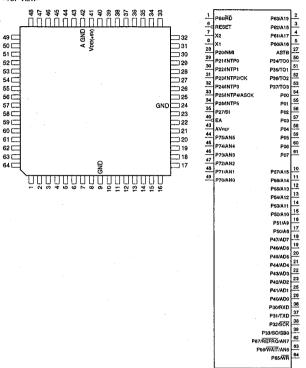
СК	: SYSTEM CLOCK INPUT
WR	: WRITE ENABLE INPUT
RD	: READ ENABLE INPUT
B/A	CHANNEL B/A SELECT INPUT
C/D	CONTROLIDATA SELECT INPUT
D0-D7	: DATA BUS INPUTS/OUTPUTS

DO-D7 : DATA BUS INPUTS/OUTPUTS
INT : INTERRIPT OUTPUT
INTAK : INTERRIPT ACKNOWLEDGE INPUT
PRI : PRIORITY INPUT
DROTXA : DMA REQUEST TXA OUTPUT
PRO : PRIORITY OUTPUT
PRO : PRIORITY OUTPUT

DTRA/DROTXB: DATA TERMINAL READY A/DMA REQUEST TXB OUTPUT DTRB/DRORAB: DATA TERMINAL READY B/DMA REQUEST RXB OUTPUT CTSA, CTSB: CLEAR TO SEND A/B INPUT DCDA, DCDB: DATA CARRIER DETECT A/B INPUT RTSA, RTSB: REQUEST TO SEND A/B OUTPUT RESET: RESET INPUT

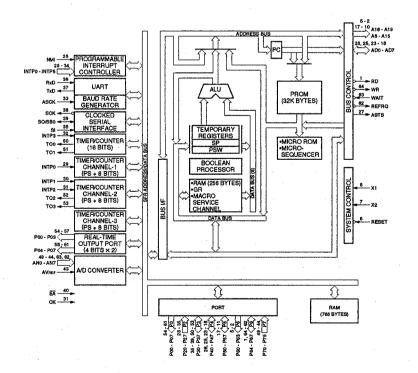
UPD78P218AGC-AB8 (NEC)

C-MOS 8-BIT SINGLE CHIP MICROCOMPUTER WITH ONE TIME PROM –TOP VIEW– $\mbox{\ \ }$



PIN No,	I/O	SIGNAL	PIN NO.	1/0	SIGNAL	PIN NO.	1/0	SIGNAL	PIN NO.	1/0	SIGNAL
1	1/0	P64/RD	17	1/0	P50/A8	33	1	P25/INTP4/ASCK	49	T	P70/ANO
2	0	P63/A19	18	1/0	P47/AD7	34	Т	P26/INTP5	50	0	P34/TO0
3	0	P62/A18	19	VO.	P46/AD6	35	1	P27/SI	51	0	P35/TO1
4	0	P61/A17	20	1/0	P45/AD5	36	1/0	P30/RXD	52	0	P36/TQ2
5	0	P60/A16	21	1/0	P44/AD4	37	1/0	P31/TXD	53	0	P37/TO3
6	1	RESET	22	1/0	P43/AD3	38	1/0	P32/SCK	54	0	P00
. 7	1	X2	23	1/0	P42/AD2	39	1/0	P33/SO/SB0	55	0	P01
8	_	X1	24	_	GND	40	ı	EA	56	O	P02
. 9	_	GND	25	1/0	P41/AD1	41	_	Voo	57	0	P03
10	1/0	P57/A15	26	1/0	P40/AD0	42	_	A GND	58	0	P04
11	1/0	P56/A14	27	0	ASTB	43	1	AVREF	59	0	P05
12	1/0	P55/A13	28	1	P20/NMI	44	1	P75/AN5	60	0	P06
13	1/0	P54/A12	29	1	P21/INTP0	45	1	P74/AN4	61	0	P07
14	1/0	P53/A11	30	- 1	P22/INTP1	46	1	P73/AN3	62	1/0	P67/REFRQ/AN7
15	1/0	P52/A10	31	-1	P23/INTP2/CK	47	1.	P72/AN2	63	1/0	P66/WAIT/ANG
16	1/0	P51/A9	32	1	P24/INTP3	48	ī	P71/AN1	64	1/0	P65/WR

INPUT		INPUT/OUTP	
ANO - AN7	; ANALOG DATA	AD0 - AD7	; ADDRESS/DATA BUS
ASCK	; ASYNCHRONOUS SERIAL CLOCK	P30 - P37	; PORT 3
AVREF	; REFERENCE VOLTAGE	P40 - P47	; PORT 4
CK	; CLOCK	P50 - P57	; PORT 5
ĒĀ	; EXTERNAL ACCESS	P64 - P67	; PORT 6
INTP0 - INTP5	; INTERRUPT FROM PERIPHERALS	SB0	; SERIAL BUS
NM1	; NON-MASKABLE INTERRUPT		
P20 - P27	; PORT 2		
P70 - P75	; PORT 7		
RESET	; SYSTEM RESET		
RXD	; RECEIVE DATA		
Si	; SERIAL DATA		
WAIT	; WAIT		
X1, X2	; EXTERNAL CRYSTAL OSCILLATOR		
OUTPUT			
A8 - A19	: ADDRESS BUS		
ASTB	: ADDRESS STROBE		
P00 - P07	: PORT 0		
P60 - P63	: PORT 6		
RD	: READ STROBE		
REFRO	: REFRESH REQUEST		
SCK	: SERIAL CLOCK		
SO	: SERIAL DATA		
TO0 - TO3	: TIMER DATA		
TXD	: TRANSMIT DATA		
WB	: WRITE STROBE		
***	, MAILE STROBE		





SECTION 7 SPARE PARTS

7-1. NOTES ON REPAIR PARTS

(1) Safety Related Components Warning

Components marked △ are critical to safe operation. Therefore, specified parts should be used in the case of replacement.

(2) Standardization of Parts

Repair parts supplied from Sony Parts Center may not be always identical with the parts which actually in use due to "accommodating the improved parts and/or engineering changes" or "standardization of genuine parts"

This manual's exploded views and electrical spare parts list are indicating the part numbers of "the standardized genuine parts at present".

(3) Stock of Parts

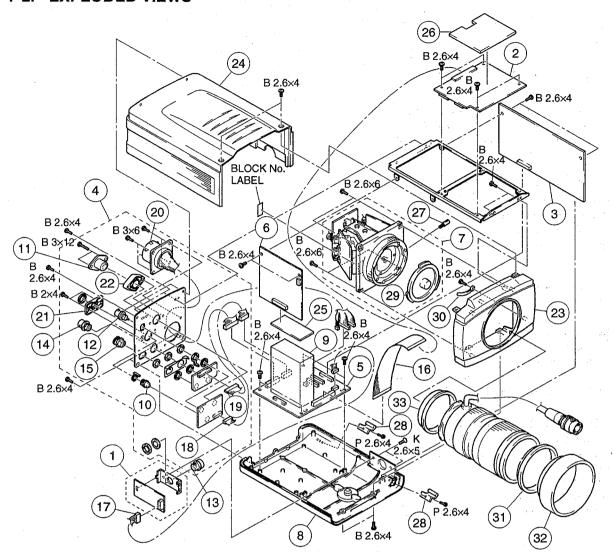
Parts marked with "o" SP (Supply Code) column of the spare parts list are not normally required for routine service work. Orders for parts marked with "o" will be processed, but allow for additional delivery time.

(4) Units for Capacitors, Inductors and Resistors

The following units are assumed in schematic diagrams, electrical parts list and exploded views unless otherwise specified.

Capacitors : μF Inductors : μH Resistors : Ω

7-2. EXPLODED VIEWS

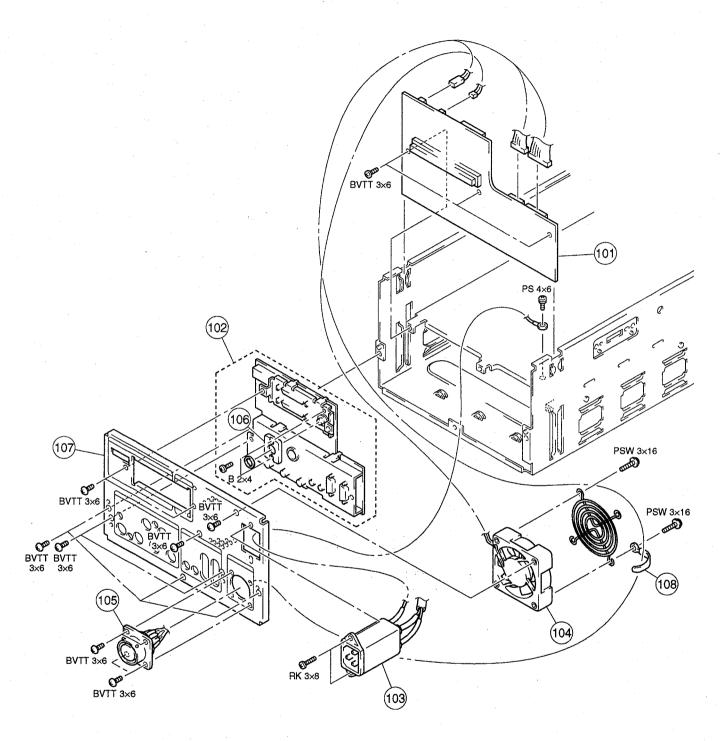


*How to read the CCD BLOCK No.

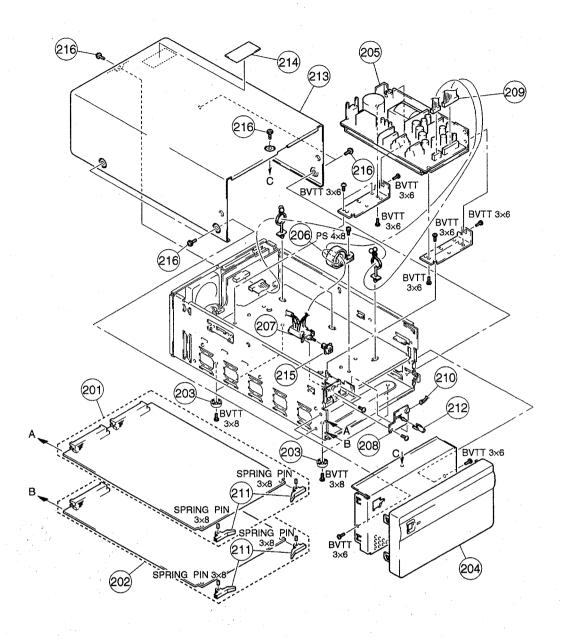
A D A XXXXX
Block number of CCD unit
Suffix of Spare part number
Model name
CCD type

No.	Part No.	SP	Description	No.	Part No.	SP	Description	
1	A-8313-449-A	A 0	MOUNTED CIRCUIT BOARD, CN-1401	18	1-957-463-13	Ls	HARNESS, SUB (MC-2)	
2	A-8313-462-A	A o	MOUNTED CIRCUIT BOARD, TG-180	19			HARNESS, SUB (MC-3)	
3			MOUNTED CIRCUIT BOARD, PR-228	20			HARNESS, SUB (MC-4)	
4	A-8313-466-A						, , ,	
5	A-8313-838-A	A o	MOUNTED CIRCUIT BOARD, MB-724	21	3-184-116-01	Ls	INSULATOR, SOCKET	
				22			SPACER, DIN	
6	A-8313-839-A	Aο	MOUNTED CIRCUIT BOARD, AT-115	23	3-607-511-01			
7			CCD BLOCK ASSY		3-607-513-01			
8			BASE ASSY, BOTTOM	25	3-607-518-01			
8 9 10			CONVERTER UNIT, DC-DC					
10			SOCKET, SYNCHRONIZE	26	3-608-412-02	2 0	HEAT SHEET 2	
			2001-1, 201101-1011	27			LEVER, MOUNT	
11	1-561-284-21	Ls	SOCKET, DIN 8P	28	3-678-684-00		HOLDER, CABLE	
			CONNECTOR, BNC	29	3-699-048-01		CAP, MOUNT	
13			CONNECTOR, 12P FEMALE	29 30	3-703-397-01		STOPPER, WIRING	
			CONNECTOR, (ROUND TYPE) 20P				, , , , , , , , , , , , , , , , , , , ,	
15			CONNECTOR, (ROUND TYPE) 12P	31	3-709-102-03	s	CAP, FRONT (VCL-1205BYS)	
				32			HOOD (VCL-1205BYS)	
16	1-782-281-11	l s	WIRE, FLAT TYPE (30-CORE)	33			CAP, REAR (VCL-1205BYS)	
			HARNESS, SUB (MC-1)	~~	J		,	
	± 55, 102 ±3		The state of the s					540

DKC-ST5 (UC)



No.	Part No.	SP Description	No	. Part No. SP Description
101 102 103 104	A-8313-454-A △1-251-141-11 1-541-981-11	O MOUNTED CIRCUIT BOARD, MB-725 O MOUNTED CIRCUIT BOARD, CN-1396 S INLET, AC (3P) S MOTOR, DC FAN C HADDRESS SIR (CAM)	100 100 100	3-607-507-01 o REAR PANEL



No.	Part No. SP	Description	No.	Part No. SP Description
201 202 203 204 205	A-8313-461-A o X-3556-910-0 s X-3679-083-1 o	MOUNTED CIRCUIT BOARD, ADA-52 MOUNTED CIRCUIT BOARD, SY-243 FOOT ASSY, MF PANEL ASSY, FRONT SWITCHING REGULATOR	211 212 213 214 215	2-182-909-01 o LEVER, PC BOARD 3-174-895-01 o HOLDER, LED 3-601-429-01 o COVER 3-607-522-01 o LABEL, MODE SELECT 4-627-977-01 s BUTTON, SWITCH
206 207 208 209 210	1-665-437-11 o 1-957-453-11 o	CORE, TROIDAL SWITCH, PUSH (AC POWER) PRINTED CIRCUIT BOARD, LED-280 HARNESS, SUB (DC1) HARNESS, SUB (LFD)	216	4-886-821-11 s SCREW, M3 CASE

7-3. ELECTRICAL PARTS LIST

ADA-52 1	BOARD	(ADA-52 BOARD)
Ref. No or Q'ty	Part No. SP Description	Ref. No. or Q'ty Part No. SP Description
1pc 2pcs 2pcs	A-8313-460-A o MOUNTED CIRCUIT BOARD, ADA-52 2-182-909-01 o LEVER, PC BOARD 7-626-320-11 s PIN, SPRING 3X8	C56 1-104-847-11 s TANTALUM, CHIP 22uF 20% 4V C57 1-163-038-91 s CERAMIC 0.1uF 25V C58 1-126-205-11 s ELECT 47uF 20% 6.3V C59 1-104-851-11 s TANTALUM, CHIP 10uF 20% 10V
C1 C2 C3 C4 C5	1-126-205-11 s ELECT 47uF 20% 6.3V 1-126-205-11 s ELECT 47uF 20% 6.3V 1-163-038-91 s CERAMIC 0.1uF 25V 1-163-038-91 s CERAMIC 0.1uF 25V 1-135-091-00 s TANTALUM, CHIP 1uF 20% 16V	C60 1-163-038-91 S CERAMIC 0.1UF 25V
C6 C7 C8 C9 C10		C65 1-163-038-91 s CERAMIC 0.1uF 25V C66 1-104-852-11 s TANTALUM, CHIP 22uF 20% 10V C67 1-126-205-11 s ELECT 47uF 20% 6.3V C68 1-163-038-91 s CERAMIC 0.1uF 25V C69 1-163-038-91 s CERAMIC 0.1uF 25V
C11 C12 C13 C14 C15	1-163-038-91 s CERAMIC 0.1uF 25V 1-163-038-91 s CERAMIC 0.1uF 25V 1-163-224-11 s CERAMIC, CHIP 7PF 0.25PF 50V 1-163-222-11 s CERAMIC 5PF 0.25PF 50V 1-163-235-11 s CERAMIC, CHIP 22PF 5% 50V 1-163-251-11 s CERAMIC, CHIP 100PF 5% 50V	C70 1-104-847-11 s TANTALUM, CHIP 22UF 20% 4V C71 1-163-038-91 s CERAMIC 0.1uF 25V C72 1-126-205-11 s ELECT 47uF 20% 6.3V C73 1-104-851-11 s TANTALUM, CHIP 10uF 20% 10V C74 1-163-038-91 s CERAMIC 0.1uF 25V C75 1-163-038-91 s CERAMIC 0.1uF 25V
C17 C18 C19 C20	1-135-091-00 s TANTALUM, CHIP 1uF 20% 16V 1-126-205-11 s ELECT 47uF 20% 6.3V 1-126-205-11 s ELECT 47uF 20% 6.3V 1-163-038-91 s CERAMIC 0.1uF 25V	C76
C21 C22 C23 C24 C25	1-163-038-91 s CERAMIC 0.1uF 25V 1-135-091-00 s TANTALUM, CHIP 1uF 20% 16V 1-164-232-11 s CERAMIC, CHIP 0.01uF 10% 50V 1-163-275-11 s CERAMIC 0.001uF 5% 50V 1-163-087-00 s CERAMIC, CHIP 4PF 50V	C80 1-104-852-11 s TANTALUM, CHIP 22uF 20% 10V C81 1-126-205-11 s ELECT 47uF 20% 6.3V C82 1-163-038-91 s CERAMIC 0.1uF 25V C83 1-163-038-91 s CERAMIC 0.1uF 25V C84 1-104-847-11 s TANTRALIM CHIP 22uF 20% 4V
C26 C27 C28 C29 C30	1-163-222-11 s CERAMIC 5PF 0.25PF 50V 1-135-091-00 s TANTALUM, CHIP 1uF 20% 16V 1-163-038-91 s CERAMIC 0.1uF 25V 1-163-038-91 s CERAMIC 0.1uF 25V 1-163-224-11 s CERAMIC, CHIP 7PF 0.25PF 50V 1-163-222-11 s CERAMIC 5PF 0.25PF 50V	C85 1-163-038-91 s CERAMIC 0.1uF 25V C86 1-126-205-11 s ELECT 47uF 20% 6.3V C87 1-104-851-11 s TANTALUM, CHIP 10uF 20% 10V C88 1-163-038-91 s CERAMIC 0.1uF 25V C89 1-163-038-91 s CERAMIC 0.1uF 25V
C31 C32 C33 C34 C35	1-163-235-11 s CERAMIC, CHIP 22PF 5% 50V 1-163-251-11 s CERAMIC, CHIP 100PF 5% 50V 1-135-091-00 s TANTALUM, CHIP 1uF 20% 16V 1-126-205-11 s ELECT 47uF 20% 6.3V	C91 1-163-038-91 s CERAMIC 0.1uF 25V C92 1-126-205-11 s ELECT 47uF 20% 6.3V C93 1-163-038-91 s CERAMIC 0.1uF 25V C94 1-163-231-11 s CERAMIC 15PF 5% 50V
C36 C37 C38 C39 C40		C98 1-126-205-11 s ELECT 47uF 20% 6.3V C99 1-163-038-91 s CERAMIC 0.1uF 25V
C41 C42 C43 C44 C45	1-163-275-11 s CERAMIC 0.001uF 5% 50V 1-163-087-00 s CERAMIC, CHIP 4PF 50V 1-163-222-11 s CERAMIC 5PF 0.25PF 50V 1-135-091-00 s TANTALUM, CHIP 1uF 20% 16V 1-163-038-91 s CERAMIC 0.1uF 25V 1-163-038-91 s CERAMIC 0.1uF 25V	C100 1-135-070-00 s TANTALUM, CHIP 0.1uF 10% 35V C101 1-164-695-11 s CERAMIC 0.0022uF 5% 50V C102 1-164-695-11 s CERAMIC 0.0022uF 5% 50V C103 1-126-205-11 s ELECT 47uF 20% 6.3V C104 1-163-038-91 s CERAMIC 0.1uF 25V
C46 C47 C48 C49 C50	1-163-224-11 s CERAMIC, CHIP 7PF 0.25PF 50V 1-163-222-11 s CERAMIC 5PF 0.25PF 50V 1-163-235-11 s CERAMIC, CHIP 22PF 5% 50V 1-163-251-11 s CERAMIC, CHIP 100PF 5% 50V	C106 1-126-934-11 s ELECT 220uF 20% 16V C107 1-126-933-11 s ELECT 100uF 20% 16V C108 1-126-933-11 s ELECT 100uF 20% 16V
C51 C52 C53 C54 C55	1-135-091-00 s TANTALUM, CHIP 1uF 20% 16V 1-104-852-11 s TANTALUM, CHIP 22uF 20% 10V	C110 1-163-038-91 s CERAMIC 0.1uF 25V C111 1-163-038-91 s CERAMIC 0.1uF 25V C112 1-163-038-91 s CERAMIC 0.1uF 25V C113 1-163-038-91 s CERAMIC 0.1uF 25V C114 1-163-038-91 s CERAMIC 0.1uF 25V C114 1-163-038-91 s CERAMIC 0.1uF 25V

(ADA-52 BOARD)

Ref. No. or Q'ty Part No. SP Description	Ref. No. or Q'ty Part No. SP Description
C116 1-163-038-91 s CERAMIC 0.1uF 25V C117 1-163-038-91 s CERAMIC 0.1uF 25V C118 1-163-038-91 s CERAMIC 0.1uF 25V C119 1-163-038-91 s CERAMIC 0.1uF 25V C120 1-163-038-91 s CERAMIC 0.1uF 25V	C202 1-126-923-11 s ELECT 220uF 20% 10V C203 1-163-038-91 s CERAMIC 0.1uF 25V C204 1-163-038-91 s CERAMIC 0.1uF 25V C205 1-163-038-91 s CERAMIC 0.1uF 25V C206 1-163-038-91 s CERAMIC 0.1uF 25V
C121 1-163-038-91 s CERAMIC 0.1uF 25V C122 1-163-038-91 s CERAMIC 0.1uF 25V C123 1-163-038-91 s CERAMIC 0.1uF 25V C124 1-163-038-91 s CERAMIC 0.1uF 25V C125 1-163-038-91 s CERAMIC 0.1uF 25V	C207 1-163-038-91 s CERAMIC 0.1uF 25V C208 1-163-038-91 s CERAMIC 0.1uF 25V C209 1-163-038-91 s CERAMIC 0.1uF 25V C210 1-163-038-91 s CERAMIC 0.1uF 25V C211 1-163-038-91 s CERAMIC 0.1uF 25V
C134 1-163-038-91 s CERAMIC 0.1uF 25V	C215 1-163-038-91 s CERAMIC 0.1uF 25V C216 1-163-038-91 s CERAMIC 0.1uF 25V
C135 1-163-229-11 s CERAMIC 12PF 5% 50V C136 1-163-038-91 s CERAMIC 0.1uF 25V C137 1-163-038-91 s CERAMIC 0.1uF 25V C138 1-163-229-11 s CERAMIC 12PF 5% 50V C139 1-126-205-11 s ELECT 47uF 20% 6.3V	C217 1-163-038-91 s CERAMIC 0.1uF 25V C218 1-163-038-91 s CERAMIC 0.1uF 25V C219 1-163-038-91 s CERAMIC 0.1uF 25V C220 1-163-038-91 s CERAMIC 0.1uF 25V C221 1-163-038-91 s CERAMIC 0.1uF 25V
C155 1-120-205-11 S ELECT 470F 20% 6.3V C154 1-126-205-11 S ELECT 470F 20% 6.3V	C221 1-163-038-91 s CERAMIC 0.1uF 25V C222 1-163-038-91 s CERAMIC 0.1uF 25V C223 1-163-038-91 s CERAMIC 0.1uF 25V C224 1-163-038-91 s CERAMIC 0.1uF 25V C225 1-163-038-91 s CERAMIC 0.1uF 25V C226 1-163-038-91 s CERAMIC 0.1uF 25V
C155 1-126-205-11 s ELECT 47uF 20% 6.3V C156 1-126-205-11 s ELECT 47uF 20% 6.3V C157 1-126-205-11 s ELECT 47uF 20% 6.3V C158 1-126-205-11 s ELECT 47uF 20% 6.3V C159 1-110-569-11 s TANTAL 47uF 20% 6.3V	
C160 1-110-569-11 s TANTAL 47uF 20% 6.3V C161 1-163-038-91 s CERAMIC 0.1uF 25V C162 1-126-205-11 s ELECT 47uF 20% 6.3V C163 1-126-205-11 s ELECT 47uF 20% 6.3V C164 1-126-205-11 s ELECT 47uF 20% 6.3V	C232 1-163-038-91 s CERAMIC 0.1uF 25V C233 1-163-038-91 s CERAMIC 0.1uF 25V C234 1-163-038-91 s CERAMIC 0.1uF 25V C235 1-163-038-91 s CERAMIC 0.1uF 25V C236 1-163-038-91 s CERAMIC 0.1uF 25V
C165 1-126-205-11 s ELECT 47uF 20% 6.3V C166 1-126-205-11 s ELECT 47uF 20% 6.3V C167 1-126-205-11 s ELECT 47uF 20% 6.3V C168 1-126-205-11 s ELECT 47uF 20% 6.3V C169 1-126-205-11 s ELECT 47uF 20% 6.3V	C237 1-163-038-91 s CERAMIC 0.1uF 25V C238 1-163-038-91 s CERAMIC 0.1uF 25V C239 1-163-038-91 s CERAMIC 0.1uF 25V C240 1-163-038-91 s CERAMIC 0.1uF 25V C241 1-163-038-91 s CERAMIC 0.1uF 25V
C170 1-163-038-91 s CERAMIC 0.1uF 25V C171 1-163-038-91 s CERAMIC 0.1uF 25V C180 1-126-205-11 s ELECT 47uF 20% 6.3V C181 1-126-205-11 s ELECT 47uF 20% 6.3V C182 1-126-205-11 s ELECT 47uF 20% 6.3V	C242 1-163-038-91 s CERAMIC 0.1uF 25V C243 1-163-038-91 s CERAMIC 0.1uF 25V C244 1-163-038-91 s CERAMIC 0.1uF 25V C245 1-163-038-91 s CERAMIC 0.1uF 25V C246 1-163-038-91 s CERAMIC 0.1uF 25V
C183 1-163-038-91 s CERAMIC 0.1uF 25V C184 1-163-038-91 s CERAMIC 0.1uF 25V C185 1-163-038-91 s CERAMIC 0.1uF 25V C186 1-163-038-91 s CERAMIC 0.1uF 25V C187 1-163-038-91 s CERAMIC 0.1uF 25V	C247 1-163-038-91 s CERAMIC 0.1uF 25V C248 1-163-038-91 s CERAMIC 0.1uF 25V C249 1-163-038-91 s CERAMIC 0.1uF 25V C250 1-163-038-91 s CERAMIC 0.1uF 25V C251 1-163-038-91 s CERAMIC 0.1uF 25V
C188 1-163-038-91 s CERAMIC 0.1uF 25V C189 1-163-239-11 s CERAMIC, CHIP 33PF 5% 50V C190 1-163-239-11 s CERAMIC, CHIP 33PF 5% 50V C191 1-163-239-11 s CERAMIC, CHIP 33PF 5% 50V C192 1-163-133-00 s CERAMIC, CHIP 470PF 5% 50V	C252 1-163-038-91 s CERAMIC 0.1uF 25V C253 1-163-038-91 s CERAMIC 0.1uF 25V C254 1-163-038-91 s CERAMIC 0.1uF 25V C255 1-163-038-91 s CERAMIC 0.1uF 25V C256 1-163-038-91 s CERAMIC 0.1uF 25V
C193 1-163-133-00 s CERAMIC, CHIP 470PF 5% 50V C194 1-163-127-00 s CERAMIC, CHIP 270PF 5% 50V C195 1-163-127-00 s CERAMIC, CHIP 270PF 5% 50V C201 1-126-924-11 s ELECT 330uF 20% 10V	C257 1-163-038-91 s CERAMIC 0.1uf 25V C258 1-163-038-91 s CERAMIC 0.1uf 25V C259 1-163-038-91 s CERAMIC 0.1uf 25V C260 1-163-038-91 s CERAMIC 0.1uf 25V

7-6

(ADA-52 BOARD)

Ref. No. or Q'ty Part No. SP Description	Ref. No. or Q'ty Part No. SP Description
C261 1-163-038-91 s CERAMIC 0.1uF 25V C262 1-163-038-91 s CERAMIC 0.1uF 25V C263 1-163-038-91 s CERAMIC 0.1uF 25V C264 1-163-038-91 s CERAMIC 0.1uF 25V C265 1-163-038-91 s CERAMIC 0.1uF 25V	C321 1-163-038-91 s CERAMIC 0.1uF 25V C322 1-163-038-91 s CERAMIC 0.1uF 25V C323 1-163-038-91 s CERAMIC 0.1uF 25V C324 1-163-038-91 s CERAMIC 0.1uF 25V C325 1-163-038-91 s CERAMIC 0.1uF 25V
C266 1-163-038-91 s CERAMIC 0.1uF 25V C267 1-163-038-91 s CERAMIC 0.1uF 25V C268 1-163-038-91 s CERAMIC 0.1uF 25V C269 1-163-038-91 s CERAMIC 0.1uF 25V C270 1-163-038-91 s CERAMIC 0.1uF 25V	C326 1-163-038-91 s CERAMIC 0.1uF 25V
C271 1-163-038-91 s CERAMIC 0.1uF 25V C272 1-163-038-91 s CERAMIC 0.1uF 25V C273 1-163-038-91 s CERAMIC 0.1uF 25V C274 1-163-038-91 s CERAMIC 0.1uF 25V C275 1-163-038-91 s CERAMIC 0.1uF 25V	C332 1-135-091-00 s TANTALUM, CHIP 1uF 20% 16V C333 1-163-038-91 s CERAMIC 0.1uF 25V C334 1-163-038-91 s CERAMIC 0.1uF 25V C335 1-110-569-11 s TANTAL 47uF 20% 6.3V C336 1-163-231-11 s CERAMIC 15PF 5% 50V
C276 1-163-038-91 s CERAMIC 0.1uF 25V C277 1-163-038-91 s CERAMIC 0.1uF 25V C278 1-163-038-91 s CERAMIC 0.1uF 25V C279 1-163-038-91 s CERAMIC 0.1uF 25V C280 1-163-038-91 s CERAMIC 0.1uF 25V	
C281 1-163-038-91 s CERAMIC 0.1uF 25V C282 1-163-038-91 s CERAMIC 0.1uF 25V C283 1-163-038-91 s CERAMIC 0.1uF 25V C284 1-163-038-91 s CERAMIC 0.1uF 25V C285 1-163-038-91 s CERAMIC 0.1uF 25V	
C287 1-163-038-91 s CERAMIC 0.1uF 25V C288 1-163-133-00 s CERAMIC, CHIP 470PF 5% 50V C289 1-163-133-00 s CERAMIC, CHIP 470PF 5% 50V C290 1-163-133-00 s CERAMIC, CHIP 470PF 5% 50V C291 1-163-133-00 s CERAMIC, CHIP 470PF 5% 50V	C347 1-110-569-11 s TANTAL 47uF 20% 6.3V C348 1-163-231-11 s CERAMIC 15PF 5% 50V C349 1-163-227-11 s CERAMIC, CHIP 10PF 5% 50V C350 1-135-091-00 s TANTALUM, CHIP 1uF 20% 16V C351 1-163-038-91 s CERAMIC 0.1uF 25V
C292 1-163-133-00 s CERAMIC, CHIP 470PF 5% 50V C293 1-163-133-00 s CERAMIC, CHIP 470PF 5% 50V C294 1-163-133-00 s CERAMIC, CHIP 470PF 5% 50V C295 1-163-133-00 s CERAMIC, CHIP 470PF 5% 50V C296 1-163-127-00 s CERAMIC, CHIP 270PF 5% 50V	C352 1-163-038-91 s CERAMIC 0.1uF 25V C353 1-110-569-11 s TANTAL 47uF 20% 6.3V C354 1-163-231-11 s CERAMIC 15PF 5% 50V C355 1-163-227-11 s CERAMIC, CHIP 10PF 5% 50V C356 1-135-091-00 s TANTALUM, CHIP 1uF 20% 16V
C297 1-163-127-00 s CERAMIC, CHIP 270PF 5% 50V C298 1-163-251-11 s CERAMIC, CHIP 100PF 5% 50V C299 1-163-251-11 s CERAMIC, CHIP 100PF 5% 50V C300 1-163-127-00 s CERAMIC, CHIP 270PF 5% 50V C301 1-126-205-11 s ELECT 47uF 20% 6.3V	C357 1-163-038-91 s CERAMIC 0.1uF 25V C358 1-163-038-91 s CERAMIC 0.1uF 25V C359 1-110-569-11 s TANTAL 47uF 20% 6.3V C360 1-163-235-11 s CERAMIC, CHIP 22PF 5% 50V C361 1-163-227-11 s CERAMIC, CHIP 10PF 5% 50V
C302 1-163-038-91 s CERAMIC 0.1uF 25V C303 1-163-038-91 s CERAMIC 0.1uF 25V C304 1-163-038-91 s CERAMIC 0.1uF 25V C305 1-163-038-91 s CERAMIC 0.1uF 25V C306 1-163-038-91 s CERAMIC 0.1uF 25V	C362 1-135-091-00 s TANTALUM, CHIP 1uF 20% 16V C363 1-163-038-91 s CERAMIC 0.1uF 25V C364 1-163-038-91 s CERAMIC 0.1uF 25V C365 1-110-569-11 s TANTAL 47uF 20% 6.3V C366 1-163-235-11 s CERAMIC, CHIP 22PF 5% 50V
C307	
C312 1-163-038-91 s CERAMIC 0.1uF 25V C313 1-126-205-11 s ELECT 47uF 20% 6.3V C314 1-126-205-11 s ELECT 47uF 20% 6.3V C315 1-163-038-91 s CERAMIC 0.1uF 25V C316 1-163-038-91 s CERAMIC 0.1uF 25V C317 1-126-205-11 s ELECT 47uF 20% 6.3V	C372 1-126-206-11 s ELECT 100uF 20% 6.3V C373 1-126-206-11 s ELECT 100uF 20% 6.3V C374 1-163-038-91 s CERAMIC 0.1uF 25V C375 1-163-038-91 s CERAMIC 0.1uF 25V C376 1-163-251-11 s CERAMIC, CHIP 100PF 5% 50V
C317 1-126-205-11 s ELECT 47uF 20% 6.3V C318 1-126-205-11 s ELECT 47uF 20% 6.3V C319 1-163-243-11 s CERAMIC, CHIP 47PF 5% 50V C320 1-163-222-11 s CERAMIC 5PF 0.25PF 50V	C377 1-163-087-00 s CERAMIC, CHIP 4PF 50V C378 1-163-038-91 s CERAMIC 0.1uF 25V C379 1-163-038-91 s CERAMIC 0.1uF 25V C381 1-163-235-11 s CERAMIC, CHIP 22PF 5% 50V

(ADA-52 1	BOARD)	(ADA-52	BOARD)
Ref. No. or Q'ty		Ref. No. or Q'ty	Part No. SP Description
C382 C383 C384 C385 C386	1-163-038-91 s CERAMIC 0.1uF 25V 1-163-038-91 s CERAMIC 0.1uF 25V 1-163-038-91 s CERAMIC 0.1uF 25V 1-163-038-91 s CERAMIC 0.1uF 25V 1-163-038-91 s CERAMIC 0.1uF 25V	IC15 IC16 IC17 IC18 IC19	8-759-174-16 s IC TC74VHC244F 8-759-186-39 s IC TC74VHC74F 8-759-185-84 s IC TC74VHC161F(EL) 8-759-185-84 s IC TC74VHC161F(EL) 8-752-326-08 s IC CXD1159Q
C387 C388 C389 C390	1-163-038-91 s CERAMIC 0.1uf 25V 1-163-038-91 s CERAMIC 0.1uf 25V 1-163-038-91 s CERAMIC 0.1uf 25V 1-163-038-91 s CERAMIC 0.1uf 25V	IC20 IC22 IC23 IC24 IC25	8-759-250-81 s IC TC5081AP 8-759-231-53 s IC TA7805S 8-759-245-79 s IC TA79005S 8-759-981-48 s IC TL082M 8-759-150-64 s IC UPC78L08T
C392 C393 C394 C395 C400	1-163-038-91 s CERAMIC 0.1uF 25V 1-163-038-91 s CERAMIC 0.1uF 25V 1-163-038-91 s CERAMIC 0.1uF 25V 1-163-038-91 s CERAMIC 0.1uF 25V 1-163-038-91 s CERAMIC 0.1uF 25V		8-759-372-52 s IC OP293-S 8-759-081-44 s IC TC74VHC04F 8-759-031-84 s IC SC7S04F 8-759-174-16 s IC TC74VHC244F 8-759-174-16 s IC TC74VHC244F
C401 C402 C403 C404 C405	1-163-038-91 s CERAMIC 0.1uf 25V 1-163-038-91 s CERAMIC 0.1uf 25V 1-163-038-91 s CERAMIC 0.1uf 25V 1-135-091-00 s TANTALUM, CHIP 1uf 20% 16V 1-135-091-00 s TANTALUM, CHIP 1uf 20% 16V	IC32 IC33 IC34 IC35 IC36	8-759-372-52 s IC OP293-S 8-759-984-88 s IC LM6361M 8-759-372-52 s IC OP293-S 8-759-984-88 s IC LM6361M 8-759-372-52 s IC OP293-S
C406 C500 C501	1-135-091-00 s TANTALUM, CHIP 1UF 20% 16V 1-135-091-00 s TANTALUM, CHIP 1UF 20% 16V 1-163-038-91 s CERAMIC 0.1UF 25V 1-163-217-11 s CERAMIC 1PF 0.25PF 50V 1-163-217-11 s CERAMIC 1PF 0.25PF 50V	IC37 IC201 IC202	8-759-984-88 s IC LM6361M 8-759-186-47 s IC TC74VHC138F 8-759-186-47 s IC TC74VHC138F
C502 C503	1-163-217-11 S CERAMIC 1PF 0.25PF 50V 1-163-217-11 S CERAMIC 1PF 0.25PF 50V	IC203	8-759-186-47 s IC TC74VHC138F 8-759-186-47 s IC TC74VHC138F
C504 CN1	1-163-038-91 s CERAMIC 0.1uF 25V 1-778-261-11 o CONNECTOR, BOARD TO BOARD 124P	IC204 IC205 IC206 IC207	8-759-081-44 s IC TC74VHC04F 8-759-186-38 s IC TC74VHC32F 8-759-186-38 s IC TC74VHC32F
CV1	1-141-311-11 s VAR, TRIMMER 20PF	IC208 IC210	8-759-186-29 s IC TC74VHC11F 8-759-186-38 s IC TC74VHC32F
CV2 D1 D2 D3 D4	0 840 408 84	IC211 IC214 IC215 IC218 IC219	8-759-186-38 s IC TC74VHC32F 8-759-186-38 s IC TC74VHC32F 8-759-186-38 s IC TC74VHC32F 8-759-186-38 s IC TC74VHC32F 8-759-186-38 s IC TC74VHC32F
F201	1-532-745-11 s FUSE 3.15A 125V	IC220 IC222	8-759-186-29 s IC TC74VHC11F 8-759-186-38 s IC TC74VHC32F
FH201 FH202	1-533-189-11 s HOLDER, FUSE 1-533-189-11 s HOLDER, FUSE	IC223	8-759-186-38 s IC TC74VHC32F 8-759-186-38 s IC TC74VHC32F 8-759-186-38 s IC TC74VHC32F
FL101 FL201 FL301 FL302 FL303	1-239-558-11 s FILTER, CHIP EMI 1-421-773-11 s FILTER, NOISE REMOVAL 1-239-289-11 s FILTER, LOW-PASS 1-239-289-11 s FILTER, LOW-PASS 1-239-289-11 s FILTER, LOW-PASS	IC229 IC230 IC231 IC232 IC234	8-759-081-44 s IC TC74VHC04F 8-759-186-38 s IC TC74VHC32F 8-759-186-38 s IC TC74VHC32F 8-759-186-29 s IC TC74VHC11F 8-759-186-38 s IC TC74VHC32F
FL304	1-239-558-11 s FILTER, CHIP EMI	IC235	8-759-186-38 s IC TC74VHC32F
IC1 IC2 IC3 IC4 IC5	8-752-053-26 s IC CXA1399Q-T4 8-759-423-92 s IC SPT7855SCT 8-752-053-26 s IC CXA1399Q-T4 8-759-423-92 s IC SPT7855SCT 8-752-053-26 s IC CXA13990-T4	IC238 IC239 IC240 IC242	8-759-174-16 s IC TC74VHC244F 8-759-174-16 s IC TC74VHC244F 8-759-174-16 s IC TC74VHC244F 8-759-186-02 s IC TC74VHCT245F(EL)
IC6 IC8 IC9 IC10 IC11	8-759-423-92 s IC SPT7855SCT 8-759-174-16 s IC TC74VHC244F 8-759-174-16 s IC TC74VHC244F 8-759-174-16 s IC TC74VHC244F 8-759-174-16 s IC TC74VHC244F	IC243 IC244 IC245 IC246 IC247	8-759-186-02 s IC TC74VHCT245F(EL) 8-759-186-02 s IC TC74VHCT245F(EL) 8-759-174-16 s IC TC74VHC244F 8-759-174-16 s IC TC74VHC244F 8-759-174-16 s IC TC74VHC244F
IC12 IC13 IC14	8-759-174-16 s IC TC74VHC244F 8-759-174-16 s IC TC74VHC244F 8-759-174-16 s IC TC74VHC244F	IC248 IC249 IC250 IC251	8-759-174-16 s IC TC74VHC244F 8-759-354-55 s IC HM5117800BJ-7EL 8-759-354-55 s IC HM5117800BJ-7EL 8-759-354-55 s IC HM5117800BJ-7EL

042

1-410-373-31 s INDUCTOR CHIP 2.2uH

(ADA-52 BOARD)	(ADA-52 BOARD)	
Ref. No. or Q'ty Part No. SP Description		
Q43 8-729-200-86 s TRANSISTOR 2SC2714-0 Q44 8-729-122-63 s TRANSISTOR 2SA1226 Q45 8-729-200-86 s TRANSISTOR 2SC2714-0 Q301 8-729-120-28 s TRANSISTOR 2SC1623-L5L6 Q302 8-729-402-81 s TRANSISTOR XN4501	R19 1-216-077-00 s METAL, CHIP 15K 5% 1/10W R20 1-216-073-00 s METAL, CHIP 10K 5% 1/10W R21 1-216-651-11 s METAL, CHIP 1K 0.5% 1/10W R22 1-216-651-11 s METAL, CHIP 1K 0.5% 1/10W	Į
~	R24 1-216-653-11 s METAL, CHIP 1.2K 0.5% 1/1 R25 1-216-665-11 s METAL, CHIP 3.9K 0.5% 1/1 R26 1-216-049-00 s METAL, CHIP 1K 5% 1/10W R27 1-216-045-00 s METAL, CHIP 680 5% 1/10W R28 1-216-049-00 s METAL, CHIP 1K 5% 1/10W	
	R29 1-216-655-11 s METAL, CHIP 1.5K 0.5% 1/1 R30 1-216-309-00 s METAL 5.6 5% 1/10W R31 1-216-309-00 s METAL 5.6 5% 1/10W R32 1-216-650-11 s METAL, CHIP 910 0.5% 1/10 R33 1-216-627-11 s METAL, CHIP 100 0.5% 1/10)W
Q313 8-729-402-87 s TRANSISTOR XN2401 Q314 8-729-402-13 s TRANSISTOR XN1501 Q315 8-729-122-63 s TRANSISTOR 2SA1226 Q316 8-729-420-36 s TRANSISTOR XN2501 Q317 8-729-402-87 s TRANSISTOR XN2401	R34 1-216-657-11 s METAL, CHIP 1.8K 0.5% 1/1 R35 1-216-659-11 s METAL, CHIP 2.2K 0.5% 1/1 R36 1-216-674-11 s METAL, CHIP 9.1K 0.5% 1/1 R37 1-216-025-00 s METAL, CHIP 100 5% 1/10W R38 1-216-073-00 s METAL, CHIP 10K 5% 1/10W	LOW
	R39 1-216-077-00 s METAL, CHIP 15K 5% 1/10W R40 1-216-624-11 s METAL, CHIP 75 0.5% 1/10W R42 1-216-659-11 s METAL, CHIP 2.2K 0.5% 1/1 R43 1-216-009-00 s METAL, CHIP 22 5% 1/10W R44 1-216-065-00 s METAL, CHIP 4.7K 5% 1/10W	LOW
Q323 8-729-122-63 s TRANSISTOR 2SA1226 Q324 8-729-420-36 s TRANSISTOR XN2501 Q325 8-729-402-87 s TRANSISTOR XN2401 Q326 8-729-402-13 s TRANSISTOR XN1501 Q327 8-729-122-63 s TRANSISTOR 2SA1226	R45 1-216-073-00 s METAL, CHIP 10K 5% 1/10W R46 1-216-073-00 s METAL, CHIP 10K 5% 1/10W R47 1-216-025-00 s METAL, CHIP 10O 5% 1/10W R48 1-216-049-00 s METAL, CHIP 1K 5% 1/10W R49 1-216-073-00 s METAL, CHIP 10K 5% 1/10W	
Q328 8-729-420-36 s TRANSISTOR XN2501 Q329 8-729-402-87 s TRANSISTOR XN2401 Q330 8-729-402-13 s TRANSISTOR XN1501 Q331 8-729-122-63 s TRANSISTOR 2SA1226 Q332 8-729-420-36 s TRANSISTOR XN2501	R50 1-216-661-11 s METAL, CHIP 2.7K 0.5% 1/1 R51 1-216-653-11 s METAL, CHIP 1.2K 0.5% 1/1 R52 1-216-673-11 s METAL, CHIP 8.2K 0.5% 1/1 R53 1-216-653-11 s METAL, CHIP 1.2K 0.5% 1/1 R54 1-216-059-00 s METAL, CHIP 2.7K 5% 1/10W	LOW LOW LOW
Q333 8-729-402-87 s TRANSISTOR XN2401 Q334 8-729-402-13 s TRANSISTOR XN1501 Q335 8-729-122-63 s TRANSISTOR 2SA1226 Q336 8-729-420-36 s TRANSISTOR XN2501 Q337 8-729-402-87 s TRANSISTOR XN2401	R55 1-216-077-00 s METAL, CHIP 15K 5% 1/10W R56 1-216-073-00 s METAL, CHIP 10K 5% 1/10W R57 1-216-651-11 s METAL, CHIP 1K 0.5% 1/10W R58 1-216-651-11 s METAL, CHIP 1K 0.5% 1/10W R59 1-216-650-11 s METAL, CHIP 910 0.5% 1/10W	V
Q338 8-729-120-28 s TRANSISTOR 2SC1623-L5L6 Q339 8-729-402-84 s TRANSISTOR XN4601 Q340 8-729-402-84 s TRANSISTOR XN4601	R61 1-216-665-11 s METAL, CHIP 3.9K 0.5% 1/1 R62 1-216-049-00 s METAL, CHIP 1K 5% 1/10W R63 1-216-045-00 s METAL, CHIP 680 5% 1/10W	
R1 1-216-073-00 s METAL, CHIP 10K 5% 1/10W R4 1-216-624-11 s METAL, CHIP 75 0.5% 1/10W R6 1-216-659-11 s METAL, CHIP 2.2K 0.5% 1/10W R7 1-216-009-00 s METAL, CHIP 22 5% 1/10W R8 1-216-065-00 s METAL, CHIP 4.7K 5% 1/10W	R64 1-216-049-00 s METAL, CHIP 1K 5% 1/10W R65 1-216-655-11 s METAL, CHIP 1.5K 0.5% 1/1 R66 1-216-309-00 s METAL 5.6 5% 1/10W R67 1-216-309-00 s METAL 5.6 5% 1/10W R68 1-216-650-11 s METAL, CHIP 910 0.5% 1/10	
R9 1-216-073-00 s METAL, CHIP 10K 5% 1/10W R10 1-216-073-00 s METAL, CHIP 10K 5% 1/10W R11 1-216-025-00 s METAL, CHIP 10O 5% 1/10W R12 1-216-049-00 s METAL, CHIP 1K 5% 1/10W R13 1-216-073-00 s METAL, CHIP 10K 5% 1/10W	R69 1-216-627-11 s METAL, CHIP 100 0.5% 1/10 R70 1-216-657-11 s METAL, CHIP 1.8K 0.5% 1/1 R71 1-216-659-11 s METAL, CHIP 2.2K 0.5% 1/1 R72 1-216-674-11 s METAL, CHIP 9.1K 0.5% 1/1	OW LOW LOW
R14 1-216-661-11 s METAL, CHIP 2.7K 0.5% 1/10W R15 1-216-653-11 s METAL, CHIP 1.2K 0.5% 1/10W R16 1-216-673-11 s METAL, CHIP 8.2K 0.5% 1/10W	R73 1-216-025-00 s METAL, CHIP 100 5% 1/10W R74 1-216-073-00 s METAL, CHIP 10K 5% 1/10W R75 1-216-077-00 s METAL, CHIP 15K 5% 1/10W	
R17 1-216-653-11 s METAL, CHIP 1.2K 0.5% 1/10W R18 1-216-059-00 s METAL, CHIP 2.7K 5% 1/10W	R76 1-216-624-11 s METAL, CHIP 75 0.5% 1/10V R78 1-216-659-11 s METAL, CHIP 2.2K 0.5% 1/1 R79 1-216-009-00 s METAL, CHIP 22 5% 1/10W	

R310

R311

1-216-073-00 s METAL, CHIP 10K 5% 1/10W

R171

R172

R638

R577

1-216-653-11 s METAL, CHIP 1.2K 0.5% 1/10W

(ADA-52 BOARD)

Ref. No.	
or Q'ty	Part No. SP Description 1-216-309-00 s METAL 5.6 5% 1/10W
R640 R641	1-216-624-11 s METAL, CHIP 75 0.5% 1/10W 1-216-081-00 s METAL, CHIP 22K 5% 1/10W
R642	1-216-073-00 s METAL, CHIP 10K 5% 1/10W
R643	1-216-065-00 s METAL, CHIP 4.7K 5% 1/10W
R644 R645	1-216-057-00 s METAL, CHIP 2.2K 5% 1/10W 1-216-057-00 s METAL, CHIP 2.2K 5% 1/10W
R646	1-216-309-00 s METAL 5.6 5% 1/10W
R647	1-216-309-00 s METAL 5.6 5% 1/10W
R648	1-216-624-11 s METAL, CHIP 75 0.5% 1/10W
R649	1-216-065-00 s METAL, CHIP 4.7K 5% 1/10W
R651	1-216-057-00 s METAL, CHIP 2.2K 5% 1/10W
R652	1-216-063-91 s METAL 3.9K 5% 1/10W
R653	1-216-013-00 s METAL, CHIP 33 5% 1/10W
R660	1-216-029-00 s METAL, CHIP 150 5% 1/10W
R661	1-216-029-00 s METAL, CHIP 150 5% 1/10W
R662	1-216-029-00 s METAL, CHIP 150 5% 1/10W
R663	1-216-029-00 s METAL, CHIP 150 5% 1/10W
R664	1-216-029-00 s METAL, CHIP 150 5% 1/10W
R665	1-216-029-00 s METAL, CHIP 150 5% 1/10W
R666	1-216-029-00 s METAL, CHIP 150 5% 1/10W
R667	1-216-029-00 s METAL, CHIP 150 5% 1/10W
R668	1-216-029-00 s METAL, CHIP 150 5% 1/10W
R669	1-216-029-00 s METAL, CHIP 150 5% 1/10W
R670	1-216-029-00 s METAL, CHIP 150 5% 1/10W
R671	1-216-029-00 s METAL, CHIP 150 5% 1/10W
R672	1-216-029-00 s METAL, CHIP 150 5% 1/10W
R673	1-216-029-00 s METAL, CHIP 150 5% 1/10W
R674	1-216-029-00 s METAL, CHIP 150 5% 1/10W
R675	1-216-029-00 s METAL, CHIP 150 5% 1/10W
R676	1-216-029-00 s METAL, CHIP 150 5% 1/10W
R677	1-216-029-00 s METAL, CHIP 150 5% 1/10W
R678	1-216-029-00 s METAL, CHIP 150 5% 1/10W
R679	1-216-029-00 s METAL, CHIP 150 5% 1/10W
R680	1-216-029-00 s METAL, CHIP 150 5% 1/10W
R681	1-216-029-00 s METAL, CHIP 150 5% 1/10W
R682	1-216-029-00 s METAL, CHIP 150 5% 1/10W
R683	1-216-029-00 s METAL, CHIP 150 5% 1/10W
R684	1-216-029-00 s METAL, CHIP 150 5% 1/10W
R685	1-216-029-00 s METAL, CHIP 150 5% 1/10W
R686	1-216-029-00 s METAL, CHIP 150 5% 1/10W
R687	1-216-029-00 s METAL, CHIP 150 5% 1/10W
R688	1-216-029-00 s METAL, CHIP 150 5% 1/10W
R689	1-216-029-00 s METAL, CHIP 150 5% 1/10W
R690	1-216-697-91 s METAL, CHIP 82K 0.50% 1/10W
R691 R692	1-216-697-91 s METAL, CHIP 82K 0.50% 1/10W 1-216-697-91 s METAL, CHIP 82K 0.50% 1/10W
R700	1-216-295-00 s METAL, CHIP 0 5% 1/10W
R701	1-216-295-00 s METAL, CHIP 0 5% 1/10W
R702	1-216-295-00 s METAL, CHIP 0 5% 1/10W
R703	1-216-295-00 s METAL, CHIP 0 5% 1/10W
R704	1-216-651-11 s METAL, CHIP 1K 0.5% 1/10W
R705	1-216-651-11 s METAL, CHIP 1K 0.5% 1/10W
R706	1-216-651-11 s METAL, CHIP 1K 0.5% 1/10W
RV100	1-238-852-11 s RES, ADJ, CERMET 470
RV101	1-238-852-11 s RES, ADJ, CERMET 470
RV102	1-238-852-11 s RES, ADJ, CERMET 470
RV301	1-238-853-11 s RES, ADJ, METAL 1K
RV302	1-238-852-11 s RES, ADJ, CERMET 470

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Ref. No.
or Q'ty Part No.
                                              SP Description
                    1-238-852-11 s RES, ADJ, CERMET 470
1-238-852-11 s RES, ADJ, CERMET 470
1-238-853-11 s RES, ADJ, METAL 1K
1-238-853-11 s RES, ADJ, METAL 1K
1-238-853-11 s RES, ADJ, METAL 1K
RV303
RV304
RV305
RV306
RV307
                    1-238-853-11 s RES, ADJ, METAL 1K
1-238-854-11 s RES, ADJ, METAL 2.2K
1-238-854-11 s RES, ADJ, METAL 2.2K
1-238-854-11 s RES, ADJ, METAL 2.2K
1-238-853-11 s RES, ADJ, METAL 1K
RV308
RV309
RV310
RV311
RV312
                     1-238-857-11 s RES, ADJ, CERMET 22K
1-238-857-11 s RES, ADJ, CERMET 22K
1-238-857-11 s RES, ADJ, CERMET 22K
RV313
RV314
RV315
S1
                     1-571-120-11 s SWITCH, SLIDE
                    1-579-738-21 s VIBRATOR, CRYSTAL 14.318182MHz
1-760-274-11 s OSCILLATOR, CRYSTAL 24.576MHz
1-579-738-21 s VIBRATOR, CRYSTAL 14.318182MHz
X1
X2
X301
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AT-115 BOARD	(AT-115 BOARD)
Ref. No. or Q'ty Part No. SP Description	Ref. No. or Q'ty Part No. SP Description
1pc A-8313-839-A o MOUNTED CIRCUIT BOARD, AT-115	CN1 1-691-542-21 s CONNECTOR, BOARD TO BOARD 48P
C1 1-104-914-11 s TANTALUM 22uF 20% 16V	CN2 1-580-756-21 o PIN, CONNECTOR 7P
C2 1-164-156-11 s CERAMIC 0.1uF 25V C3 1-164-156-11 s CERAMIC 0.1uF 25V C4 1-113-991-11 s TANTAL 33uF 20% 16V C5 1-113-991-11 s TANTAL 33uF 20% 16V C6 1-113-991-11 s TANTAL 33uF 20% 16V	D1 8-719-105-91 s DIODE RD5.6M-B2 D2 8-719-024-81 s DIODE 1SS300-TE85L D3 8-719-024-81 s DIODE 1SS300-TE85L D4 8-719-800-76 s DIODE 1SS226
C7	D5 8-719-800-76 s DIODE 1SS226 D6 8-719-024-81 s DIODE 1SS300-TE85L D7 8-719-024-81 s DIODE 1SS300-TE85L
C10 1-104-913-11 s TANTALUM, CHIP 10uF 20% 16V C11 1-164-156-11 s CERAMIC 0.1uF 25V C12 1-104-852-11 s TANTALUM, CHIP 22uF 20% 10V C13 1-104-852-11 s TANTALUM, CHIP 22uF 20% 10V C14 1-164-156-11 s CERAMIC 0.1uF 25V C15 1-162-927-11 s CERAMIC, CHIP 100PF 5% 50V	IC1 8-759-066-61 s IC TC4053BFS IC2 8-759-082-61 s IC TC4W53FU IC3 8-759-173-16 s IC TL062CPW IC5 8-759-700-07 s IC NJM2903M IC6 8-759-209-90 s IC TC4S71F
C16 1-135-177-21 s TANTALUM, CHIP 1uF 10% 25V C17 1-135-177-21 s TANTALUM, CHIP 1uF 10% 25V C18 1-162-927-11 s CERAMIC, CHIP 100PF 5% 50V C19 1-164-156-11 s CERAMIC 0.1uF 25V C20 1-104-914-11 s TANTALUM 22uF 20% 16V	IC7 8-759-173-16 s IC TL062CPW IC8 8-759-989-91 s IC TL7705ACPS IC9 8-759-925-74 s IC SN74HC04ANS IC10 8-759-461-96 s IC UPD78P218AGC-AB8-S10V1.00 IC11 8-759-635-27 s IC M62352GP
C21 1-104-852-11 s TANTALUM, CHIP 22uF 20% 10V C22 1-164-156-11 s CERAMIC 0.1uF 25V C23 1-107-826-11 s CERAMIC 0.1uF 10% 16V	IC12 8-759-551-68 s IC M6M80021FP IC13 8-759-054-56 s IC SN75179BPS IC14 8-759-082-61 s IC TC4W53FU
C24 1-104-913-11 s TANTALUM, CHIP 10uF 20% 16V C25 1-104-851-11 s TANTALUM, CHIP 10uF 20% 10V	Q1 8-729-026-53 s TRANSISTOR 2SA1576A-T106-QR Q2 8-729-026-53 s TRANSISTOR 2SA1576A-T106-QR Q3 8-729-026-53 s TRANSISTOR 2SA1576A-T106-QR
C26 1-104-851-11 s TANTALUM, CHIP 10uF 20% 10V C27 1-104-852-11 s TANTALUM, CHIP 22uF 20% 10V C28 1-164-156-11 s CERAMIC 0.1uF 25V C29 1-164-156-11 s CERAMIC 0.1uF 25V	Q4 8-729-905-35 s TRANSISTOR 2SC4081R Q5 8-729-907-00 s TRANSISTOR DTC114EU Q6 8-729-402-19 s TRANSISTOR XN6501 Q7 8-729-402-19 s TRANSISTOR XN6501
C30 1-104-823-11 s TANTALUM, CHIP 47uF 20% 16V C31 1-164-156-11 s CERAMIC 0.1uF 25V C32 1-104-913-11 s TANTALUM, CHIP 10uF 20% 16V	Q7 8-729-402-19 s TRANSISTOR XN6501 Q8 8-729-905-35 s TRANSISTOR 2SC4081R Q9 8-729-402-84 s TRANSISTOR XN4601 Q10 8-729-402-84 s TRANSISTOR XN4601
C33 1-104-913-11 s TANTALUM, CHIP 10uF 20% 16V C34 1-126-392-11 s ELECT, CHIP 100uF 20% 6.3V C35 1-162-957-11 s CERAMIC 220PF 5% 50V	Q11 8-729-109-44 s TRANSISTOR 2SK94 Q12 8-729-905-35 s TRANSISTOR 2SC4081R
C36 1-164-156-11 s CERAMIC 0.1uF 25V C37 1-113-682-11 s TANTALUM 33uF 20% 10V C38 1-164-156-11 s CERAMIC 0.1uF 25V	Q13 8-729-905-35 s TRANSISTOR 2SC4081R Q14 8-729-402-84 s TRANSISTOR XN4601 Q15 8-729-402-19 s TRANSISTOR XN6501
C39 1-104-913-11 s TANTALUM, CHIP 10uF 20% 16V C40 1-164-156-11 s CERAMIC 0.1uF 25V	016 8-729-403-32 s TRANSISTOR XN6534 017 8-729-143-13 s TRANSISTOR 2SC4176-B34 018 8-729-028-73 s TRANSISTOR DTA114EUA-T106 019 8-729-026-53 s TRANSISTOR 2SA1576A-T106-QR
C41 1-164-156-11 s CERAMIC 0.1uF 25V C42 1-113-682-11 s TANTALUM 33uF 20% 10V C43 1-104-913-11 s TANTALUM, CHIP 10uF 20% 16V	Q20 8-729-028-73 s TRANSISTOR DTA114EUA-T106
C44 1-164-156-11 s CERAMIC 0.1uF 25V C45 1-113-682-11 s TANTALUM 33uF 20% 10V	Q21 8-729-800-37 s TRANSISTOR 2SD1048-X7 Q22 8-729-807-87 s TRANSISTOR 2SB1295-UL6
C46 1-164-156-11 s CERAMIC 0.1uF 25V C47 1-164-156-11 s CERAMIC 0.1uF 25V C48 1-135-210-11 s TANTALUM 4.7uF 10% 10V C49 1-162-918-11 s CERAMIC, CHIP 18PF 5% 50V C50 1-162-918-11 s CERAMIC, CHIP 18PF 5% 50V	R1 1-216-809-11 s METAL, CHIP 100 5% 1/16W R2 1-216-809-11 s METAL, CHIP 100 5% 1/16W R3 1-216-809-11 s METAL, CHIP 100 5% 1/16W R4 1-216-830-11 s METAL, CHIP 5.6K 5% 1/16W R5 1-216-830-11 s METAL, CHIP 5.6K 5% 1/16W
C51	R6 1-216-830-11 s METAL, CHIP 5.6K 5% 1/16W R7 1-216-830-11 s METAL, CHIP 5.6K 5% 1/16W R8 1-216-833-11 s METAL, CHIP 10K 5% 1/16W R9 1-216-833-11 s METAL, CHIP 10K 5% 1/16W R10 1-216-827-11 s METAL, CHIP 3.3K 5% 1/16W
C56 1-164-156-11 s CERAMIC 0.1uF 25V C57 1-164-004-11 s CERAMIC, CHIP 0.1uF 10% 25V	R11 1-216-833-11 s METAL, CHIP 10K 5% 1/16W R12 1-216-833-11 s METAL, CHIP 10K 5% 1/16W

R70

R71

1-216-829-11 s METAL, CHIP 4.7K 5% 1/16W 1-216-857-11 s METAL, CHIP 1M 5% 1/16W

1-216-813-11 s METAL, CHIP 220 5% 1/16W

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CN-1396 BOARD
                                                                        CN-1401 BOARD
                                                                        Ref. No.
Ref No
                                                                        or Q'ty Part No.
or O'ty Part No.
                       SP Description
                                                                                                SP Description
          A-8313-454-A o MOUNTED CIRCUIT BOARD, CN-1396
                                                                        1pc
                                                                                  A-8313-449-A o MOUNTED CIRCUIT BOARD, CN-1401
          3-184-116-01 s INSULATOR, SOCKET
1pc
          7-621-772-10 s SCREW +B 2X4
                                                                        CN101
                                                                                  1-573-806-21 s CONNECTOR (1.5MM)(SMD) 6P MALE
2pcs
          7-621-770-87 s SCREW +B 2.6X5
                                                                        CN102
                                                                                   1-562-221-51 s CONNECTOR, FEMALE, 12P
4pcs
          7-685-546-14 s SCREW +BTP 3X8 TYPE2 N-S
4pcs
                                                                                  8-719-158-55 s DIODE RD15SB
8-719-158-55 s DIODE RD15SB
                                                                        D101
          1-163-038-91 s CERAMIC 0.1uF 25V 1-110-569-11 s TANTAL 47uF 20% 6.3V
C102
                                                                        D102
                                                                                  8-719-158-55 s DIODE RD15SB
C103
                                                                        D103
          1-163-259-91 s CERAMIC 220PF 5% 50V
1-163-259-91 s CERAMIC 220PF 5% 50V
C104
                                                                        D104
                                                                                   8-719-158-55 s DIODE RD15SB
C105
                                                                        D105
                                                                                   8-719-158-55 s DIODE RD15SB
          1\text{-}691\text{-}865\text{-}11 s CONNECTOR, SQUARE TYPE 50P 1\text{-}691\text{-}865\text{-}11 s CONNECTOR, SQUARE TYPE 50P
                                                                                  1-239-400-11 s FILTER, CHIP EMI
CN102
                                                                        FL101
                                                                                  1-239-400-11 s FILTER, CHIP EMI
                                                                        FL102
CN103
                                                                                  1-239-400-11 s FILTER, CHIP EMI
          1-695-581-11 s CONNECTOR, D- SUB
CN104
                                                                        FL103
CN107
          1-566-728-21 s TERMINAL, S
                                                                        FL104
                                                                                  1-239-400-11 s FILTER, CHIP EMI
CN108
          1-770-081-12 s CONNECTOR, BNC
CN109
          1-750-668-11 s CONNECTOR, BNC
          1-540-256-21 s SOCKET, SYNCHRONIZE
CN110
D102
          8-719-106-43 s DIODE RD9.1M-B1
          8-719-106-43 s DIODE RD9.1M-B1
                                                                        CN-1462 BOARD
D107
D108
          8-719-106-43 s DIODE RD9.1M-B1
D109
          8-719-106-43 s DIODE RD9.1M-B1
                                                                        Ref. No.
          8-719-106-43 s DIODE RD9.1M-B1
                                                                        or Q'ty Part No.
                                                                                                SP Description
D110
D111
          8-719-106-43 s DIODE RD9.1M-B1
                                                                                  1-164-156-11 s CERAMIC 0.1uF 25V
                                                                                  1-135-091-00 s TANTALUM, CHIP 1uF 20% 16V
1-164-156-11 s CERAMIC 0.1uF 25V
          8-719-106-43 s DIODE RD9.1M-B1
                                                                        C302
D112
          8-719-106-43 s DIODE RD9.1M-B1
                                                                        C303
D113
          8-719-106-43 s DIODE RD9.1M-B1
D114
                                                                        CN301
          8-719-106-43 s DIODE RD9.1M-B1
                                                                                  1-695-209-21 s PIN, CONNECTOR (PC BOARD) 15P
D115
          8-719-106-43 s DIODE RD9.1M-B1
                                                                        D301
                                                                                   8-719-158-15 s DIODE RD5.6SB
D116
          8-719-106-43 s DIODE RD9.1M-B1
                                                                        D302
                                                                                   8-719-158-15 s DIODE RD5.6SB
D117
          8-719-106-43 s DIODE RD9.1M-B1
                                                                                   8-719-158-15 s DIODE RD5.6SB
                                                                        D303
D118
          8-719-106-43 s DIODE RD9.1M-B1
8-719-106-43 s DIODE RD9.1M-B1
                                                                                  8-719-158-15 s DIODE RD5.6SB
8-719-158-15 s DIODE RD5.6SB
8-719-158-15 s DIODE RD5.6SB
                                                                        D304
D119
                                                                        D305
D120
                                                                        D306
                                                                                   8-719-158-15 s DIODE RD5.6SB
FL102
          1-233-283-11 s FILTER, EMI (SMD)
FL107
          1-233-283-11 s FILTER, EMI (SMD)
                                                                        D307
                                                                                   8-719-158-15 s DIODE RD5.6SB
          1-233-283-11 s FILTER, EMI (SMD)
                                                                        D308
                                                                                   8-719-158-15 s DIODE RD5.6SB
FL108
FL109
          1-233-283-11 s FILTER, EMI (SMD)
                                                                        D309
                                                                                   8-719-158-15 s DIODE RD5.6SB
          1-233-283-11 s FILTER, EMI (SMD)
                                                                        D310
                                                                                   8-719-158-15 s DIODE RD5.6SB
FL110
          1-233-283-11 s FILTER, EMI
                                         (SMD)
                                                                        D311
                                                                                   8-719-158-15 s DIODE RD5.6SB
FL111
          1-233-283-11 s FILTER, EMI (SMD)
                                                                        D312
                                                                                   8-719-158-15 s DIODE RD5.6SB
FL112
          1-233-283-11 s FILTER, EMI (SMD)
FL113
FL114
          1-233-283-11 s FILTER, EMI
                                         (SMD)
                                                                        T<sub>3</sub>01
                                                                                  1-412-031-11 s INDUCTOR CHIP 47uH
FL115
          1-233-283-11 s FILTER, EMI (SMD)
FL116
          1-233-283-11 s FILTER, EMI (SMD)
          1-233-283-11 s FILTER, EMI (SMD)
FL117
          1-233-283-11 s FILTER, EMI (SMD)
FL118
          1-233-283-11 s FILTER, EMI (SMD)
FL119
                                                                        LED-280 BOARD
л101
          1-507-792-00 s.JACK
                                                                        Ref. No.
SW101
          1-516-925-21 s SWITCH, DIP 8-CKT
                                                                        or Q'ty Part No.
                                                                                                SP Description
                                                                        1pc
                                                                                   1-665-437-11 o PRINTED CIRCUIT BOARD, LED-280
                                                                                   3-174-895-01 o HOLDER, LED
                                                                        1pc
                                                                        CN201
                                                                                  1-506-481-11 s CONNECTOR, 2P, MALE
                                                                                   8-719-920-05 s SLP281C-50
                                                                        D201.
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8-729-905-36 s TRANSISTOR 2SC4081-S

0211

MB-725 BOARD	PA-200 BOARD
Ref. No. or Q'ty Part No. SP Description	Ref. No. or Q'ty Part No. SP Description
1pc A-8313-453-A o MOUNTED CIRCUIT BOARD, MB-725	C100 1-113-642-11 s TANTAL 47uF 20% 10V C101 1-164-004-11 s CERAMIC, CHIP 0.1uF 10% 25V
C1 1-163-038-91 s CERAMIC 0.1uF 25V C2 1-163-038-91 s CERAMIC 0.1uF 25V C3 1-126-936-11 s ELECT 3300uF 20% 16V	C100 1-113-642-11 s TANTAL 47uF 20% 10V C101 1-164-004-11 s CERAMIC, CHIP 0.1uF 10% 25V C102 1-164-004-11 s CERAMIC, CHIP 0.1uF 10% 25V C103 1-135-177-21 s TANTALUM, CHIP 1uF 10% 25V C104 1-113-981-11 s TANTALUM 22uF 20% 20V
CN1 1-778-258-11 o CONNECTOR, BOARD TO BOARD 124P CN2 1-778-258-11 o CONNECTOR, BOARD TO BOARD 124P CN5 1-770-291-11 o PIN, CONNECTOR (PC BOARD) 7P CN7 1-764-101-11 s PIN, CONNECTOR (PC BOARD) 2P CN8 1-506-487-11 s CONNECTOR, 8P, MALE	C105 1-107-687-11 s TANTALUM 3.3uF 20% 20V C106 1-113-981-11 s TANTALUM 22uF 20% 20V C107 1-164-004-11 s CERAMIC, CHIP 0.1uF 10% 25V C108 1-113-642-11 s TANTAL 47uF 20% 10V C109 1-164-004-11 s CERAMIC, CHIP 0.1uF 10% 25V
CN9 1-506-491-11 s CONNECTOR, 12P, MALE CN10 1-564-505-11 s PLUG, CONNECTOR 2P	C110 1-113-642-11 s TANTAL 47uF 20% 10V C111 1-164-004-11 s CERAMIC, CHIP 0.1uF 10% 25V
D1 8-719-800-76 s DIODE 1SS226	CN100 1-764-082-21 s PIN, CONNECTOR (PC BOARD) 11P CN101 1-580-055-21 s PIN, CONNECTOR 2P
F1	CN102 1-764-081-21 s PIN, CONNECTOR (PC BOARD) 9P
FH1 1-533-189-11 s HOLDER, FUSE FH2 1-533-189-11 s HOLDER, FUSE	D101 8-719-159-10 s DIODE RD5.1SB-T2
FL1 1-421-773-11 s FILTER, NOISE REMOVAL	IC101 8-759-031-84 s IC SC7S04F IC102 8-759-031-84 s IC SC7S04F IC103 8-752-052-72 s IC CXA1439M
IC1 8-759-518-85 s IC S16MD01	
Q1 8-729-027-56 s TRANSISTOR DTC143TKA Q2 8-729-027-56 s TRANSISTOR DTC143TKA	L100 1-408-789-21 s INDUCTOR CHIP 100uH L101 1-408-789-21 s INDUCTOR CHIP 100uH L102 1-408-789-21 s INDUCTOR CHIP 100uH
R1 1-216-033-00 s METAL, CHIP 220 5% 1/10W	Q100 8-729-926-19 s TRANSISTOR 2SC4103-Q
RY1 1-515-648-11 s RELAY, POWER (DS)	R100 1-216-041-00 s METAL, CHIP 470 5% 1/10W R101 1-216-295-00 s METAL, CHIP 0 5% 1/10W R102 1-216-295-00 s METAL, CHIP 0 5% 1/10W R103 1-216-809-11 s METAL, CHIP 100 5% 1/16W

PA-201 F		PA-202 BOARD						
Ref. No.	Part No. SP Description	Ref. No.	Part No. SP Description					
C200 C201 C202 C203 C204	1-113-642-11 s TANTAL 47uf 20% 10V 1-164-004-11 s CERAMIC, CHIP 0.1uf 10% 25V 1-164-004-11 s CERAMIC, CHIP 0.1uf 10% 25V 1-135-177-21 s TANTALUM, CHIP 1uf 10% 25V 1-113-981-11 s TANTALUM 22uf 20% 20V	C300 C301 C302 C303	1-113-642-11 s TANTAL 47uF 20% 10V 1-164-004-11 s CERAMIC, CHIP 0.1uF 10% 25V 1-164-004-11 s CERAMIC, CHIP 0.1uF 10% 25V 1-135-177-21 s TANTALUM, CHIP 1uF 10% 25V 1-113-981-11 s TANTALUM 22uF 20% 20V					
C205 C206 C207 C208 C209	1-107-687-11 s TANTALUM 3.3uF 20% 20V 1-113-981-11 s TANTALUM 22uF 20% 20V 1-164-004-11 s CERAMIC, CHIP 0.1uF 10% 25V 1-113-642-11 s TANTAL 47uF 20% 10V 1-164-004-11 s CERAMIC, CHIP 0.1uF 10% 25V	C305 C306 C307 C308 C309	1-107-687-11 s TANTALUM 3.3uF 20% 20V 1-113-981-11 s TANTALUM 22uF 20% 20V 1-164-004-11 s CERAMIC, CHIP 0.1uF 10% 25V 1-113-642-11 s TANTAL 47uF 20% 10V 1-164-004-11 s CERAMIC, CHIP 0.1uF 10% 25V					
			1-113-642-11 s TANTAL 47uF 20% 10V 1-164-004-11 s CERAMIC, CHIP 0.1uF 10% 25V					
CN200 CN201 CN202	1-764-082-21 s PIN, CONNECTOR (PC BOARD) 11P 1-580-055-21 s PIN, CONNECTOR 2P 1-764-081-21 s PIN, CONNECTOR (PC BOARD) 9P		1-695-320-31 o CONNECTOR (1.5MM)(SMD) 2P MALE 8-719-159-10 s DIODE RD5.1SB-T2					
D201 IC201 IC202	8-719-159-10 s DIODE RD5.1SB-T2 8-759-031-84 s IC SC7S04F 8-759-031-84 s IC SC7S04F 8-752-052-72 s IC CXA1439M 1-408-789-21 s INDUCTOR CHIP 100uH	IC301 IC302 IC303	8-759-031-84 s IC SC7S04F 8-759-031-84 s IC SC7S04F 8-752-052-72 s IC CXA1439M					
IC203	8-752-052-72 s IC CXA1439M 1-408-789-21 s INDUCTOR CHIP 100uH	L300 L301 L302	1-408-789-21 s INDUCTOR CHIP 100uH 1-408-789-21 s INDUCTOR CHIP 100uH 1-408-789-21 s INDUCTOR CHIP 100uH					
L201 L202	1-408-789-21 s INDUCTOR CHIP 100uH 1-408-789-21 s INDUCTOR CHIP 100uH	Q300	8-729-926-19 s TRANSISTOR 2SC4103-Q					
	8-729-926-19 s TRANSISTOR 2SC4103-Q 1-216-041-00 s METAL, CHIP 470 5% 1/10W	R301	1-216-041-00 s METAL, CHIP 470 5% 1/10W 1-216-295-00 s METAL, CHIP 0 5% 1/10W 1-216-295-00 s METAL, CHIP 0 5% 1/10W					
R201 R202 R203	1-216-041-00 s METAL, CHIP 470 5% 1/10W 1-216-295-00 s METAL, CHIP 0 5% 1/10W 1-216-295-00 s METAL, CHIP 0 5% 1/10W 1-216-809-11 s METAL, CHIP 100 5% 1/16W		1-216-809-11 s METAL, CHIP 100 5% 1/16W					

PR-228 B	OARD	(PR-228	BOARD)
Ref. No.	Part No. SP Description	Ref. No.	Part No. SP Description
1pc	A-8313-463-A o MOUNTED CIRCUIT BOARD, PR-228	C221	1-162-911-11 s CERAMIC, CHIP 6PF 50V
C1 C2 C3 C4	1-162-927-11 s CERAMIC, CHIP 100PF 5% 50V 1-126-395-11 s ELECT, CHIP 22uF 20% 16V 1-107-685-11 s TANTALUM 15uF 20% 6.3V 1-126-391-11 s ELECT, CHIP 47uF 20% 6.3V	C225 C226 C227 C229	1-135-177-21 s TANTALUM, CHIP 1uF 10% 25V 1-164-315-11 s CERAMIC 470PF 5% 50V 1-104-563-11 s FILM, CHIP 0.1uF 5% 16V 1-162-970-11 s CERAMIC, CHIP 0.01uF 10% 25V
C5 C6 C7 C8 C9	1-126-391-11 s ELECT, CHIP 47uF 20% 6.3V 1-164-156-11 s CERAMIC 0.1uF 25V 1-135-177-21 s TANTALUM, CHIP 1uF 10% 25V 1-107-689-21 s TANTALUM 1uF 20% 35V 1-135-072-21 s TANTALUM, CHIP 0.22uF 10% 35V	C230 C231 C301 C302 C304	1-135-177-21 s TANTALUM, CHIP 1uF 10% 25V 1-107-826-11 s CERAMIC 0.1uF 10% 16V 1-164-156-11 s CERAMIC 0.1uF 25V 1-162-970-11 s CERAMIC, CHIP 0.01uF 10% 25V 1-104-913-11 s TANTALUM, CHIP 10uF 20% 16V
C10 C11 C12 C13 C14	Part No. SP Description A-8313-463-A o MOUNTED CIRCUIT BOARD, PR-228 1-162-927-11 s CERAMIC, CHIP 100PF 5% 50V 1-126-395-11 s ELECT, CHIP 22uF 20% 16V 1-107-685-11 s TANTALUM 15uF 20% 6.3V 1-126-391-11 s ELECT, CHIP 47uF 20% 6.3V 1-126-391-11 s ELECT, CHIP 47uF 20% 6.3V 1-126-391-11 s ELECT, CHIP 47uF 20% 6.3V 1-164-156-11 s CERAMIC 0.1uF 25V 1-135-177-21 s TANTALUM, CHIP 1uF 10% 25V 1-107-689-21 s TANTALUM, CHIP 0.22uF 10% 35V 1-107-689-21 s TANTALUM, CHIP 1uF 10% 25V 1-107-689-21 s TANTALUM, CHIP 1uF 10% 25V 1-107-689-21 s TANTALUM, CHIP 1uF 10% 25V 1-104-913-11 s TANTALUM, CHIP 10uF 20% 16V 1-104-913-11 s TANTALUM, CHIP 10uF 20% 16V 1-104-752-11 s TANTALUM, CHIP 33uF 20% 6.3V	C305 C306 C307 C309 C310	1-107-685-11 s TANTALUM 15uF 20% 6.3V 1-164-156-11 s CERAMIC 0.1uF 25V 1-164-156-11 s CERAMIC 0.1uF 25V 1-135-180-21 s TANTALUM, CHIP 3.3uF 20% 6.3V 1-164-156-11 s CERAMIC 0.1uF 25V
C15 C16 C17 C18 C19	1-164-156-11 s CERAMIC 0.1uF 25V 1-104-752-11 s TANTALUM, CHIP 33uF 20% 6.3V	C314 C316	1-164-156-11 s CERAMIC 0.1uF 25V 1-162-911-11 s CERAMIC, CHIP 6PF 50V 1-164-156-11 s CERAMIC 0.1uF 25V 1-162-927-11 s CERAMIC, CHIP 100PF 5% 50V 1-164-156-11 s CERAMIC 0.1uF 25V
C101 C103 C104 C105 C106	1-162-970-11 s CERAMIC 0.1UF 25V 1-162-970-11 s CERAMIC, CHIP 0.01uF 10% 25V 1-104-913-11 s TANTALUM, CHIP 10uF 20% 16V 1-107-685-11 s TANTALUM 15uF 20% 6.3V 1-164-156-11 s CERAMIC 0.1uF 25V 1-164-156-11 s CERAMIC 0.1uF 25V	C321 C322	1-164-156-11 s CERAMIC 0.1uF 25V 1-135-180-21 s TANTALUM, CHIP 3.3uF 20% 6.3V 1-135-180-21 s TANTALUM, CHIP 3.3uF 20% 6.3V 1-164-156-11 s CERAMIC 0.1uF 25V 1-162-911-11 s CERAMIC, CHIP 6PF 50V
C108 C109 C110 C111	1-135-180-21 s TANTALUM, CHIP 3.3uF 20% 6.3V 1-164-156-11 s CERAMIC 0.1uF 25V 1-164-156-11 s CERAMIC 0.1uF 25V 1-162-911-11 s CERAMIC, 0.1uF 25V		1-164-315-11 s CERAMIC 470PF 5% 50V 1-104-563-11 s FILM, CHIP 0.1uF 5% 16V 1-162-970-11 s CERAMIC, CHIP 0.01uF 10% 25V 1-107-826-11 s CERAMIC 0.1uF 10% 16V 1-113-990-11 s TANTALUM 15uF 20% 16V
0111	1-162-927-11 s CERAMIC 0.1UF 25V 1-162-927-11 s CERAMIC 0.1UF 25V 1-164-156-11 s CERAMIC 0.1UF 25V 1-164-156-11 s CERAMIC 0.1UF 25V 1-135-180-21 s TANTALUM, CHIP 3.3UF 20% 6.3V 1-135-180-21 s TANTALUM, CHIP 3.3UF 20% 6.3V 1-164-156-11 s CERAMIC 0.1UF 25V 1-162-911-11 s CERAMIC 0.1UF 25V 1-104-913-11 s TANTALUM, CHIP 10UF 20% 16V	C402 C403 C406 C407 C408	1-107-685-11 s TANTALUM 15uF 20% 6.3V 1-104-913-11 s TANTALUM, CHIP 10uF 20% 16V 1-113-990-11 s TANTALUM 15uF 20% 16V 1-107-685-11 s TANTALUM 15uF 20% 6.3V 1-104-913-11 s TANTALUM, CHIP 10uF 20% 16V
C120 C121 C123 C126 C127	1-164-156-11 s CERAMIC 0.1uF 25V 1-162-911-11 s CERAMIC, CHIP 6PF 50V 1-104-913-11 s TANTALUM, CHIP 10uF 20% 16V 1-164-315-11 s CERAMIC 470PF 5% 50V 1-104-563-11 s FILM, CHIP 0.1uF 5% 16V	C411 C412 C413 C414 C415	1-107-685-11 s TANTALUM 15uF 20% 6.3V 1-162-924-11 s CERAMIC 56PF 5% 50V 1-162-919-11 s CERAMIC, CHIP 22PF 5% 50V 1-165-176-11 s CERAMIC, CHIP 0.047uF 10% 16V 1-135-177-21 s TANTALUM, CHIP 1uF 10% 25V
C129 C130 C201	1-162-970-11 s CERAMIC, CHIP 0.01uF 10% 25V 1-107-826-11 s CERAMIC 0.1uF 10% 16V 1-162-970-11 s CERAMIC, CHIP 0.01uF 10% 25V 1-104-913-11 s TANTALUM, CHIP 10uF 20% 16V 1-107-685-11 s TANTALUM 15uF 20% 6.3V	C416 C417 C418 C420 C421	1-135-177-21 s TANTALUM, CHIP 1UF 10% 25V 1-164-156-11 s CERAMIC 0.1UF 25V 1-162-920-11 s CERAMIC, CHIP 27PF 5% 50V 1-162-923-11 s CERAMIC, CHIP 47PF 5% 50V 1-162-919-11 s CERAMIC, CHIP 22PF 5% 50V
C205 C206 C208	1-164-156-11 s CERAMIC 0.1uF 25V 1-164-156-11 s CERAMIC 0.1uF 25V 1-135-180-21 s TANTALUM, CHIP 3.3uF 20% 6.3V	C423 C424 C501	1-104-752-11 s TANTALUM, CHIP 33uF 20% 6.3V 1-162-911-11 s CERAMIC, CHIP 6PF 50V 1-162-916-11 s CERAMIC, CHIP 12PF 5% 50V 1-113-990-11 s TANTALUM 15uF 20% 16V 1-107-685-11 s TANTALUM 15uF 20% 6.3V
C211 C212 C213 C215 C217	1-164-156-11 s CERAMIC 0.1uF 25V 1-162-911-11 s CERAMIC 0.1uF 25V 1-162-921-11 s CERAMIC 0.1uF 25V 1-162-927-11 s CERAMIC 0.1uF 25V 1-164-156-11 s CERAMIC 0.1uF 25V 1-164-156-11 s CERAMIC 0.1uF 25V 1-164-156-11 s CERAMIC 0.1uF 25V	C503 C506 C507 C508 C511	1-104-913-11 s TANTALUM, CHIP 10uF 20% 16V 1-113-990-11 s TANTALUM 15uF 20% 16V 1-107-685-11 s TANTALUM 15uF 20% 6.3V 1-104-913-11 s TANTALUM, CHIP 10uF 20% 16V 1-107-685-11 s TANTALUM 15uF 20% 6.3V
C217 C218 C219 C220	1-135-180-21 s TANTALUM, CHIP 3.3uF 20% 6.3V	C513 C514	1-162-924-11 s CERAMIC 56PF 5% 50V 1-162-919-11 s CERAMIC, CHIP 22PF 5% 50V 1-165-176-11 s CERAMIC, CHIP 0.047uF 10% 16V 1-135-177-21 s TANTALUM, CHIP 1uF 10% 25V

8-729-905-36 s TRANSISTOR 2SC4081-S

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Ref. No. or Q'ty	Part No. SP Description	Ref. No. or Q'ty Part No. SP Description
R214 R215 R216 R217 R221	Part No. SP Description 1-216-809-11 s METAL, CHIP 100 5% 1/16W 1-216-817-11 s METAL, CHIP 470 5% 1/16W 1-216-829-11 s METAL, CHIP 4.7K 5% 1/16W 1-216-821-11 s METAL, CHIP 1K 5% 1/16W 1-216-821-11 s METAL, CHIP 1K 5% 1/16W	R327 1-216-817-11 s METAL, CHIP 470 5% 1/16W R328 1-216-845-11 s METAL, CHIP 100K 5% 1/16W R329 1-216-845-11 s METAL, CHIP 100K 5% 1/16W R330 1-216-821-11 s METAL, CHIP 1K 5% 1/16W R331 1-216-829-11 s METAL, CHIP 4.7K 5% 1/16W
R222 R223 R224 R226 R227	1-216-837-11 s METAL, CHIP 22K 5% 1/16W 1-216-809-11 s METAL, CHIP 100 5% 1/16W 1-216-809-11 s METAL, CHIP 100 5% 1/16W 1-216-817-11 s METAL, CHIP 470 5% 1/16W 1-216-845-11 s METAL, CHIP 100K 5% 1/16W	R332 1-216-824-11 s METAL, CHIP 1.8K 5% 1/16W R337 1-216-841-11 s METAL, CHIP 47K 5% 1/16W R338 1-216-837-11 s METAL, CHIP 22K 5% 1/16W R339 1-216-841-11 s METAL, CHIP 47K 5% 1/16W R340 1-216-837-11 s METAL, CHIP 22K 5% 1/16W
R228 R229 R231 R232 R233	1-216-845-11 s METAL, CHIP 100K 5% 1/16W 1-216-821-11 s METAL, CHIP 1K 5% 1/16W 1-216-829-11 s METAL, CHIP 4.7K 5% 1/16W 1-216-835-11 s METAL, CHIP 15K 5% 1/16W 1-216-837-11 s METAL, CHIP 22K 5% 1/16W	R341 1-216-837-11 s METAL, CHIP 22K 5% 1/16W R342 1-216-837-11 s METAL, CHIP 22K 5% 1/16W R343 1-216-848-11 s METAL, CHIP 180K 5% 1/16W R344 1-216-841-11 s METAL, CHIP 47K 5% 1/16W R345 1-216-841-11 s METAL, CHIP 47K 5% 1/16W
R237 R238 R239 R240 R241	1-216-821-11 s METAL, CHIP 1K 5% 1/16W 1-216-841-11 s METAL, CHIP 47K 5% 1/16W 1-216-845-11 s METAL, CHIP 100K 5% 1/16W 1-216-837-11 s METAL, CHIP 22K 5% 1/16W 1-216-841-11 s METAL, CHIP 47K 5% 1/16W	
R242 R243 R244 R245 R246	1-216-837-11 s METAL, CHIP 22K 5% 1/16W 1-216-837-11 s METAL, CHIP 22K 5% 1/16W 1-216-837-11 s METAL, CHIP 22K 5% 1/16W 1-216-848-11 s METAL, CHIP 180K 5% 1/16W 1-216-841-11 s METAL, CHIP 47K 5% 1/16W	
R247 R248 R249 R250 R251	1-216-841-11 s METAL, CHIP 47K 5% 1/16W 1-216-841-11 s METAL, CHIP 47K 5% 1/16W	R357 1-216-825-11 s METAL, CHIP 2.2K 5% 1/16W 1-218-692-11 s METAL, CHIP 1K 0.50% 1/16W 1-216-821-11 s METAL, CHIP 1K 5% 1/16W 1-218-692-11 s METAL, CHIP 1K 0.50% 1/16W 1-216-829-11 s METAL, CHIP 4.7K 5% 1/16W
R252 R253 R254 R255 R256	1-216-841-11 s METAL, CHIP 47K 5% 1/16W 1-216-841-11 s METAL, CHIP 47K 5% 1/16W 1-216-829-11 s METAL, CHIP 4.7K 5% 1/16W 1-216-841-11 s METAL, CHIP 47K 5% 1/16W 1-216-841-11 s METAL, CHIP 47K 5% 1/16W	R408 1-218-703-11 s METAL 3K 0.50% 1/16W R409 1-216-822-11 s METAL, CHIP 1.2K 5% 1/16W R410 1-216-864-11 s METAL, CHIP 0.5% 1/16W
R257 R259 R301 R302 R303	1-216-809-11 s METAL, CHIP 100 5% 1/16W 1-216-825-11 s METAL, CHIP 2.2K 5% 1/16W 1-216-829-11 s METAL, CHIP 4.7K 5% 1/16W 1-216-829-11 s METAL, CHIP 4.7K 5% 1/16W 1-216-829-11 s METAL, CHIP 4.7K 5% 1/16W	R411 1-216-829-11 s METAL, CHIP 4.7K 5% 1/16W 1-216-835-11 s METAL, CHIP 15K 5% 1/16W 1-216-829-11 s METAL, CHIP 4.7K 5% 1/16W 1-216-809-11 s METAL, CHIP 100 5% 1/16W 1-216-829-11 s METAL, CHIP 4.7K 5% 1/16W 1-216-829-11 s METAL, CHIP 4.7K 5% 1/16W
R304 R305 R306 R308 R309	1-216-809-11 s METAL, CHIP 100 5% 1/16W 1-216-831-11 s METAL, CHIP 6.8K 5% 1/16W 1-216-821-11 s METAL, CHIP 1K 5% 1/16W 1-216-809-11 s METAL, CHIP 100 5% 1/16W 1-216-840-11 s METAL, CHIP 39K 5% 1/16W	R424 1-216-825-11 s METAL, CHIP 2.2K 5% 1/16W R425 1-216-821-11 s METAL, CHIP 1K 5% 1/16W R426 1-216-825-11 s METAL, CHIP 2.2K 5% 1/16W R427 1-218-712-11 s METAL 6.8K 0.50% 1/16W R428 1-218-700-11 s METAL 2.2K 0.50% 1/16W
R310 R311 R312 R313 R314	1-216-821-11 s METAL, CHIP 1K 5% 1/16W 1-216-825-11 s METAL, CHIP 2.2K 5% 1/16W 1-216-809-11 s METAL, CHIP 100 5% 1/16W 1-216-833-11 s METAL, CHIP 10K 5% 1/16W 1-216-823-11 s METAL, CHIP 1.5K 5% 1/16W	R429 1-216-827-11 s METAL, CHIP 3.3K 5% 1/16W R430 1-216-797-11 s METAL, CHIP 10 5% 1/16W R431 1-216-797-11 s METAL, CHIP 10 5% 1/16W R432 1-216-797-11 s METAL, CHIP 10 5% 1/16W R433 1-216-797-11 s METAL, CHIP 10 5% 1/16W
R315 R316 R317 R318 R319	1-216-829-11 s METAL, CHIP 4.7K 5% 1/16W 1-216-829-11 s METAL, CHIP 4.7K 5% 1/16W 1-216-809-11 s METAL, CHIP 100 5% 1/16W 1-216-817-11 s METAL, CHIP 470 5% 1/16W 1-216-829-11 s METAL, CHIP 4.7K 5% 1/16W	R443 1-216-833-11 s METAL, CHIP 10K 5% 1/16W 1-216-833-11 s METAL, CHIP 10K 5% 1/16W R445 1-216-809-11 s METAL, CHIP 10O 5% 1/16W R448 1-218-692-11 s METAL, CHIP 1K 0.50% 1/16W R449 1-218-680-11 s METAL 330 0.50% 1/16W
R320 R324 R325 R326	1 214 027 11 a MERNI CUID 227 59 1/16W	R450 1-216-821-11 s METAL, CHIP 1K 5% 1/16W R451 1-218-680-11 s METAL 330 0.50% 1/16W R452 1-218-696-11 s METAL 1.5K 0.50% 1/16W R453 1-218-699-11 s METAL, CHIP 2K 0.50% 1/16W

1-218-692-11 s METAL, CHIP 1K 0.50% 1/16W 1-216-829-11 s METAL, CHIP 4.7K 5% 1/16W 1-218-692-11 s METAL, CHIP 1K 0.50% 1/16W 1-218-702-11 s METAL, CHIP 2.7K 0.50% 1/16W 1-218-703-11 s METAL 3K 0.50% 1/16W R607 1-216-822-11 s METAL, CHIP 1.2K 5% 1/16W R608 1-216-864-11 s METAL, CHIP 0 5% 1/16W 1-216-829-11 s METAL, CHIP 4.7K 5% 1/16W R609 R610 R620 1-216-835-11 s METAL, CHIP 15K 5% 1/16W 1-216-829-11 s METAL, CHIP 4.7K 5% 1/16W 1-216-809-11 s METAL, CHIP 100 5% 1/16W 1-216-829-11 s METAL, CHIP 4.7K 5% 1/16W R621 R622 R623

R524

R525

R526

R527

R528

R529

R530

R531

R532

R533

1-216-821-11 s METAL, CHIP 1K 5% 1/16W

1-218-700-11 s METAL 2.2K 0.50% 1/16W

1-216-825-11 s METAL, CHIP 2.2K 5% 1/16W 1-218-712-11 s METAL 6.8K 0.50% 1/16W

1-216-827-11 s METAL, CHIP 3.3K 5% 1/16W

1-216-797-11 s METAL, CHIP 10 5% 1/16W 1-216-797-11 s METAL, CHIP 10 5% 1/16W 1-216-797-11 s METAL, CHIP 10 5% 1/16W 1-216-797-11 s METAL, CHIP 10 5% 1/16W

(PR-228 BOARD)

(PR-228)	BOARD)			(PR-220)	BUARD)
Ref. No. or Q'ty	Part No. SP	Description		Ref. No. or Q'ty	Part N
R624 R625 R626 R627 R628	1-216-821-11 s 1-216-825-11 s 1-218-712-11 s	METAL, CHIP 2.2K 5% METAL, CHIP 1K 5% 1/ METAL, CHIP 2.2K 5% METAL 6.8K 0.50% 1/1 METAL 2.2K 0.50% 1/1	/16W 1/16W L6W	RV404 RV405 RV406 RV407 RV408	1-238- 1-238- 1-238- 1-238- 1-238-
R629 R630 R631 R632 R633	1-216-797-11 s 1-216-797-11 s 1-216-797-11 s	METAL, CHIP 3.3K 5% METAL, CHIP 10 5% 1, METAL, CHIP 10 5% 1, METAL, CHIP 10 5% 1, METAL, CHIP 10 5% 1,	/16W /16W /16W	RV501 RV504 RV505 RV506 RV507	1-238- 1-238- 1-238- 1-238- 1-238-
R641 R642 R643 R646 R647	1-216-833-11 s 1-216-809-11 s 1-218-692-11 s	METAL, CHIP 10K 5% 1 METAL, CHIP 10K 5% 1 METAL, CHIP 10O 5% 1 METAL, CHIP 1K 0.50% METAL 330 0.50% 1/16	L/16W L/16W & 1/16W	RV508 RV601 RV604 RV605 RV606	1-238- 1-238- 1-238- 1-238- 1-238-
R648 R649 R650 R651 R652	1-218-680-11 s 1-218-696-11 s 1-218-699-11 s	METAL, CHIP 1K 5% 1/16 METAL 330 0.50% 1/16 METAL 1.5K 0.50% 1/1 METAL, CHIP 2K 0.50% METAL, CHIP 470 0.50	5W L6W ∂ 1/16W	RV607 RV608	1-238- 1-238-
R653 R654 R655 R656 R658	1-218-691-11 s 1-218-692-11 s 1-218-658-11 s	METAL 47 0.50% 1/16V METAL 910 0.50% 1/16 METAL, CHIP 1K 0.50% METAL 39 0.50% 1/16V METAL 750 0.50% 1/16V	5W } 1/16W V		
R659 R660 R661 R662 R663	1-218-735-11 s 1-218-644-11 s 1-218-728-11 s	METAL 33 0.50% 1/16 METAL 62K 0.50% 1/16 METAL 10 0.50% 1/16 METAL 33K 0.50% 1/16 METAL 750 0.50% 1/16	6W N 6W		
R664 R665 R666 R667 R668	1-218-693-11 s 1-218-698-11 s 1-216-821-11 s	METAL, CHIP 5.6K 0.9 METAL 1.1K 0.50% 1/2 METAL 1.8K 0.50% 1/2 METAL, CHIP 1K 5% 1 METAL 3.9K 0.50% 1/3	16W 16W /16W		
R669 R670 R671 R672 R673	1-218-716-11 s 1-216-841-11 s 1-216-841-11 s	METAL 10K 0.50% 1/1 METAL 10K 0.50% 1/1 METAL, CHIP 47K 5% METAL, CHIP 47K 5% METAL, CHIP 47K 5%	6W 1/16W 1/16W		
R674 R675 R676 R681 R682	1-216-831-11 s 1-216-835-11 s 1-216-831-11 s	METAL, CHIP 1K 5% 1 METAL, CHIP 6.8K 5% METAL, CHIP 15K 5% METAL, CHIP 6.8K 5% METAL, CHIP 2.7K 5%	1/16W 1/16W 1/16W		
R683 R684 R685 R686 R687	1-216-825-11 s 1-216-825-11 s 1-216-817-11 s	METAL, CHIP 0 5% 1/ METAL, CHIP 2.2K 5% METAL, CHIP 2.2K 5% METAL, CHIP 470 5% METAL, CHIP 100K 5%	1/16W 1/16W 1/16W		
R688 R689 R695 R696 R697	1-216-845-11 s 1-216-864-11 s 1-218-696-11 s	METAL, CHIP 4.7K 5% METAL, CHIP 100K 5% METAL, CHIP 0 5% 1/ METAL 1.5K 0.50% 1/ METAL 1.8K 0.50% 1/	1/16W 16W 16W		
RV103 RV202 RV302 RV401	1-238-855-11 s 1-238-855-11 s	RES, ADJ, METAL 4.7 RES, ADJ, METAL 4.7 RES, ADJ, METAL 4.7 RES, ADJ, CERMET 47	K K		

SP Description No. 8-853-11 s RES, ADJ, METAL 1K 8-855-11 s RES, ADJ, METAL 4.7K 8-858-11 s RES, ADJ, METAL 47K 8-854-11 s RES, ADJ, METAL 2.2K 8-854-11 s RES, ADJ, METAL 2.2K 8-852-11 s RES, ADJ, CERMET 470 3-853-11 s RES, ADJ, METAL 1K 3-855-11 s RES, ADJ, METAL 4.7K 3-858-11 s RES, ADJ, METAL 47K 3-854-11 s RES, ADJ, METAL 2.2K 8-854-11 s RES, ADJ, METAL 2.2K 3-852-11 s RES, ADJ, CERMET 470 3-853-11 s RES, ADJ, METAL 1K 3-855-11 s RES, ADJ, METAL 4.7K 3-858-11 s RES, ADJ, METAL 47K 8-854-11 s RES, ADJ, METAL 2.2K 8-854-11 s RES, ADJ, METAL 2.2K

RM-171 1	BOARD	SY-243 B	 OARD		
Ref. No	Part No. SP Description	Ref. No.	Part No. SP	Description	
1pc	A-8313-470-A o MOUNTED CIRCUIT BOARD, RM-171	1pc 2pcs	A-8313-461-A o	MOUNTED CIRCUIT BOARD, LEVER, PC BOARD	SY-243
C1 C2	1-164-232-11 s CERAMIC, CHIP 0.01uF 10% 50V 1-104-851-11 s TANTALUM, CHIP 10uF 20% 10V	2pcs	7-626-320-11 s	PIN, SPRING 3X8	No. 1
CN1	1-764-007-11 s CONNECTOR (SMD) 12P MALE	BH1 BZ1	1-550-104-41 s 1-529-029-11 s	HOLDER, BATTERY	
R1 R2 R3 R4 R5	1-216-652-11 s METAL, CHIP 1.1K 0.5% 1/10W 1-216-655-11 s METAL, CHIP 1.5K 0.5% 1/10W 1-216-657-11 s METAL, CHIP 1.8K 0.5% 1/10W 1-216-660-11 s METAL, CHIP 2.4K 0.5% 1/10W 1-216-663-11 s METAL, CHIP 3.3K 0.5% 1/10W	C1 C2 C3 C4 C5	1-163-038-91 s 1-163-038-91 s 1-163-038-91 s 1-163-038-91 s	CERAMIC 0.1uF 25V CERAMIC 0.1uF 25V CERAMIC 0.1uF 25V CERAMIC 0.1uF 25V CERAMIC 0.1uF 25V	
R6 R7 R8 R9 R10	1-216-668-11 s METAL, CHIP 5.1K 0.5% 1/10W 1-216-652-11 s METAL, CHIP 1.1K 0.5% 1/10W 1-216-655-11 s METAL, CHIP 1.5K 0.5% 1/10W 1-216-657-11 s METAL, CHIP 1.8K 0.5% 1/10W 1-216-660-11 s METAL, CHIP 2.4K 0.5% 1/10W	C6 C7 C8 C9	1-124-778-00 s 1-163-038-91 s 1-163-038-91 s 1-126-193-11 s	ELECT 22uF 20% 6.3V CERAMIC 0.1uF 25V CERAMIC 0.1uF 25V ELECT 1uF 20% 50V CERAMIC 680PF 10% 50V	
R11 R12 R13 R14 R15	1-216-663-11 s METAL, CHIP 3.3K 0.5% 1/10W 1-216-668-11 s METAL, CHIP 5.1K 0.5% 1/10W 1-216-652-11 s METAL, CHIP 1.1K 0.5% 1/10W 1-216-655-11 s METAL, CHIP 1.5K 0.5% 1/10W 1-216-657-11 s METAL, CHIP 1.8K 0.5% 1/10W	C11 C12 C13 C14 C15	1-163-038-91 s 1-124-778-00 s 1-163-038-91 s 1-126-206-11 s	CERAMIC 0.1uF 25V ELECT 22uF 20% 6.3V CERAMIC 0.1uF 25V ELECT 100uF 20% 6.3V ELECT 100uF 20% 6.3V	
R16 R17 R18 R19 R22	1-216-660-11 s METAL, CHIP 2.4K 0.5% 1/10W 1-216-663-11 s METAL, CHIP 3.3K 0.5% 1/10W 1-216-668-11 s METAL, CHIP 5.1K 0.5% 1/10W 1-216-652-11 s METAL, CHIP 1.1K 0.5% 1/10W 1-216-675-11 s METAL, CHIP 10K 0.5% 1/10W	C16 C17 C18 C19 C20	1-163-038-91 s 1-163-229-11 s 1-163-099-00 s	CERAMIC 0.1uF 25V CERAMIC 0.1uF 25V CERAMIC 12PF 5% 50V CERAMIC, CHIP 18PF 5% ELECT 10uF 20% 16V	50V
R23 R24 R25 R26 R27	1-216-675-11 s METAL, CHIP 10K 0.5% 1/10W 1-216-675-11 s METAL, CHIP 10K 0.5% 1/10W 1-216-675-11 s METAL, CHIP 10K 0.5% 1/10W 1-216-655-11 s METAL, CHIP 1.5K 0.5% 1/10W 1-216-657-11 s METAL, CHIP 1.8K 0.5% 1/10W	C21 C22 C23 C24 C25	1-163-038-91 s 1-163-038-91 s 1-163-038-91 s	CERAMIC 0.1uF 25V CERAMIC 0.1uF 25V CERAMIC 0.1uF 25V CERAMIC 0.1uF 25V CERAMIC 0.1uF 25V	
R28 S1 S2 S3 S4 S5	1-216-660-11 s METAL, CHIP 2.4K 0.5% 1/10W 1-692-829-11 s SWITCH, TACTILE	C26 C27 C28 C29 C30	1-163-038-91 s 1-163-038-91 s 1-163-038-91 s	CERAMIC 0.1uF 25V CERAMIC 0.1uF 25V CERAMIC 0.1uF 25V CERAMIC 0.1uF 25V CERAMIC 0.1uF 25V	
S6 S8 S9 S10 S11	1-692-829-11 s SWITCH, TACTILE 1-692-829-11 s SWITCH, TACTILE 1-692-829-11 s SWITCH, TACTILE 1-692-829-11 s SWITCH, TACTILE 1-692-829-11 s SWITCH, TACTILE	C31 C32 C33 C34 C35	1-124-779-00 s 1-124-779-00 s 1-163-038-91 s	ELECT 10uF 20% 16V ELECT 10uF 20% 16V ELECT 10uF 20% 16V CERAMIC 0.1uF 25V CERAMIC 0.1uF 25V	
S12 S13	1-692-829-11 s SWITCH, TACTILE 1-692-829-11 s SWITCH, TACTILE 1-692-829-11 s SWITCH, TACTILE 1-692-829-11 s SWITCH, TACTILE 1-692-829-11 s SWITCH, TACTILE	C36 C37 C38 C39 C40	1-163-038-91 s 1-163-038-91 s 1-163-038-91 s	CERAMIC 0.1uF 25V CERAMIC 0.1uF 25V CERAMIC 0.1uF 25V CERAMIC 0.1uF 25V CERAMIC 0.1uF 25V	
S18 S19 S20 S22	1-692-829-11 s SWITCH, TACTILE 1-692-829-11 s SWITCH, TACTILE 1-692-829-11 s SWITCH, TACTILE 1-692-829-11 s SWITCH, TACTILE	C41 C42 C43 C44 C45	1-163-038-91 s 1-126-193-11 s 1-163-038-91 s	CERAMIC 0.1uF 25V CERAMIC 0.1uF 25V ELECT 1uF 20% 50V CERAMIC 0.1uF 25V CERAMIC 0.1uF 25V	
S23 S24 S25	1-692-829-11 s SWITCH, TACTILE 1-692-829-11 s SWITCH, TACTILE 1-692-829-11 s SWITCH, TACTILE	C46 C47 C48 C49 C50	1-163-038-91 s 1-163-038-91 s 1-163-038-91 s	CERAMIC 0.1uF 25V CERAMIC 0.1uF 25V CERAMIC 0.1uF 25V CERAMIC 0.1uF 25V CERAMIC 0.1uF 25V	
		C51 C52		CERAMIC 0.1uF 25V CERAMIC 0.1uF 25V	

(SY-243 BOARD)	(SY-243 BOARD)
Ref. No. or Q'ty Part No. SP Description	Ref. No. or Q'ty Part No. SP Description
C566 1-163-038-91 s CERAMIC 0.1uF 25V C567 1-163-038-91 s CERAMIC 0.1uF 25V	F1 1-532-743-11 s FUSE, GLASS TUBE 2A 125V
C568 1-163-038-91 s CERAMIC 0.1uf 25V C569 1-163-038-91 s CERAMIC 0.1uf 25V C570 1-163-038-91 s CERAMIC 0.1uf 25V	FH1 1-533-189-11 s HOLDER, FUSE FH2 1-533-189-11 s HOLDER, FUSE
C566 1-163-038-91 s CERAMIC 0.1uF 25V C567 1-163-038-91 s CERAMIC 0.1uF 25V C568 1-163-038-91 s CERAMIC 0.1uF 25V C569 1-163-038-91 s CERAMIC 0.1uF 25V C570 1-163-038-91 s CERAMIC 0.1uF 25V C571 1-163-038-91 s CERAMIC 0.1uF 25V C572 1-163-038-91 s CERAMIC 0.1uF 25V C573 1-163-038-91 s CERAMIC 0.1uF 25V C574 1-163-038-91 s CERAMIC 0.1uF 25V C575 1-163-038-91 s CERAMIC 0.1uF 25V C576 1-163-038-91 s CERAMIC 0.1uF 25V C577 1-163-038-91 s CERAMIC 0.1uF 25V C578 1-163-038-91 s CERAMIC 0.1uF 25V C579 1-163-038-91 s CERAMIC 0.1uF 25V C579 1-163-038-91 s CERAMIC 0.1uF 25V C580 1-163-038-91 s CERAMIC 0.1uF 25V C581 1-163-038-91 s CERAMIC 0.1uF 25V C582 1-163-038-91 s CERAMIC 0.1uF 25V C583 1-163-038-91 s CERAMIC 0.1uF 25V	FL1 1-239-400-11 s FILTER, CHIP EMI FL2 1-421-773-11 s FILTER, NOISE REMOVAL FL3 1-239-400-11 s FILTER, CHIP EMI FL4 1-239-558-11 s FILTER, CHIP EMI FL5 1-239-558-11 s FILTER, CHIP EMI
C576 1-163-038-91 s CERAMIC 0.1uF 25V C577 1-163-038-91 s CERAMIC 0.1uF 25V	FL6 1-239-558-11 s FILTER, CHIP EMI FL7 1-239-558-11 s FILTER, CHIP EMI
C578 1-163-038-91 s CERAMIC 0.1uF 25V C579 1-163-038-91 s CERAMIC 0.1uF 25V C580 1-163-038-91 s CERAMIC 0.1uF 25V	IC1 8-759-185-69 s IC TC74VHC27F(EL) IC2 8-759-186-38 s IC TC74VHC32F IC3 8-759-186-38 s IC TC74VHC32F IC4 8-759-081-44 s IC TC74VHC04F
C581 1-163-038-91 s CERAMIC 0.1uF 25V C582 1-163-038-91 s CERAMIC 0.1uF 25V C583 1-163-038-91 s CERAMIC 0.1uF 25V	IC5 8-759-081-44 s IC TC74VHC04F IC6 8-759-185-67 s IC TC74VHC20F(EL)
C584 1-163-038-91 s CERAMIC 0.1uF 25V C585 1-163-038-91 s CERAMIC 0.1uF 25V	IC7 8-759-186-38 s IC TC74VHC32F IC8 8-759-081-48 s IC TC74VHC08F IC9 8-759-081-48 s IC TC74VHC08F
C586 1-163-038-91 s CERAMIC 0.1uF 25V C587 1-126-205-11 s ELECT 47uF 20% 6.3V	TC10 8-759-925-72 g TC SN74HC02ANS
C588 1-126-205-11 s ELECT 47uF 20% 6.3V C589 1-163-038-91 s CERAMIC 0.1uF 25V C590 1-163-038-91 s CERAMIC 0.1uF 25V	IC11 8-759-925-90 s IC SN74HC74ANS IC12 8-759-186-39 s IC TC74VHC74F IC13 8-759-174-16 s IC TC74VHC244F IC14 8-759-926-48 s IC SN74HC244ANS
C591 1-163-038-91 s CERAMIC 0.1uF 25V C592 1-163-038-91 s CERAMIC 0.1uF 25V	IC15 8-759-925-80 s IC SN74HC14ANS
C587 1-126-205-11 s ELECT 47uF 20% 6.3V C588 1-126-205-11 s ELECT 47uF 20% 6.3V C589 1-163-038-91 s CERAMIC 0.1uF 25V C590 1-163-038-91 s CERAMIC 0.1uF 25V C591 1-163-038-91 s CERAMIC 0.1uF 25V C592 1-163-038-91 s CERAMIC 0.1uF 25V C593 1-163-038-91 s CERAMIC 0.1uF 25V C594 1-163-038-91 s CERAMIC 0.1uF 25V C600 1-163-127-00 s CERAMIC, CHIP 270PF 5% 50V	IC16 8-759-973-71 s IC TL7705CPS-B IC17 8-759-054-56 s IC SN75179BPS IC18 8-759-925-76 s IC SN74HC08NS IC19 8-759-426-14 s IC UPD70741GC-25-7EA
C601 1-163-127-00 s CERAMIC, CHIP 270PF 5% 50V C602 1-163-127-00 s CERAMIC, CHIP 270PF 5% 50V	IC20 8-759-186-53 s IC TC74VHC163F
C603 1-163-127-00 s CERAMIC, CHIP 270PF 5% 50V C604 1-163-127-00 s CERAMIC, CHIP 270PF 5% 50V C605 1-163-127-00 s CERAMIC, CHIP 270PF 5% 50V	IC21 8-759-038-28 s IC MC145407F IC22 8-759-055-21 s IC HN58C66SFP25TZ IC23 8-759-055-21 s IC HN58C66SFP25TZ IC25 8-759-190-79 s IC UPD72001GC-11-3B6
C606 1-163-127-00 s CERAMIC, CHIP 270PF 5% 50V C607 1-163-127-00 s CERAMIC, CHIP 270PF 5% 50V C608 1-163-127-00 s CERAMIC, CHIP 270PF 5% 50V	IC26 8-759-174-16 s IC TC74VHC244F IC27 8-759-371-04 s IC HM514260CJ7-Z
C609 1-163-127-00 s CERAMIC, CHIP 270FF 5% 50V C610 1-163-127-00 s CERAMIC, CHIP 270FF 5% 50V	IC28 8-759-926-56 s IC SN74HC273ANS IC29 8-759-065-20 s IC RTC-4553B IC30 8-759-926-56 s IC SN74HC273ANS
C611 1-163-038-91 s CERAMIC 0.1uF 25V	IC31 8-752-378-41 s IC CXD207-109Q
CN1 1-778-261-11 o CONNECTOR, BOARD TO BOARD 124P CN11 1-526-660-21 o SOCKET, IC 32P	IC32 8-752-364-81 s IC CXK581000AM-70LL IC33 8-759-186-02 s IC TC74VHCT245F(EL) IC34 8-759-926-48 s IC SN74EC244ANS
CNI2 1-526-660-21 o SOCKET, IC 32P D2 8-719-800-76 s DIODE 1SS226	IC35 8-759-926-48 s IC SN74HC244ANS IC36 8-759-926-48 s IC SN74HC244ANS
D3 8-719-404-20 s DIODE LN1251C-TR D4 8-719-404-20 s DIODE LN1251C-TR D5 8-719-404-20 s DIODE LN1251C-TR D6 8-719-404-20 s DIODE LN1251C-TR	IC37 8-759-926-56 s IC SN74HC273ANS IC38 8-759-926-56 s IC SN74HC273ANS IC39 8-759-473-39 o IC 27C1001-10F1-ST5LV1.00 IC40 8-759-473-40 o IC 27C1001-10F1-ST5UV1.00 IC100 8-759-287-50 s IC CXD8932Q
D7 8-719-404-20 s DIODE LN1251C-TR D8 8-719-404-20 s DIODE LN1251C-TR D9 8-719-404-20 s DIODE LN1251C-TR D10 8-719-404-20 s DIODE LN1251C-TR D11 8-719-800-76 s DIODE LN2526	IC101 8-759-297-58 s IC DS1000Z-75 IC102 8-759-297-60 s IC DS1000Z-75 IC103 8-759-297-58 s IC DS1000Z-75 IC104 8-759-987-27 s IC LM1881M IC105 8-759-186-39 s IC TC74VHC74F
D12 8-719-800-76 s DIODE 1SS226 D500 8-719-801-95 s DIODE 2GWJ42	IC106 8-759-081-44 s IC TC74VHC04F IC107 8-759-081-44 s IC TC74VHC04F

R67

(SY-243 BOARD)

Ref. No. or Q'ty	Part No.	SP Description			Ref. No. or Q'ty	Part No. SI	P Descript	ion
R68 R69 R70 R71 R72	1-216-065-0 1-216-065-0 1-216-065-0	0 s METAL, CHIP 4.7K 0 s METAL, CHIP 4.7K 0 s METAL, CHIP 4.7K 0 s METAL, CHIP 4.7K 0 s METAL, CHIP 4.7K	5% 1/10W 5% 1/10W 5% 1/10W		X2	1-760-721-11 s	; CRYSTAL	20MHz
R73 R74 R75 R76 R77	1-216-065-0 1-216-065-0 1-216-065-0	O S METAL, CHIP 4.7K O S METAL, CHIP 4.7K O S METAL, CHIP 4.7K O S METAL, CHIP 4.7K O S METAL, CHIP 4.7K	5% 1/10W 5% 1/10W 5% 1/10W					
R78 R79 R80 R81 R82	1-216-065-0 1-216-065-0 1-216-065-0	0 s METAL, CHIP 4.7K 0 s METAL, CHIP 4.7K 0 s METAL, CHIP 4.7K 0 s METAL, CHIP 4.7K 0 s METAL, CHIP 4.7K	5% 1/10W 5% 1/10W 5% 1/10W					
R83 R84 R85 R86 R92	1-216-035-0 1-216-035-0 1-216-049-0	0 s METAL, CHIP 4.7K 0 s METAL, CHIP 270 0 s METAL, CHIP 270 0 s METAL, CHIP 1K 5 0 s METAL, CHIP 33 5	5% 1/10W 5% 1/10W % 1/10W					
R93 R500 R501 R502 R503	1-216-089-9 1-216-089-9 1-216-089-9	0 s METAL, CHIP 1K 5 1 s METAL 47K 5% 1/1 1 s METAL 47K 5% 1/1 1 s METAL 47K 5% 1/1 0 s METAL, CHIP 10K	OW OW OW					
R504 R505 R506 R510 R511	1-216-073-0 1-216-073-0 1-216-035-0	0 s METAL, CHIP 2.2K 0 s METAL, CHIP 10K 0 s METAL, CHIP 10K 0 s METAL, CHIP 270 0 s METAL, CHIP 270	5% 1/10W 5% 1/10W 5% 1/10W					
R512 R513 R514 R515 R516	1-216-035-0 1-216-035-0 1-216-035-0	0 s METAL, CHIP 270 0 s METAL, CHIP 270 0 s METAL, CHIP 270 0 s METAL, CHIP 270 0 s METAL, CHIP 270	5% 1/10W 5% 1/10W 5% 1/10W					
R517 R518 R519 R520 R521	1-216-035-0 1-216-035-0 1-216-035-0	0 s METAL, CHIP 270 0 s METAL, CHIP 270 0 s METAL, CHIP 270 0 s METAL, CHIP 270 0 s METAL, CHIP 270	5% 1/10W 5% 1/10W 5% 1/10W					
R522 R523 R524 R525 R526	1-216-035-0 1-216-035-0 1-216-035-0	0 s METAL, CHIP 270 0 s METAL, CHIP 270 0 s METAL, CHIP 270 0 s METAL, CHIP 270 0 s METAL, CHIP 270	5% 1/10W 5% 1/10W 5% 1/10W					
R527 R528 R529 R530 R531	1-216-035-0 1-216-035-0 1-216-035-0	0 s METAL, CHIP 270 0 s METAL, CHIP 270 0 s METAL, CHIP 270 0 s METAL, CHIP 270 0 s METAL, CHIP 270	5% 1/10W 5% 1/10W 5% 1/10W					
R532 R533		0 s METAL, CHIP 270 0 s METAL, CHIP 270						
S1 S2 S3 S4 S500	1-571-187-1 1-571-967-1 1-571-120-1	1 s SWITCH, TACTIL (1 s SWITCH, TACTIL (1 s SWITCH, DIP (PIA 1 s SWITCH, SLIDE 1 s THERMISTOR	REFLOW TYP					
X1	1-567-862-1	1 s CRYSTAL, 4.9152M	HZ					

TG-180 BG	OARD		(TG-180	BOARD)		
Ref. No. or Q'ty	Part No. Si	P Description	Ref. No. or Q'ty		SP Description	•
1pc C1 C2 C3 C4	A-8313-462-A (1-164-156-11 : 1-164-004-11 : 1-162-970-11 : 1-162-9	MOUNTED CIRCUIT BOARD, TG-180 CERAMIC 0.1uf 25V CERAMIC, CHIP 0.1uf 10% 25V CERAMIC, CHIP 0.01uf 10% 25V TANTALUM 4.7uf 20% 25V CERAMIC 0.1uf 25V	C58 C59 C60 C61 C62	1-164-004-1 1-162-970-1 1-104-851-1	1 s CERAMIC 0.1uF 25V 1 s CERAMIC, CHIP 0.1uF 1 1 s CERAMIC, CHIP 0.01uF 1 s TANTALUM, CHIP 10uF 2 1 s CERAMIC, CHIP 0.01uF	10% 25V 0% 10V
C5 C6 C7 C8 C9	1-162-970-11 s 1-164-156-11 s 1-164-004-11 s	S CERAMIC 0.1uF 25V S CERAMIC, CHIP 0.01uF 10% 25V S CERAMIC 0.1uF 25V S CERAMIC, CHIP 0.1uF 10% 25V S CERAMIC, CHIP 0.1uF 10% 25V S CERAMIC, CHIP 0.01uF 10% 25V	C63 C64 C65 C66 C67	1-162-970-1 1-162-970-1 1-104-851-1	1 s TANTALUM, CHIP 10uF 2 1 s CERAMIC, CHIP 0.01uF 1 s CERAMIC, CHIP 0.01uF 1 s TANTALUM, CHIP 10uF 2 1 s TANTALUM, CHIP 10uF 2	10% 25V 10% 25V 0% 10V
C10	1-104-851-11 s 1-162-970-11 s 1-104-851-11 s 1-162-970-11 s	S TANTALUM, CHIP 10uF 20% 10V S CERAMIC, CHIP 0.01uF 10% 25V S TANTALUM, CHIP 10uF 20% 10V S CERAMIC, CHIP 0.01uF 10% 25V S CERAMIC, CHIP 0.01uF 10% 25V S CERAMIC, CHIP 0.01uF 10% 25V	C68 C69 C70 C71 C72	1-162-970-1 1-126-394-1 1-128-397-2	1 s TANTALUM 10uF 20% 20V 1 s CERAMIC, CHIP 0.01uF 1 s ELECT, CHIP 10uF 20% 1 s ELECT 100uF 20% 16V 1 s ELECT, CHIP 10uF 20%	10% 25V 16V
C15 C16 C17 C18	1-104-913-11 s 1-104-851-11 s 1-113-985-11 s 1-162-970-11 s	TANTALUM, CHIP 10uF 20% 16V TANTALUM, CHIP 10uF 20% 10V TANTALUM 10uF 20% 20V CERAMIC, CHIP 0.01uF 10% 25V	C73 C74 C75 C76 C77	1-162-923-1 1-162-923-1 1-162-923-1	1 s ELECT, CHIP 10uF 20% 1 s CERAMIC, CHIP 47PF 5% 1 s CERAMIC, CHIP 47PF 5% 1 s CERAMIC, CHIP 47PF 5% 1 s CERAMIC, CHIP 47PF 5%	50V 50V 50V
C20 C21 C22 C23 C24	1-164-156-11 : 1-164-156-11 : 1-164-156-11 : 1-104-851-11 :	S CERAMIC 0.1uF 25V S CERAMIC 0.1uF 25V S CERAMIC 0.1uF 25V S TANTALUM, CHIP 10uF 20% 10V S CERAMIC 0.1uF 25V	C78 C101 C102 C103 C104	1-162-923-1 1-162-921-1 1-104-851-1	1 s CERAMIC, CHIP 33PF 5% 1 s CERAMIC, CHIP 47PF 5% 1 s CERAMIC, CHIP 33PF 5% 1 s TANTALUM, CHIP 10uF 2 1 s CERAMIC, CHIP 0.01uF	50V 50V 0% 10V
C25 C26 C27 C28 C29	1-164-156-11 : 1-104-851-11 : 1-164-156-11 : 1-164-156-11 : 1-164-004-11 :	S CERAMIC 0.1uF 25V S TANTALUM, CHIP 10uF 20% 10V S CERAMIC 0.1uF 25V S CERAMIC 0.1uF 25V S CERAMIC, CHIP 0.1uF 10% 25V	C105 C106 C107 C108 C109	1-162-970-1 1-162-970-1 1-162-970-1 1-162-970-1	1 s CERAMIC, CHIP 0.01uF 1 s CERAMIC, CHIP 0.01uF 1 s CERAMIC, CHIP 0.01uF 1 s CERAMIC, CHIP 0.01uF 1 s CERAMIC, CHIP 0.01uF	10% 25V 10% 25V 10% 25V 10% 25V
C30 C31 C32 C33 C34 C35	1-113-987-11 s 1-164-156-11 s 1-162-970-11 s 1-164-156-11 s	S CERAMIC, CHIP 0.01uF 10% 25V S TANTALUM 4.7uF 20% 25V S CERAMIC 0.1uF 25V S CERAMIC, CHIP 0.01uF 10% 25V S CERAMIC 0.1uF 25V S CERAMIC, CHIP 0.1uF 10% 25V	C110 C111 CN1 CN2 CN3 CN4	1-162-923-1 1-764-082-2 1-764-081-2 1-764-082-2 1-764-081-2	1 s CERAMIC, CHIP 0.01uF 1 s CERAMIC, CHIP 47PF 5% 1 s PIN, CONNECTOR (PC BO 1 s PIN, CONNECTOR (PC BO 1 s PIN, CONNECTOR (PC BO 1 s PIN, CONNECTOR (PC BO	50V ARD) 11P ARD) 9P ARD) 11P ARD) 9P
C36 C37 C38 C39 C40	1-104-851-11 s 1-162-970-11 s 1-104-851-11 s	S CERAMIC, CHIP 0.01uF 10% 25V TANTALUM, CHIP 10uF 20% 10V CERAMIC, CHIP 0.01uF 10% 25V TANTALUM, CHIP 10uF 20% 10V CERAMIC, CHIP 0.01uF 10% 25V	CN5 CN6 CN7	1-764-081-2 1-569-481-2	1 s PIN, CONNECTOR (PC BO 1 s PIN, CONNECTOR (PC BO 1 s CONNECTOR, FPC 30P 1 s OSCILLATOR, CRYSTAL 4	ARD) 9P
C41 C42 C43 C44 C45	1-162-970-11 s 1-104-851-11 s 1-104-913-11 s 1-113-985-11 s	S CERAMIC, CHIP 0.01uF 10% 25V S TANTALUM, CHIP 10uF 20% 10V S TANTALUM, CHIP 10uF 20% 16V S TANTALUM 10uF 20% 20V S CERAMIC, CHIP 0.01uF 10% 25V	D1 D2 D3 D4	8-719-024-8 8-719-024-8 8-719-024-8	1 s DIODE 1SS300-TE85L 1 s DIODE 1SS300-TE85L 1 s DIODE 1SS300-TE85L 1 s DIODE 1SS300-TE85L	
C46 C47 C48 C49 C50	1-164-156-11 s 1-104-913-11 s 1-164-156-11 s 1-135-212-21 s	S CERAMIC 0.1uF 25V S TANTALUM, CHIP 10uF 20% 16V S CERAMIC 0.1uF 25V S TANTALUM, CHIP 2.2uF 10% 35V S TANTALUM, CHIP 2.2uF 10% 35V S TANTALUM, CHIP 10uF 20% 10V	IC1 IC2 IC3 IC4 IC5	8-759-073-5 8-759-073-5 8-759-172-3	2 s IC TC74AC04F-EL 2 s IC TC74AC04F-EL 2 s IC TC74AC04F-EL 3 s IC UPD16502GS(1) 2 s IC CXD2437TQ	
C51 C52 C53 C54 C55	1-162-970-11 : 1-113-985-11 : 1-164-004-11 : 1-162-970-11 :	E CERAMIC, CHIP 1001 20% 10V E TANTALUM 10uF 20% 20V E CERAMIC, CHIP 0.1uF 10% 25V E CERAMIC, CHIP 0.01uF 10% 25V E TANTALUM 4.7uF 20% 25V	IC6 IC7 IC8 IC9 IC10	8-759-073-5 8-759-172-3 8-759-172-3	2 s IC TC74AC04F-EL 2 s IC TC74AC04F-EL 3 s IC UPD16502Gs(1) 3 s IC UPD16502Gs(1) 2 s IC TC74AC04F-EL	
C56 C57	1-164-156-11	S CERAMIC 0.1uF 25V S CERAMIC, CHIP 0.01uF 10% 25V	IC11 IC12 IC13	8-759-172-3	2 s IC TC74AC04F-EL 3 s IC UPD16502GS(1) 6 s IC TC7W08F	

(TG-180 BOARD)

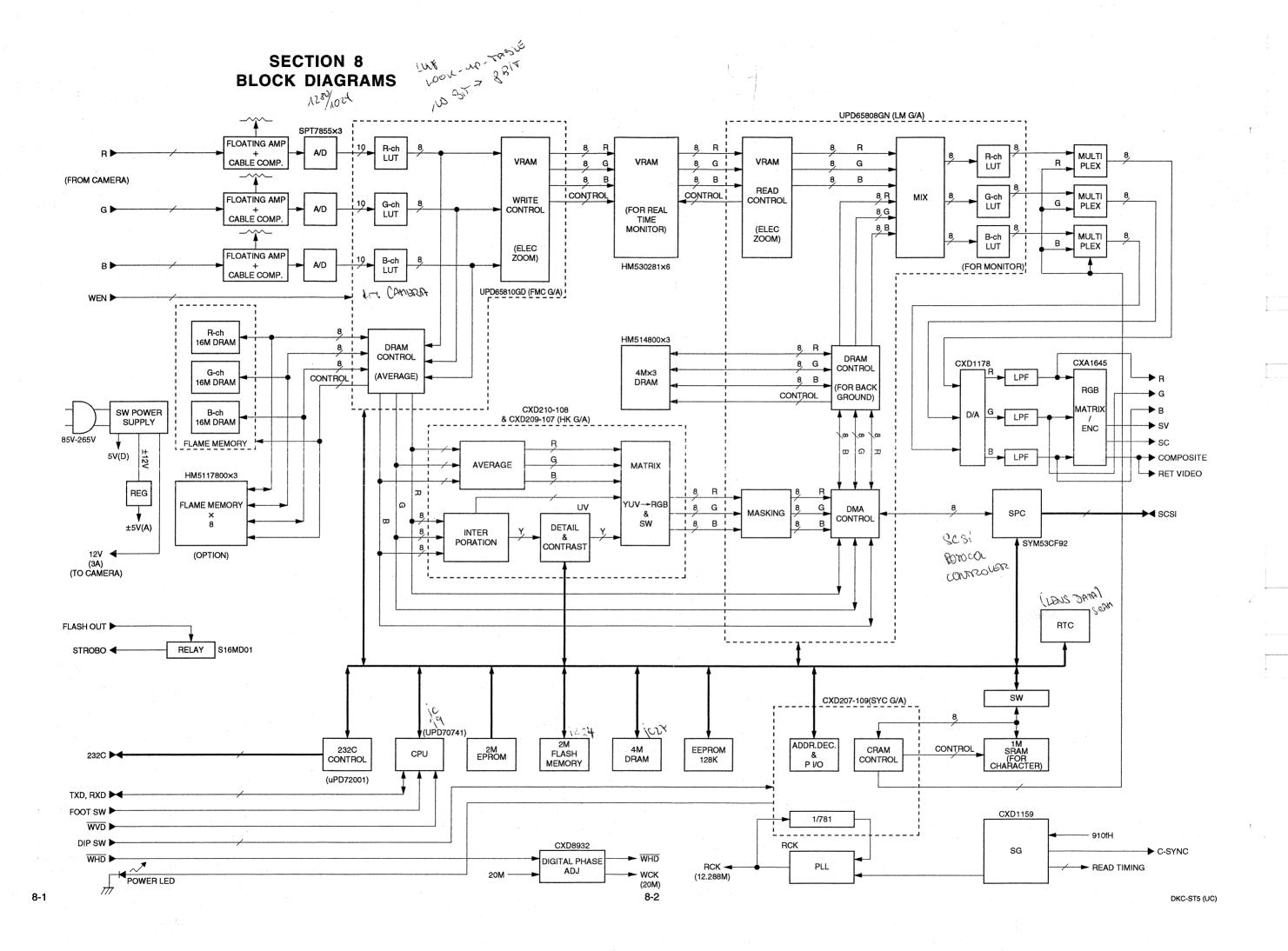
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IC101 IC102 IC103 IC104	8-759-190-60 s IC CXD8843R 8-759-234-20 s IC TC7S08F 8-759-925-90 s IC SN74HC74ANS 8-759-927-46 s IC SN74HC00ANS
L1 L2 L3 L4	1-408-789-21 s INDUCTOR CHIP 100uH 1-408-789-21 s INDUCTOR CHIP 100uH 1-408-789-21 s INDUCTOR CHIP 100uH 1-408-789-21 s INDUCTOR CHIP 100uH
Q2 Q3 Q4	8-729-429-98 s TRANSISTOR XP1401 8-729-429-44 s TRANSISTOR XP1501-TXE 8-729-905-35 s TRANSISTOR 2SC4081R 8-729-429-98 s TRANSISTOR XP1401 8-729-429-44 s TRANSISTOR XP1501-TXE
Q7 Q8	8-729-905-35 s TRANSISTOR 2SC4081R 8-729-429-98 s TRANSISTOR XP1401 8-729-429-44 s TRANSISTOR XP1501-TXE 8-729-905-35 s TRANSISTOR 2SC4081R 8-729-026-53 s TRANSISTOR 2SA1576A-T106-QR
Q11	8-729-905-35 s TRANSISTOR 2SC4081R
R1 R2 R3 R4 R5	1-216-845-11 s METAL, CHIP 100K 5% 1/16W 1-216-833-11 s METAL, CHIP 10K 5% 1/16W 1-216-837-11 s METAL, CHIP 22K 5% 1/16W 1-216-841-11 s METAL, CHIP 47K 5% 1/16W 1-216-846-11 s METAL, CHIP 120K 5% 1/16W
R6 R7 R8 R9 R10	1-216-833-11 s METAL, CHIP 10K 5% 1/16W 1-216-833-11 s METAL, CHIP 10K 5% 1/16W 1-216-849-11 s METAL, CHIP 220K 5% 1/16W 1-216-843-11 s METAL, CHIP 68K 5% 1/16W 1-216-835-11 s METAL, CHIP 15K 5% 1/16W
R11 R12 R13 R14 R15	1-216-857-11 s METAL, CHIP 1M 5% 1/16W 1-216-845-11 s METAL, CHIP 100K 5% 1/16W 1-216-833-11 s METAL, CHIP 10K 5% 1/16W 1-216-837-11 s METAL, CHIP 22K 5% 1/16W 1-216-841-11 s METAL, CHIP 47K 5% 1/16W
R16 R17 R18 R19 R20	1-216-846-11 s METAL, CHIP 120K 5% 1/16W 1-216-833-11 s METAL, CHIP 10K 5% 1/16W 1-216-833-11 s METAL, CHIP 10K 5% 1/16W 1-216-849-11 s METAL, CHIP 220K 5% 1/16W 1-216-843-11 s METAL, CHIP 68K 5% 1/16W
R21 R22 R23 R24 R25	1-216-835-11 s METAL, CHIP 15K 5% 1/16W 1-216-857-11 s METAL, CHIP 1M 5% 1/16W 1-216-845-11 s METAL, CHIP 100K 5% 1/16W 1-216-845-11 s METAL, CHIP 100K 5% 1/16W 1-216-833-11 s METAL, CHIP 10K 5% 1/16W
R26 R27 R28 R29 R30	1-216-837-11 s METAL, CHIP 22K 5% 1/16W 1-216-841-11 s METAL, CHIP 47K 5% 1/16W 1-216-846-11 s METAL, CHIP 120K 5% 1/16W 1-216-833-11 s METAL, CHIP 10K 5% 1/16W 1-216-833-11 s METAL, CHIP 10K 5% 1/16W
R31 R32 R33 R34 R35	1-216-849-11 s METAL, CHIP 220K 5% 1/16W 1-216-843-11 s METAL, CHIP 68K 5% 1/16W 1-216-835-11 s METAL, CHIP 15K 5% 1/16W 1-216-857-11 s METAL, CHIP 1M 5% 1/16W 1-216-827-11 s METAL, CHIP 3.3K 5% 1/16W
R36 R37 R38 R39	1-216-825-11 s METAL, CHIP 2.2K 5% 1/16W 1-216-829-11 s METAL, CHIP 4.7K 5% 1/16W 1-216-820-11 s METAL, CHIP 820 5% 1/16W 1-216-813-11 s METAL, CHIP 220 5% 1/16W

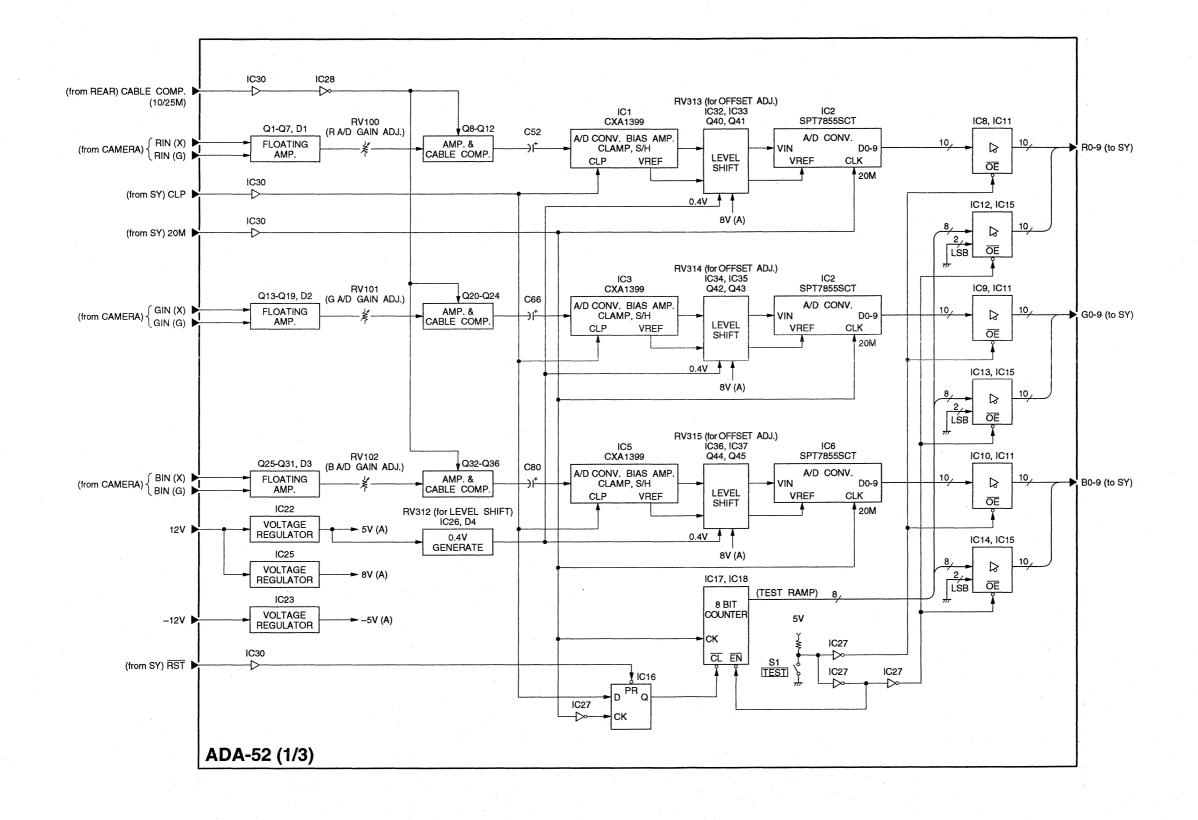
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Ref. No. or Q'ty Part No. SP Description
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R40
R41
R42
R43
R44
                      1-216-813-11 s METAL, CHIP 220 5% 1/16W
1-216-813-11 s METAL, CHIP 220 5% 1/16W
1-216-813-11 s METAL, CHIP 220 5% 1/16W
R101
R102
R103
                      1-238-856-11 s RES, ADJ, METAL 10K
1-238-856-11 s RES, ADJ, METAL 10K
1-238-856-11 s RES, ADJ, METAL 10K
1-238-857-11 s RES, ADJ, CERMET 22K
1-238-857-11 s RES, ADJ, CERMET 22K
RV1
RV2
RV3
RV4
RV5
RV6
                       1-238-857-11 s RES, ADJ, CERMET 22K
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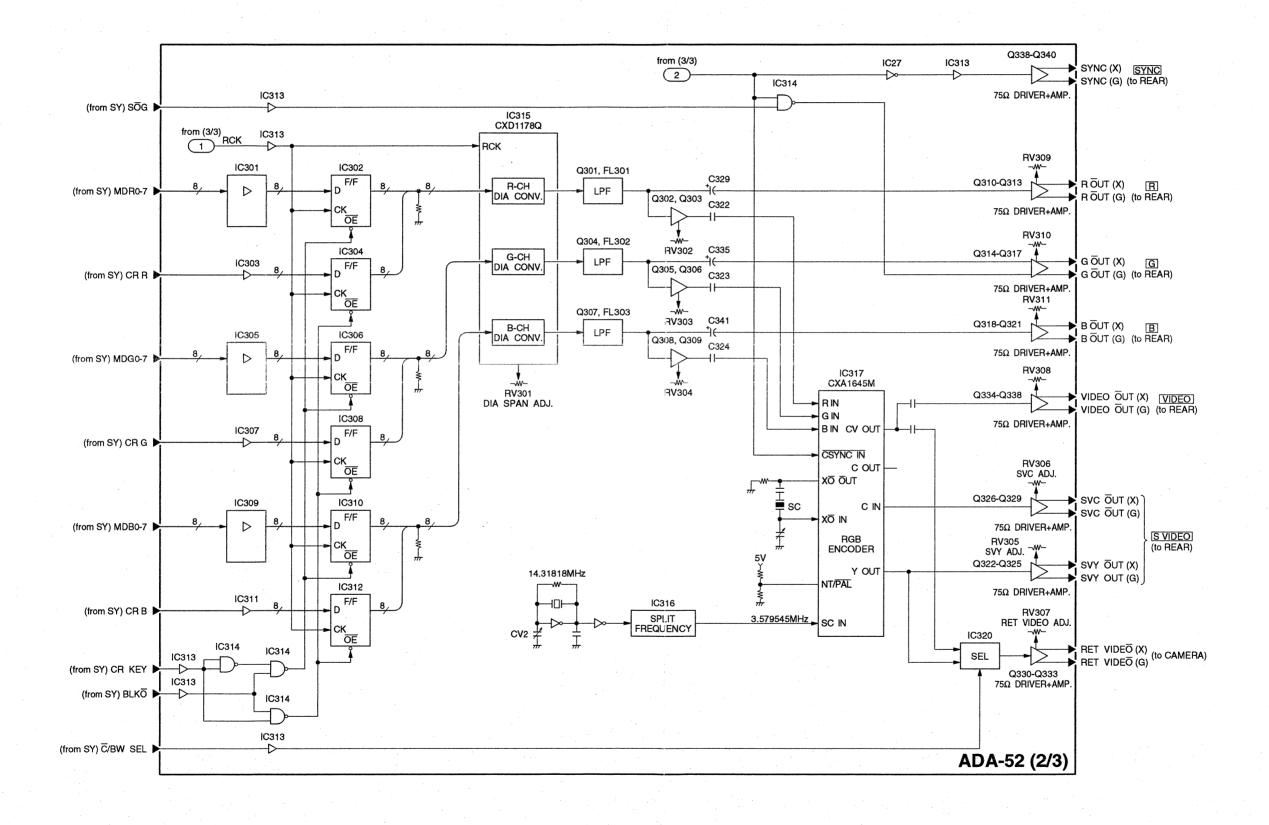
Ref. No. or Q'ty Part No. SP Description CAMERA BLOCK> 1pc 1-543-590-21 s CORE, TROIDAL 1pc 1-571-877-11 s SWITCH, PUSH (AC POWER) 1pc 1-982-281-11 s WIRE, FLAT TYPE (30-CORE) 1pc 1-957-462-11 o HARNESS, SUB (MC-1) 1pc 1-957-463-11 s HARNESS, SUB (MC-2) 1pc 1-957-464-11 s HARNESS, SUB (MC-2) 1pc 1-957-465-11 s HARNESS, SUB (MC-3) 1pc 1-957-465-11 s HARNESS, SUB (MC-4) CN-1395 BOARD: CN207 1-540-256-21 s SOCKET, SYNCHRONIZE "FLASH" CN204 1-562-382-31 s CONNECTOR, BNC "MONITOR" CN205 1-561-284-21 s SOCKET, DIN 8P "VF" CN-1462 BOARD: CN302 1-569-422-11 s CONNECTOR, 20P FEMALE "LENS 1" CN303 1-779-426-11 o CONNECTOR, 12P "REMOTE"

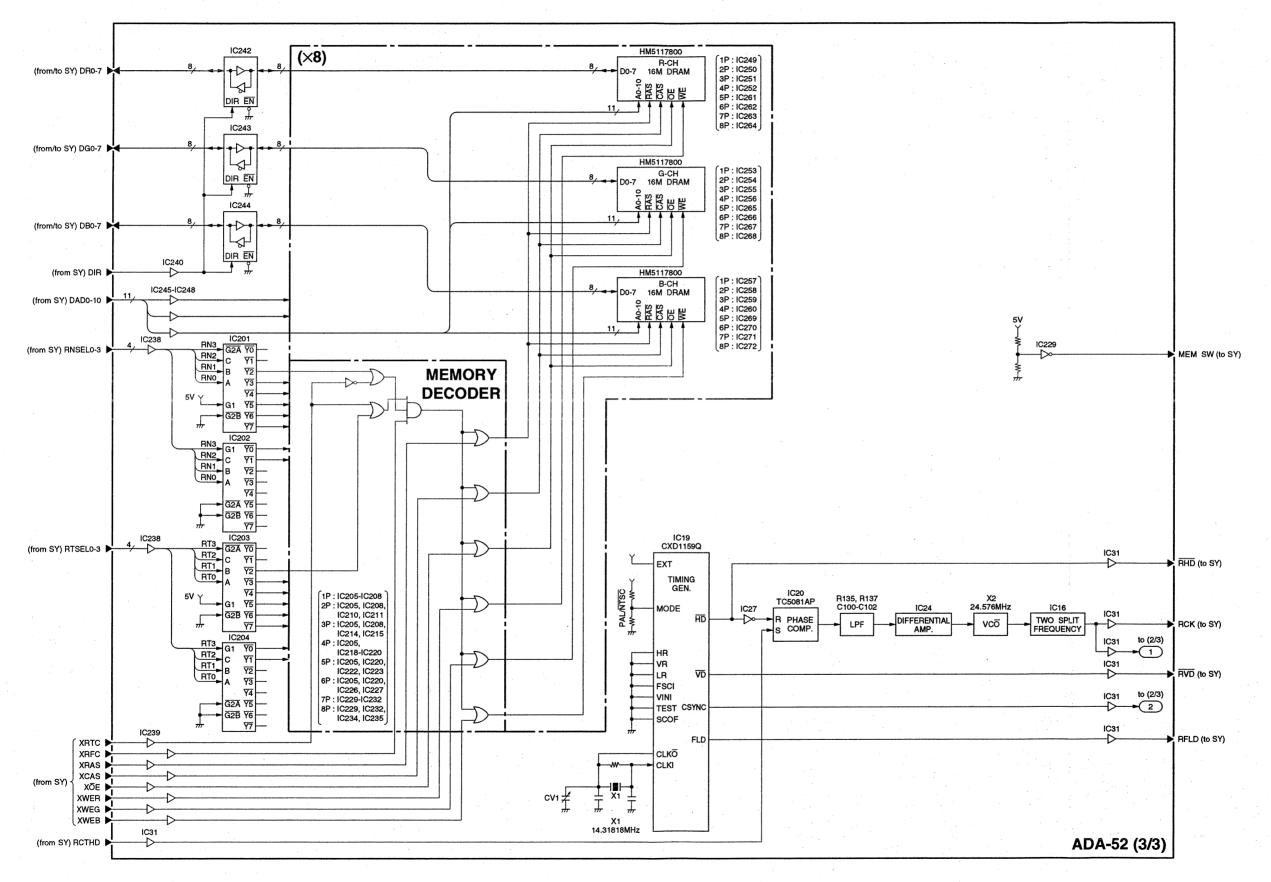
PACKING MATERIALS & SUPPLIED ACCESSORIES

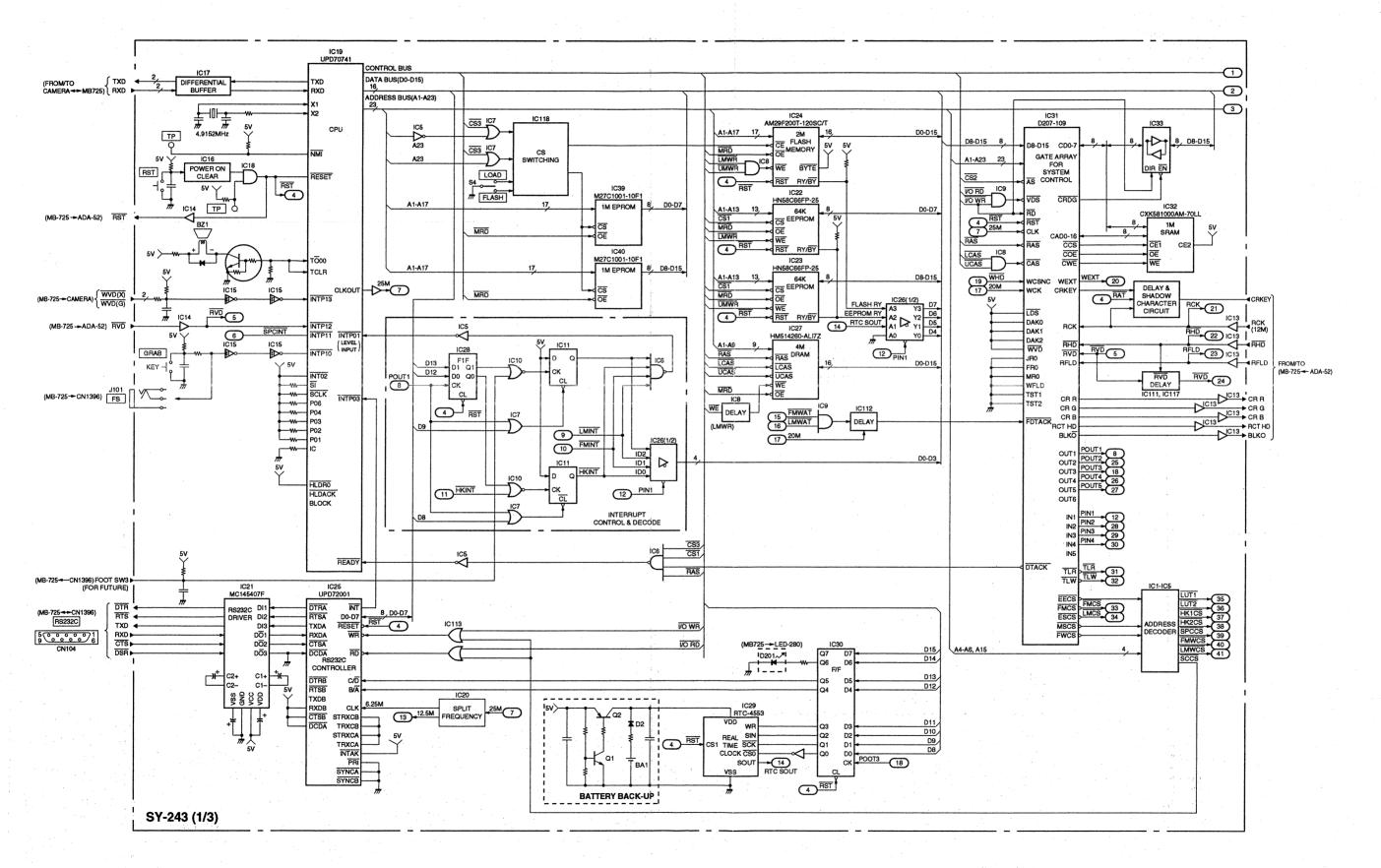
Ref. No. or Q'ty	Part No. SP	Description
2pcs	A-8278-617-A o	
1pc 1pc 1pc		CABLE, CONNECTION (CCZ-A10) SHEET, PROTECTION MANUAL (HARD WARE), INSTRUCTION

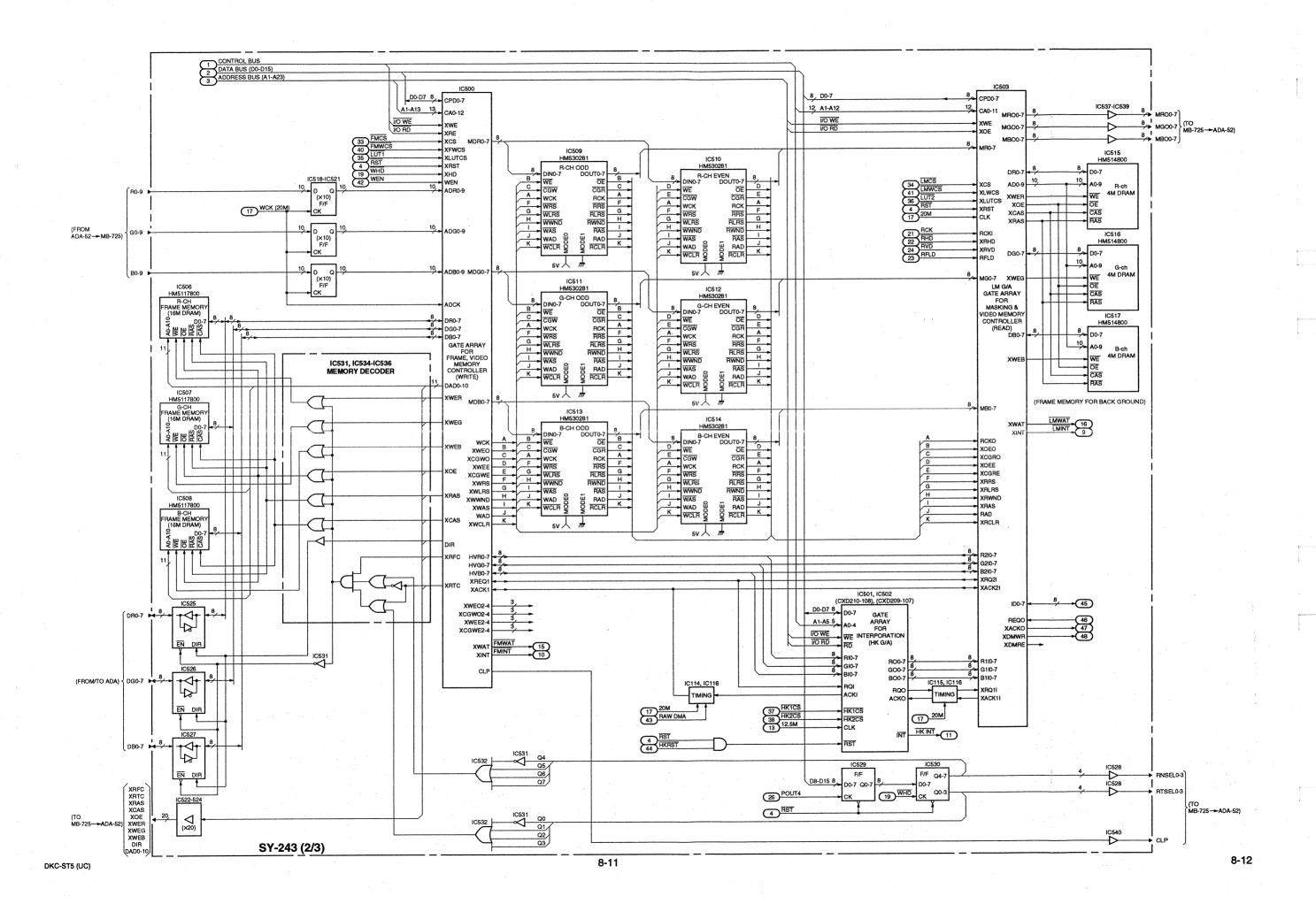


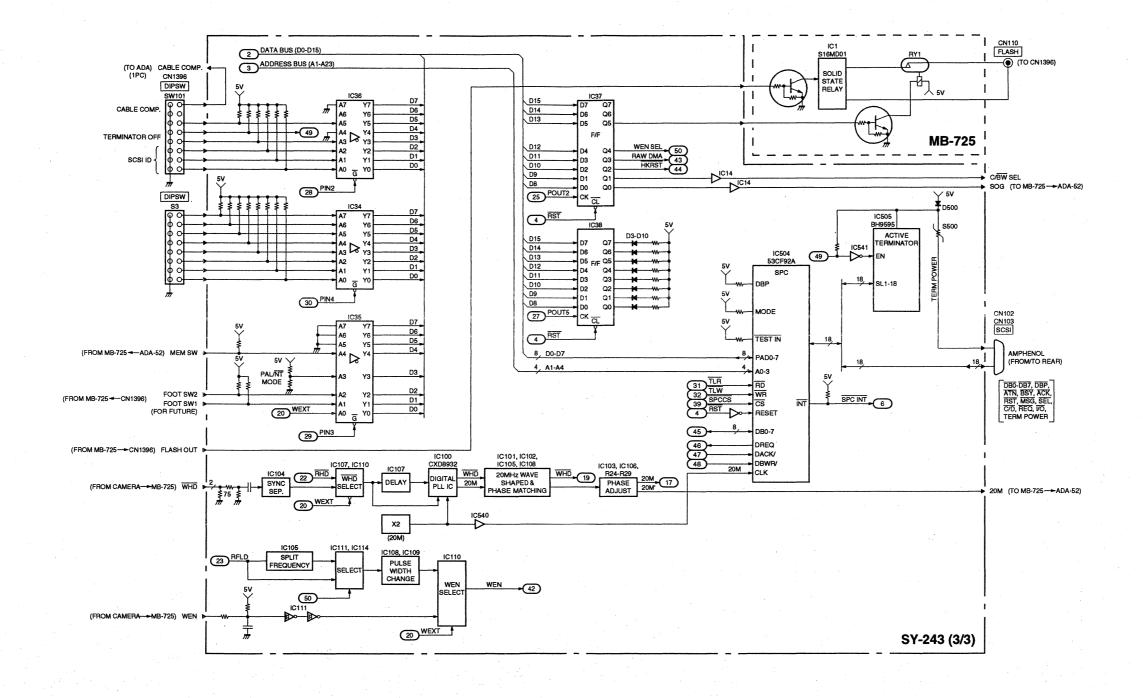


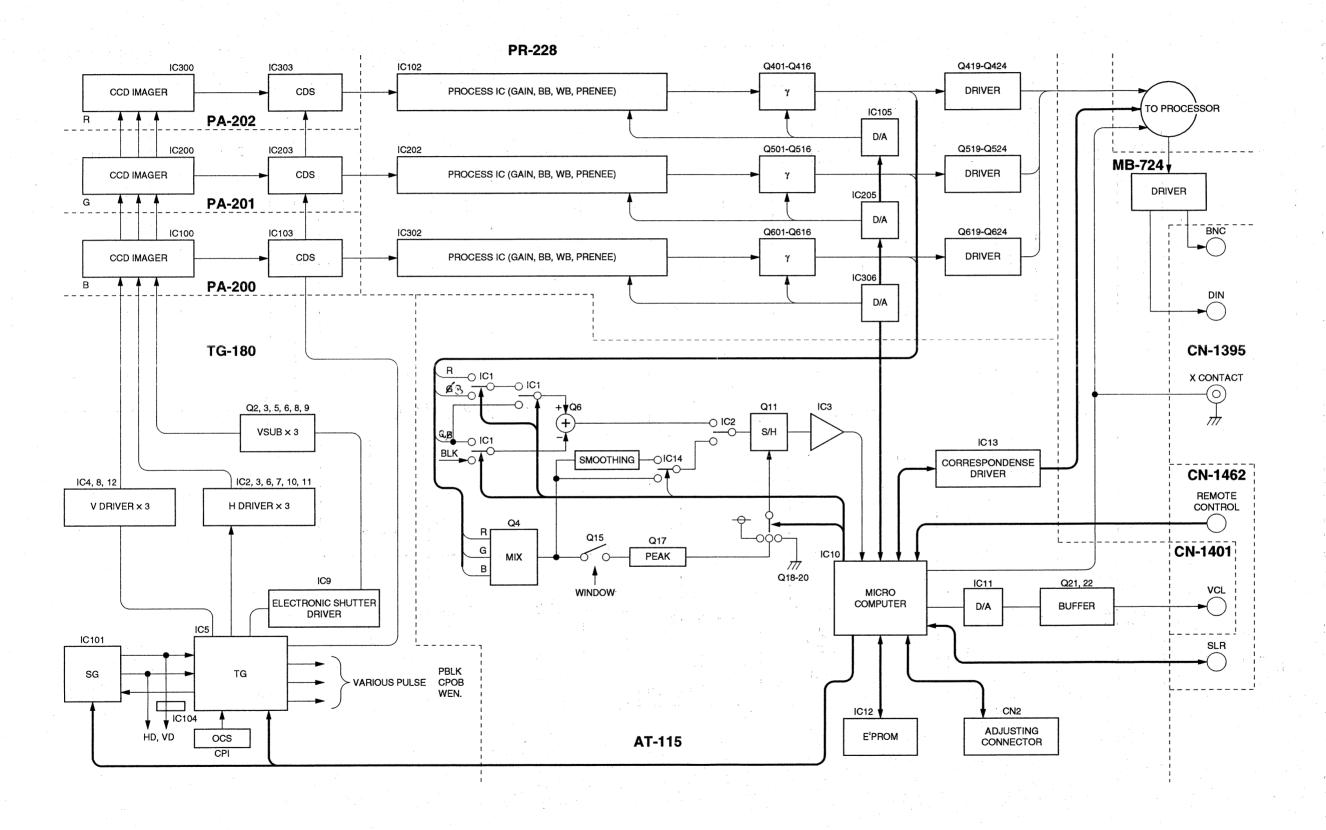






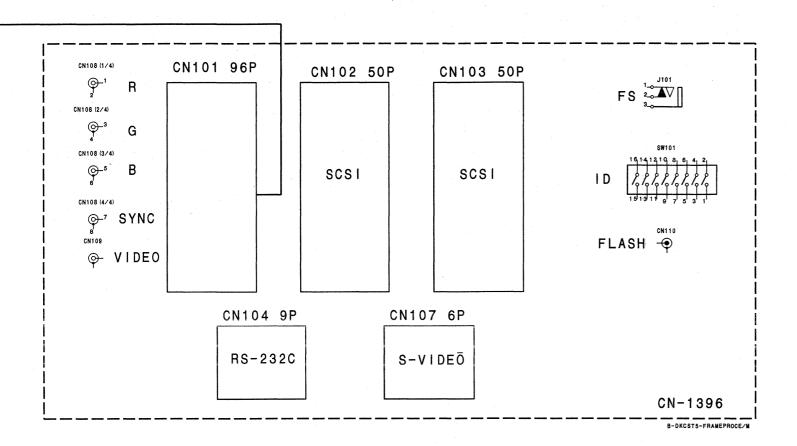






FRAME (PROCESSOR) FRAME (PROCESSOR) **SECTION 9** PRINTED WIRING BOARDS AND SCHEMATIC DIAGRAMS 6 7 10 12 13 14 8 9 11 15 CN2 124P CN4 100P CN11 96P CN1 124P CN3 100P CN2 100P CN1 124P CN1 124P CN2 100P Ε CN10 2P ADA-52 SY-243 CN7 2P, CN8 8P, CN9 12P CN5 7P MB-725 CAMERA 26P CN1 5P CN2 7P CN201 2P Н FAN LED-280 B-DKCST5-FRAMEPROCE/M SW REG

16	17	18	19	20	21	22	23	24	25	26	27	28	29	30



THIS NOTE IS COMMON FOR PRINTED WIRING BOARDS AND SCHEMATIC DIAGRAMS.

(In addition to this, the necessary note is printed in each block.)

- For Schematic Diagrams.
- Caution when replacing chip parts.

 New parts must be attached after removal of chip.

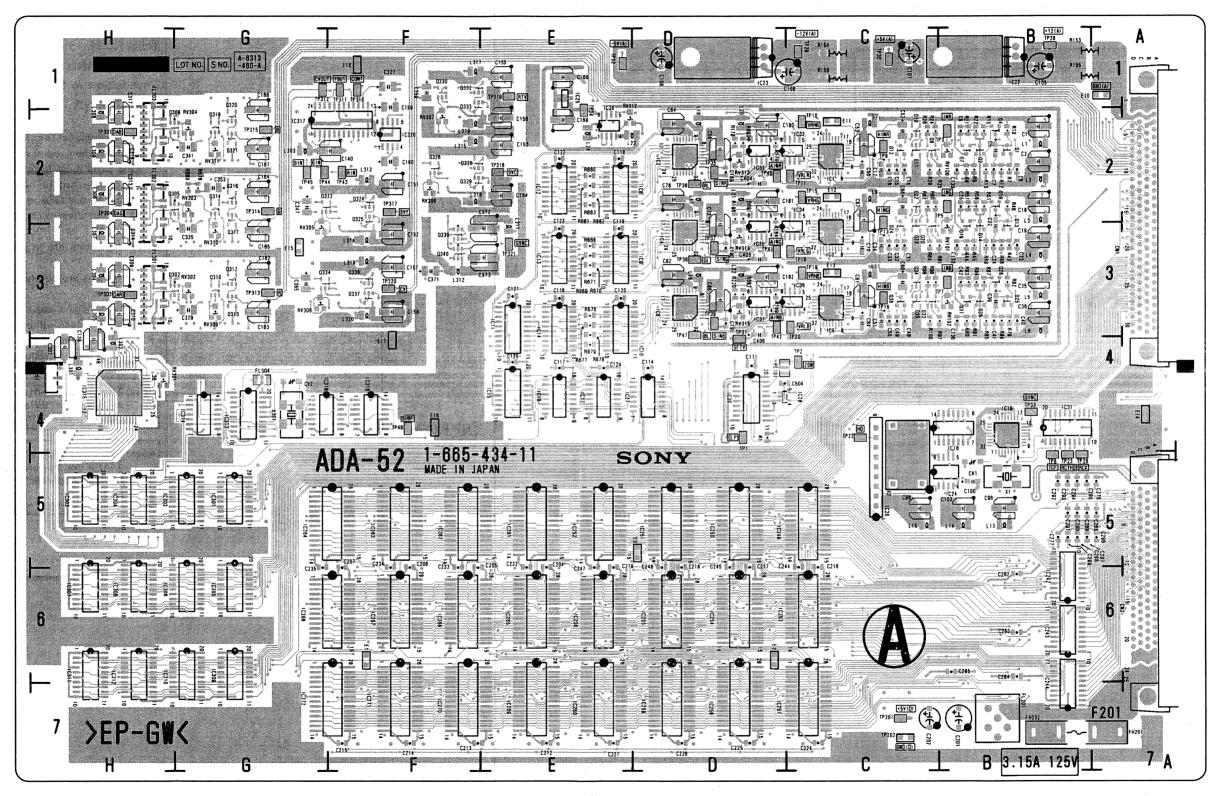
 Be careful not to heat the minus side of tantalum capacitor, because it is damaged by the heat.
- All resistors are in ohms, 1/10W unless otherwise noted.
 k: 1000 , M : 1000k .
- All capacitors are in μF unless otherwise noted.
 pF: μμF.
- 50V or less are not indicated except for electrolytics and tantalums.
- All variable and adjustable resistors have characteristic curve B, unless otherwise noted.
- nonflammable resistor.
- : fusibe resistor.

Note:The components identified by mark 🖒 are critical for safety. Replace only with part number specified.

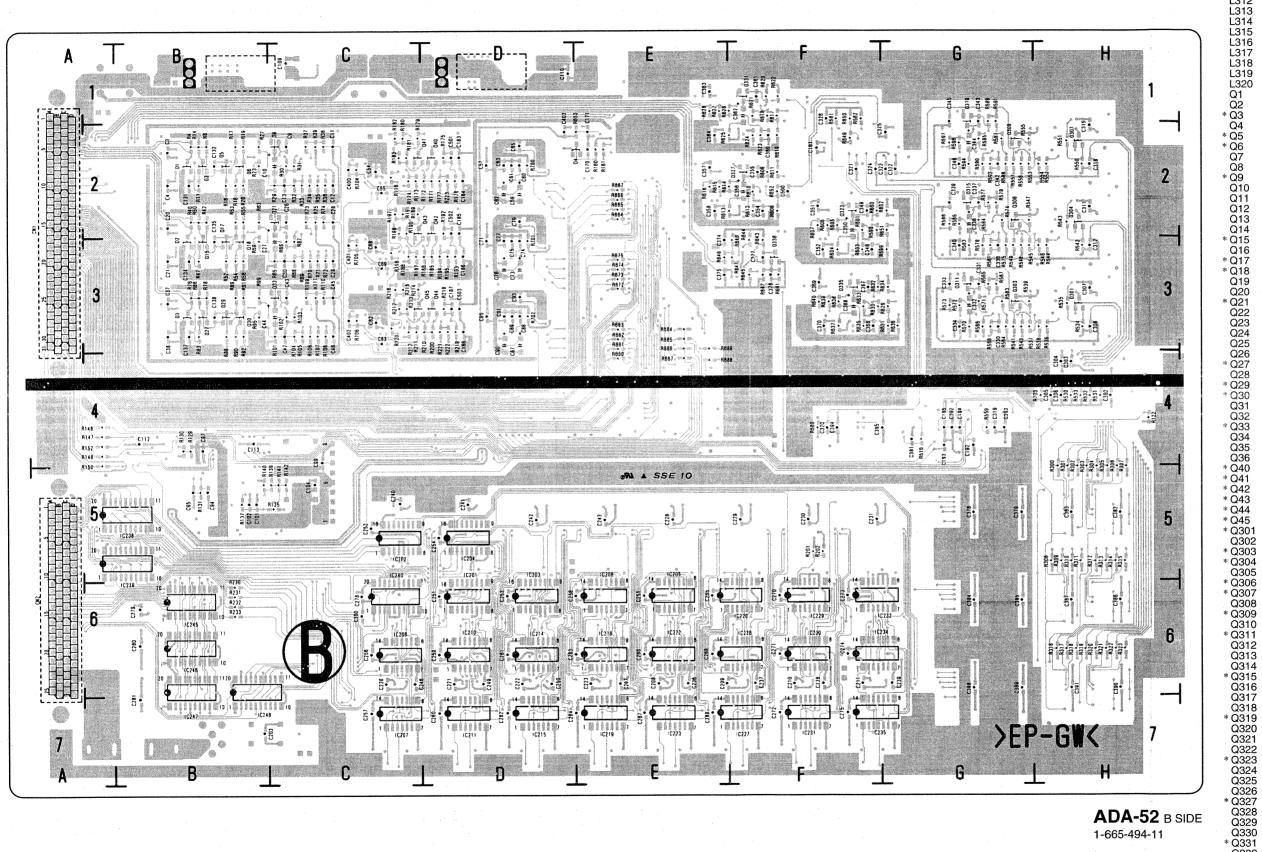
Note: Les composants identifiés par une marque 🛆 sont d'une importance critique pour la sécurité. Ne les remplacer que par des pièces de numéro spécifié.

ADA-52 BOARD(1/2)

* IC232 * IC234 * IC235 * IC238 * IC240 IC242 IC243 IC244 * IC245 * IC247 * IC248 IC255 IC256 IC257 IC256 IC257 IC256 IC257 IC260 IC261 IC262 IC263 IC261 IC263 IC261 IC263 IC263 IC261 IC261 IC261 IC261 IC262 IC263 IC263 IC263 IC264 IC265 IC266 IC267 IC268 IC269 IC270 IC301 IC302 IC303 IC304 IC305 IC306 IC307 IC316 IC317 IC318 IC318 IC316 IC317 IC318 IC317 IC318 IC316 IC317 IC318 IC316 IC317 IC318 IC317 IC318 IC318 IC317 IC318 IC318 IC316 IC317 IC318 IC317 IC318 IC317 IC318 IC318 IC317 IC318 IC318 IC317 IC308 IC309



ADA-52 A SIDE 1-665-494-11



ADA-52 B SIDE 1-665-494-11

E3 F3 F2 Q334 * Q335 Q336 Q337 * Q338 Q339 Q340 RV100 RV102 RV301 RV302 RV303 RV304 RV305 RV306 RV307 RV308 RV309 RV310 RV311 RV312 RV312 RV313 RV314 RV315 S1 TP1
TP2
TP3
TP5
TP7
TP8
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TP38
TP39 TP41 TP42 TP43 TP44 TP45 TP46 TP46 TP47 TP201 TP202 TP301 TP303 TP304 TP310 TP311 TP312 TP312 G1 TP313 G3 TP314 G2 TP315 G2 TP317 F2 TP318 E2 TP319 E1 TP320 F3 TP321 E3 X1 B5 C5 G4 X301

ADA-52 BOARD(2/2)

L311

L312 L313 L314 L315

L316 L317 L318

* Q17

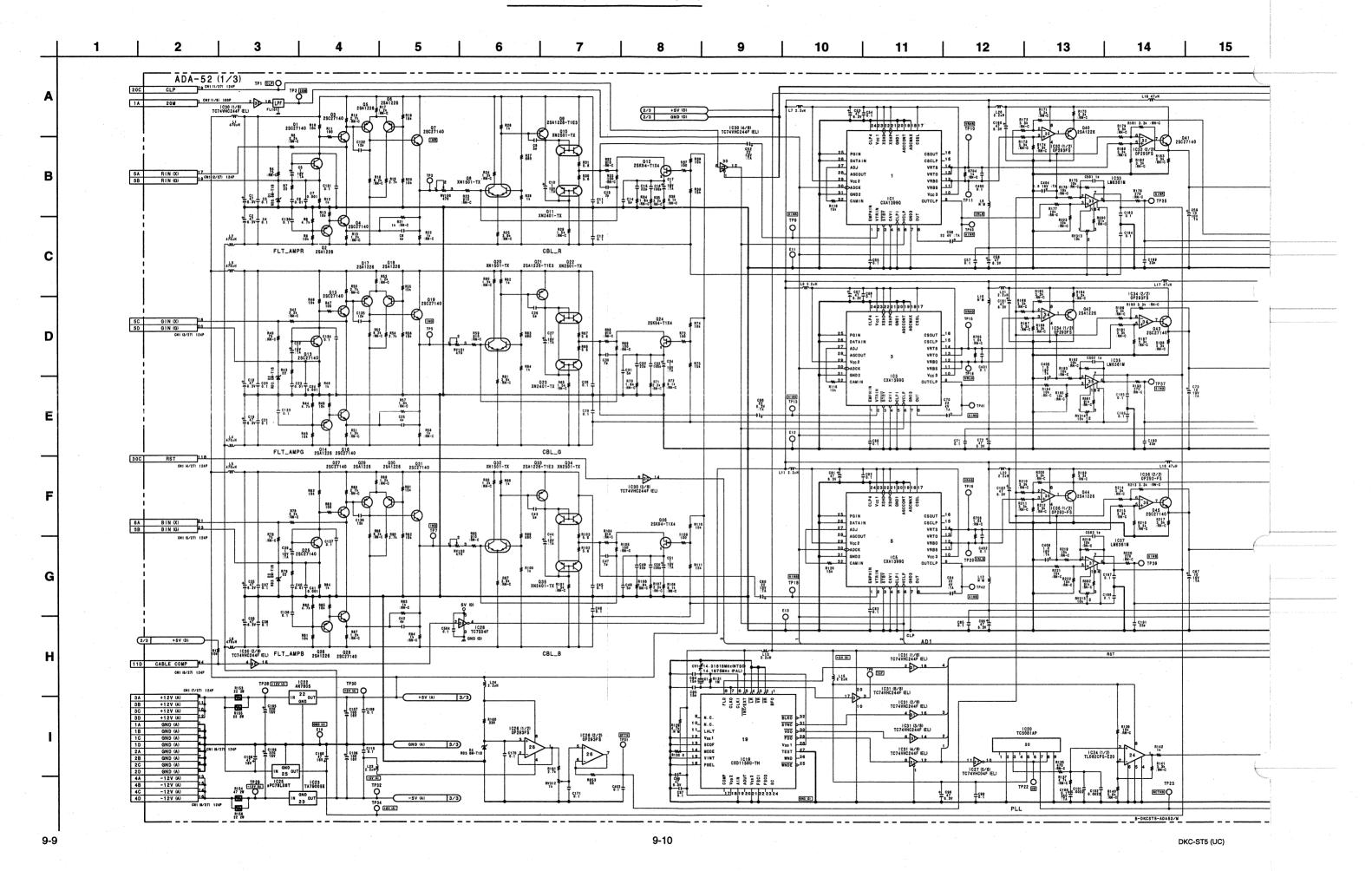
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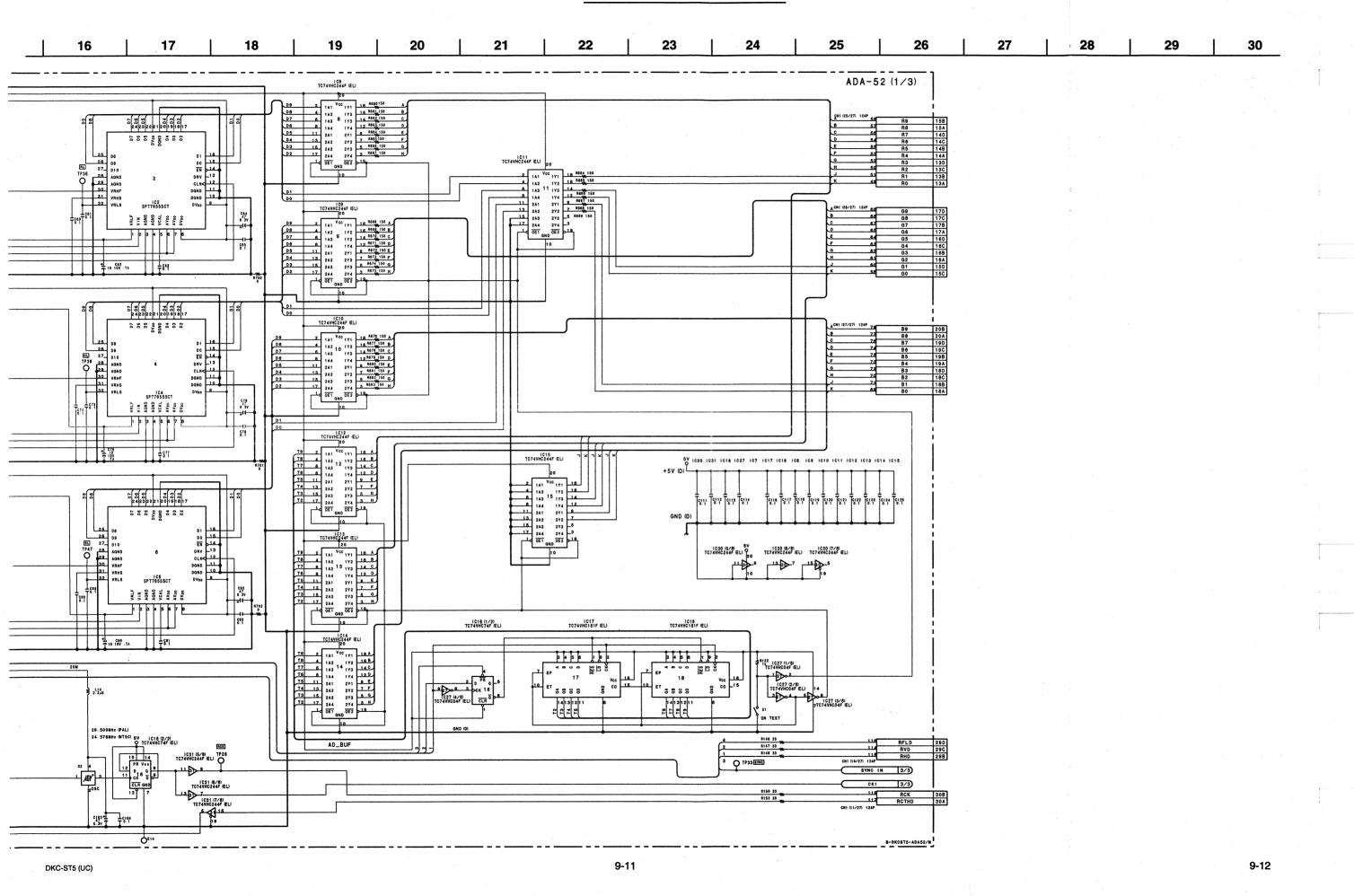
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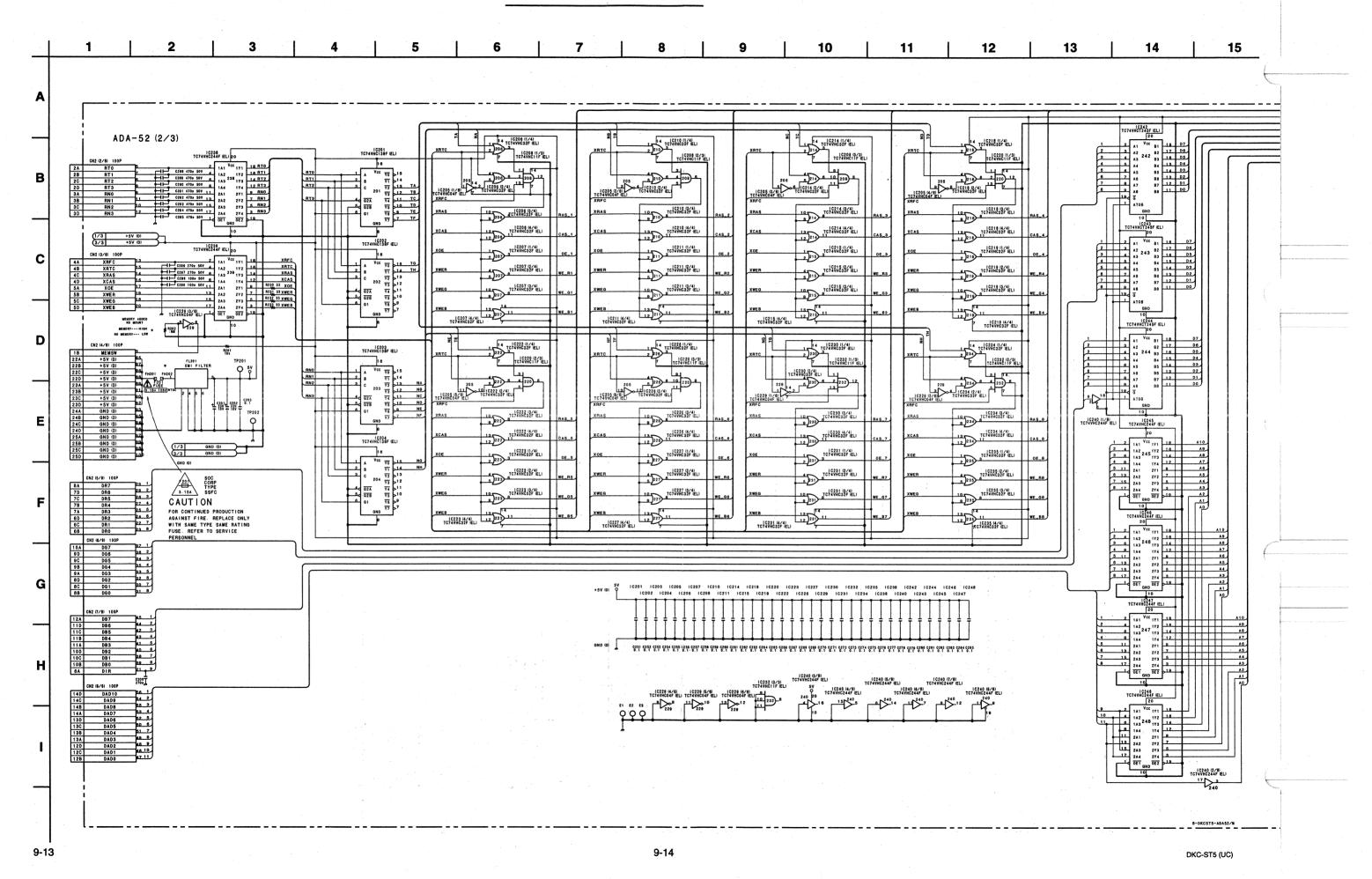
* Q311

Q326

* B SIDE

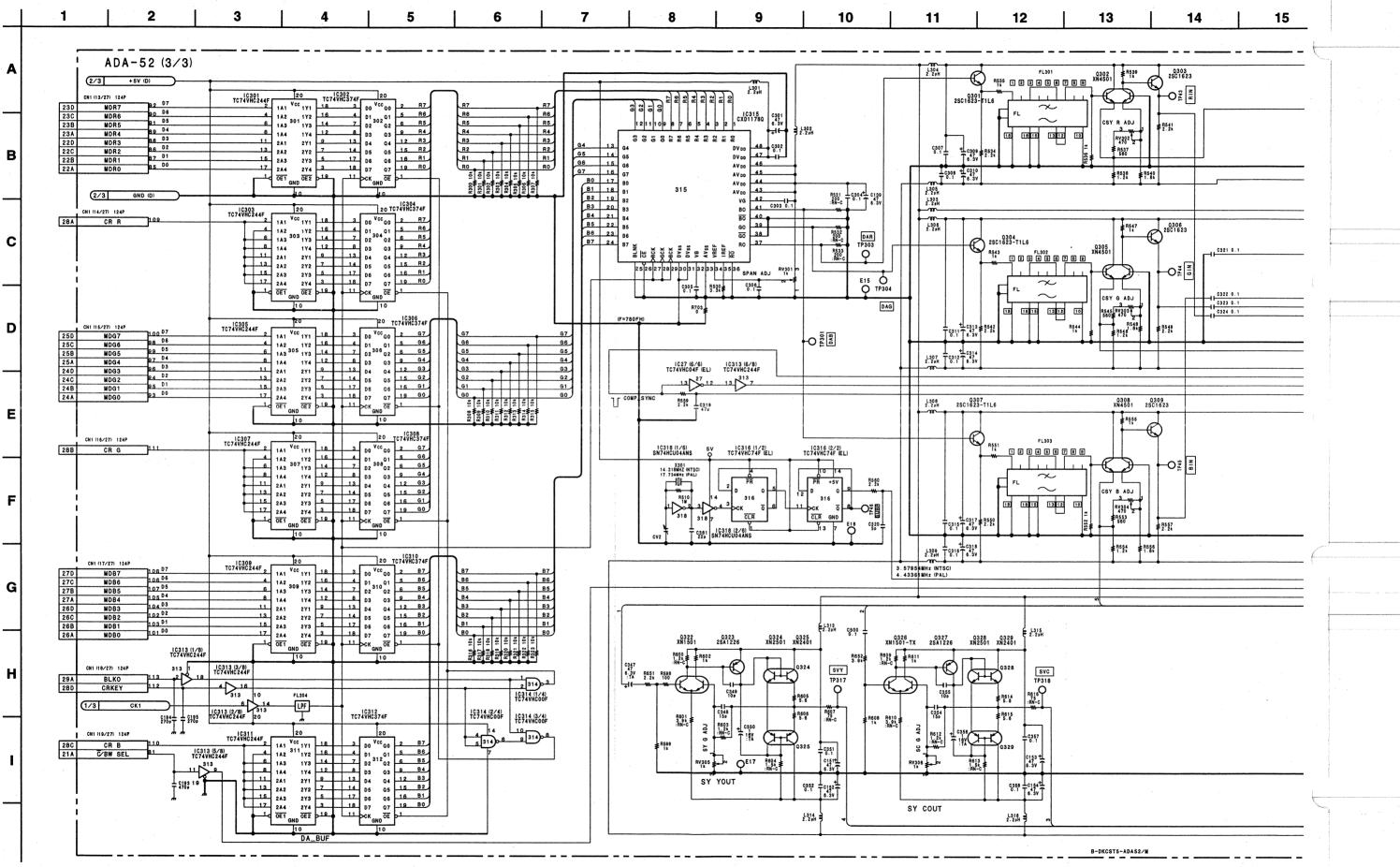


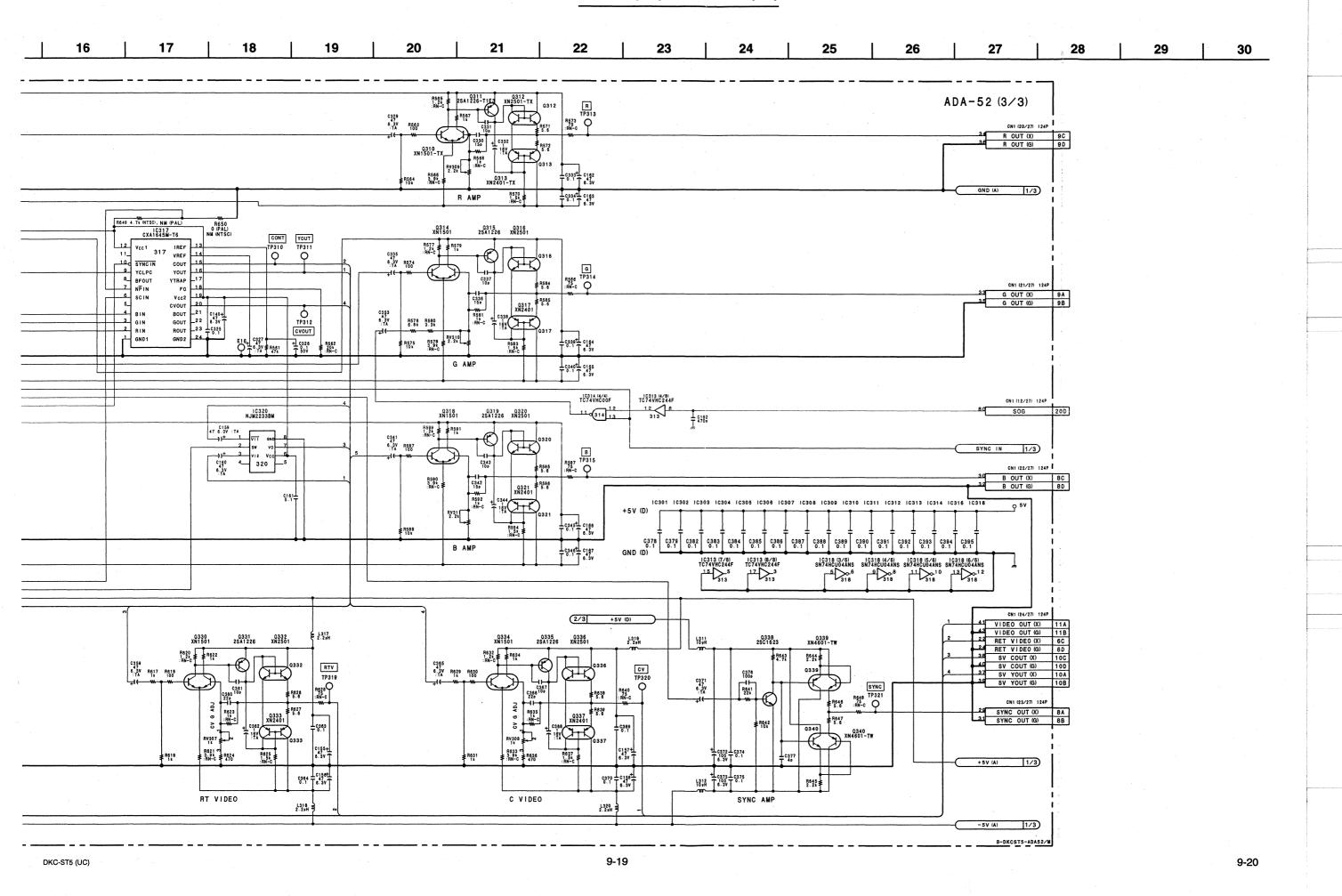




16		17	18	19	20	21	22	23	24	25	26	27	28	29
													AD	A-52 (2/3)
								1 1						
	A10 A0 A1 A2 A3 WE R1 WE B1 RAS 2	D0 1 2 1/00 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1	1/05 26 D6 D6 D7 D7 D7 D7 D7 D	00 01 02 03 A5 A7 A6 A1 A1 A3 A3	1	A8 A8 A10 A10 A1	234	AB AB A10	11 A1 A6 11	A8 A10 A7 A0 A1 A6 A1 A5 A2 A4 A3	C244 0.1 1 CC255 HWS117800BJ-7E 0.1 2 Vcc Vss 2 1. 3 1/00 1/07 2 2 1/00 1/07 2 3 5 1/02 1/05 2 0. 0. 0. 0. 0. 0. 0. 0. 0. 0. 0. 0. 0.	07 06 06 06 06 06 06 06 06 06 06 06 06 06	C 140	07 08 05 04 A9 A8 A7 A8 A8
	RAS. 2 CAS. 2 OE. 2 OE. 2 A10 A10 A1 A2 A3 WE. B2	0.28 1 1 1 1 2 2 5 0 H 1 1 1 2 2 5 0 H 1 1 1 2 2 5 0 H 1 1 2 2 5 1 H 1 2 1 2 1 2 1 2 1 2 1 2 1 2 1 2 1 2	Ves 28. Ves 28. Ves 28. Ves 27. D7. D7. D8. D8. D8. D8. D8. D	D0 D1 D2	33 1 1 2254 HM51178008J-7EL 1 1 Vcc V88 28 28 27 1/00 1/07 27 07 1/00 1/06 26 0.06 1/02 1/05 25 0.05 1/04 24 0.06 1/02 1/05 25 0.05 1/04 24 0.06 1/02 1/05 25 0.05 1/04 24 0.06 1/02 1/05 1/04 1/05 1/0	D0 D1 D2	727 C258 HW5117800BJ-7EL 1 Vcc Vss 28 28 27 29 29 29 29 29 29 29	D7 06 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0	11 11 16 11	A9 A10 A10 A7 A6 A1 A2 A4 A3	C249 1 C286 MB51178008J-TEL 3	5 D5 D4	C210	D7 D8 D5 D4 A8 A8 A7 A8 A8
	A10 A0 A1 A2 A3 WE R3 WE R3 WE R3 FAS.4	D1 2 1/00 D1 3 1/00 D1 3 1/00 D2 1/00 D3 6 W 7 RAS D 8 N C W 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1	1/06 26 D6 1/05 25 D5 1/04 24 D4 CAS 23	A9 A8 A7 A6 A1 A5 A4 A3	274 1 1 1 2 2 8 9 1 7 8 1 7 8 9 8 9 1 7 8 1 7 8 9 8 9 1 7 8 1 7 8 9 8 9 1 7 8 1 7 8 9 1 7 9 1	00 01 02 03	1 1 1/C259 MB5117800BJ-7EL 1 1 Vcc Vss 28 2 1/700 1/77 27 3 1/701 1/76 26 4 1/702 1/705 25 5 1/703 1/74 24 6 1/702 1/705 25 6 6 WE CAS 23 7 C RAS 25 8 NC 259 49 21 9 A10 A8 20 10 A0 A7 18 11 A1 A6 18 12 A2 A5 17 13 A3 A4 15 14 Vcc Vss 15	AB. A10 AB. A1	14 Vcc Vss 1	 	C249 12207 HHS117800BJ-7EL 1	AB AB AA A	C519 1271 MM51178088J-7EL 28 29 27 28 29 29 29 29 29 29 29	07 08 08 08 08 08 08 08 08 08 08 08 08 08
	ME 63 ME 83 ME 83 ME 83 ME 84 ME 84	HM511	\$\frac{23}{11}\$ \$\frac{1}{16868}_{3}\$-7EL\$ \$\frac{7}{127}\$ \$\frac{2}{7}\$ \$\frac{7}{127}\$ \$\frac{7}{27}\$ \$\frac{7}{126}\$ \$\frac{2}{68}\$ \$\frac{1}{68}\$ \$\frac{2}{68}\$ \$\frac{1}{68}\$ \$\frac{2}{68}\$ \$\frac{1}{68}\$ \$\frac{2}{68}\$ \$\frac{2}{68}\$ \$\frac{2}{2}\$	A9 A10 A7 A6 A1 A2 A3	1 1 1 1 1 1 1 1 1 1	A9 A10 A10 A1 A1 A2 A3	Column C	#E. R7 #E. 07 #E. 87 #E. 87 #RAS. 8 OE. 8 #E. 88 #E. 88 #E. 88 #E. 88 #E. 88 AB	1 Vec Vec	97 96 95 94 AB AB A7 A6 A1 A2 A3	CANT 6.1 HWS1178089-7EL HWS1178089-7EL 1 Vcc Vss 2 1 3. 1/01 1/06 2 2 4 1/02 1/05 2 3 1/01 1/06 2 3 5 1/03 1/04 2 3 6 WE GRS 2 2 8 NC 268 A9 2 9 A10 A6 2 10 A0 A7 1 11 A1 A6 1 12 A2 A5 1 13 A3 A4 1 14 Vcc Vss 1	97 98 95 95 94 10 10 10 10 10 10 10 10 10 10 10 10 10	Color Colo	07 06 05 05 04

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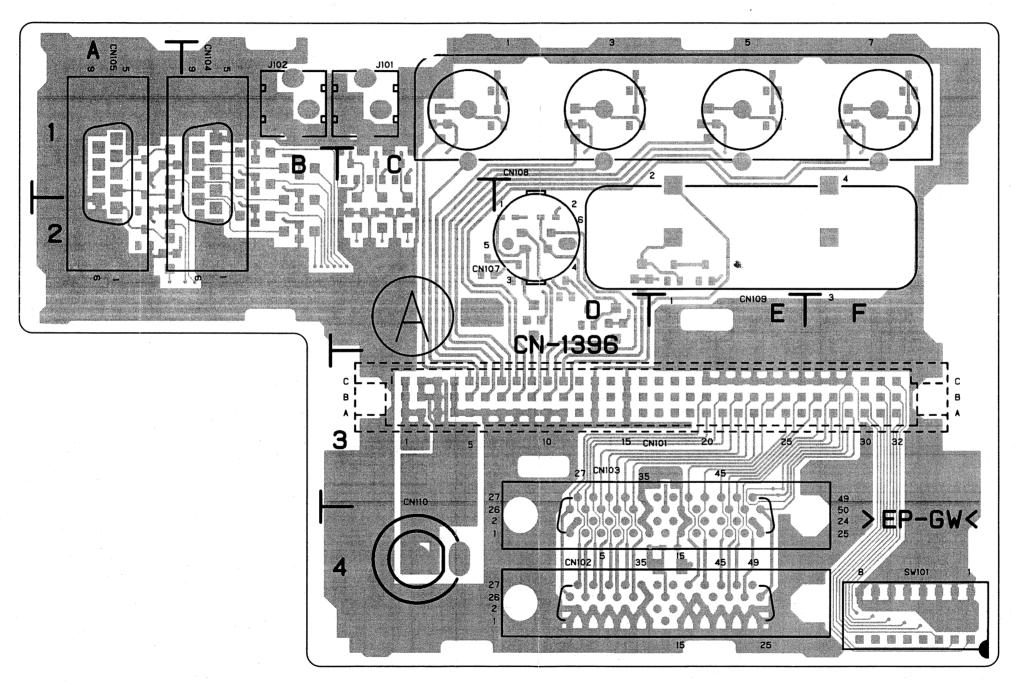




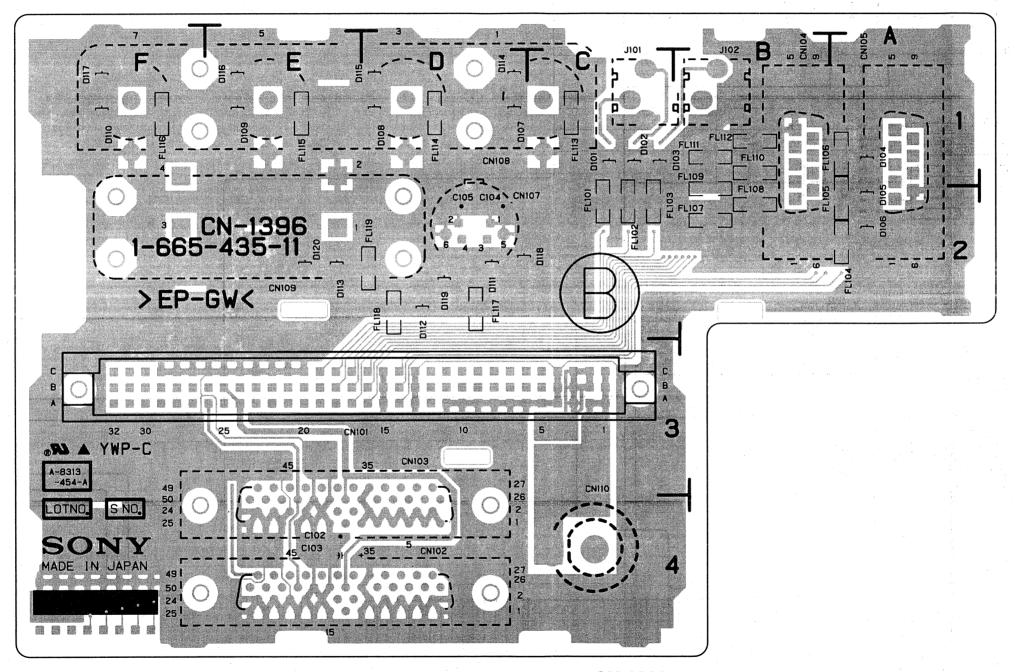
CN-1396 BOARD

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CN102 E4
CN103 E4
CN107 D2
CN108 C1
CN109 E2
CN110 C1
* D107 D1
* D108 D1
* D109 E1
* D110 F1
* D111 D2
* D112 D2
* D113 E2
* D114 D1
* D115 D1
* D116 E1
* D117 F1
* D118 D2
* D119 D2
* D110 E2
* FL109 B2
* FL109 B2
* FL109 B2
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* FL108 B2
* FL109 B2
* FL110 B1
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* FL111 B1
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* FL

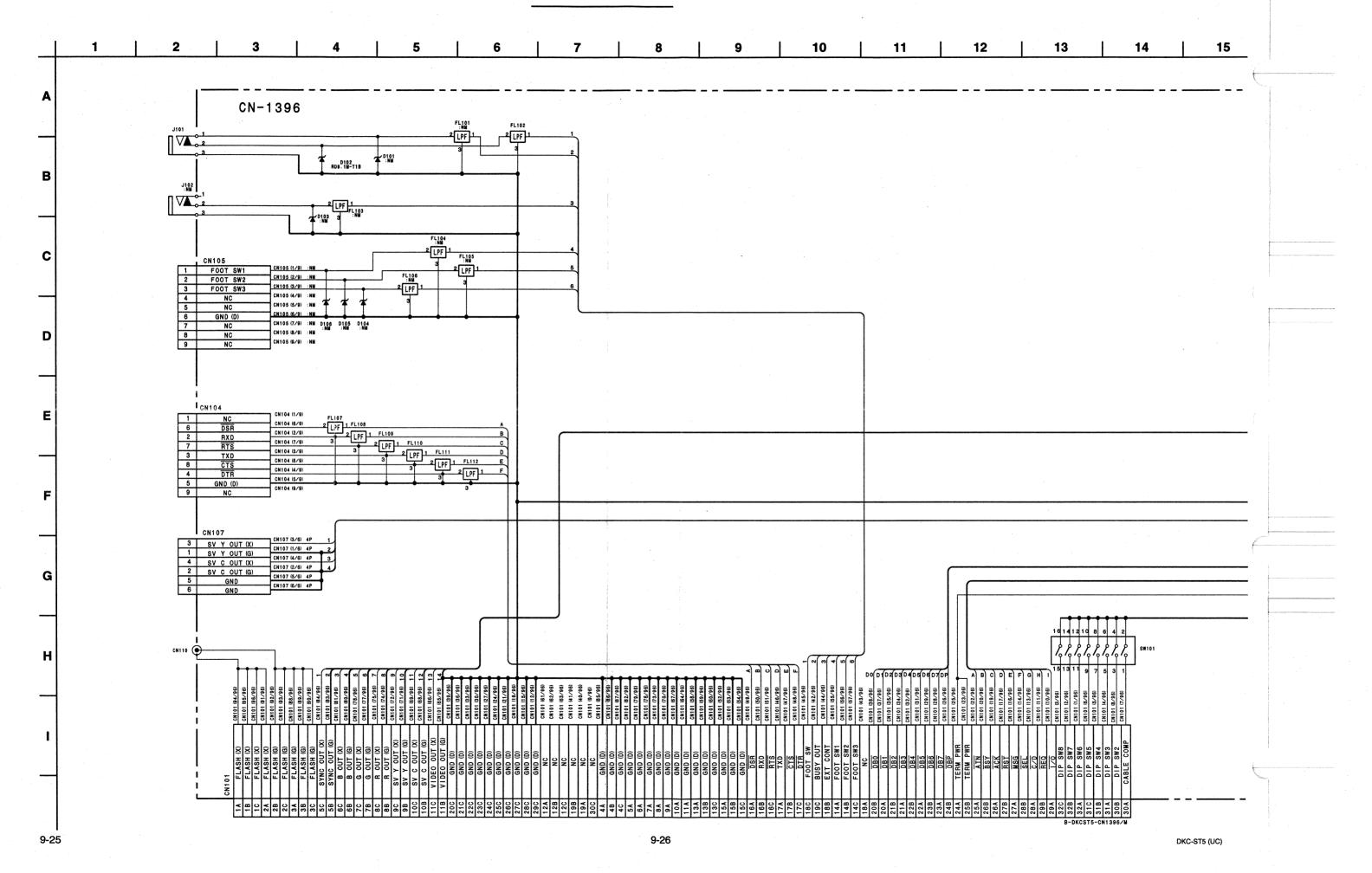
* B SIDE

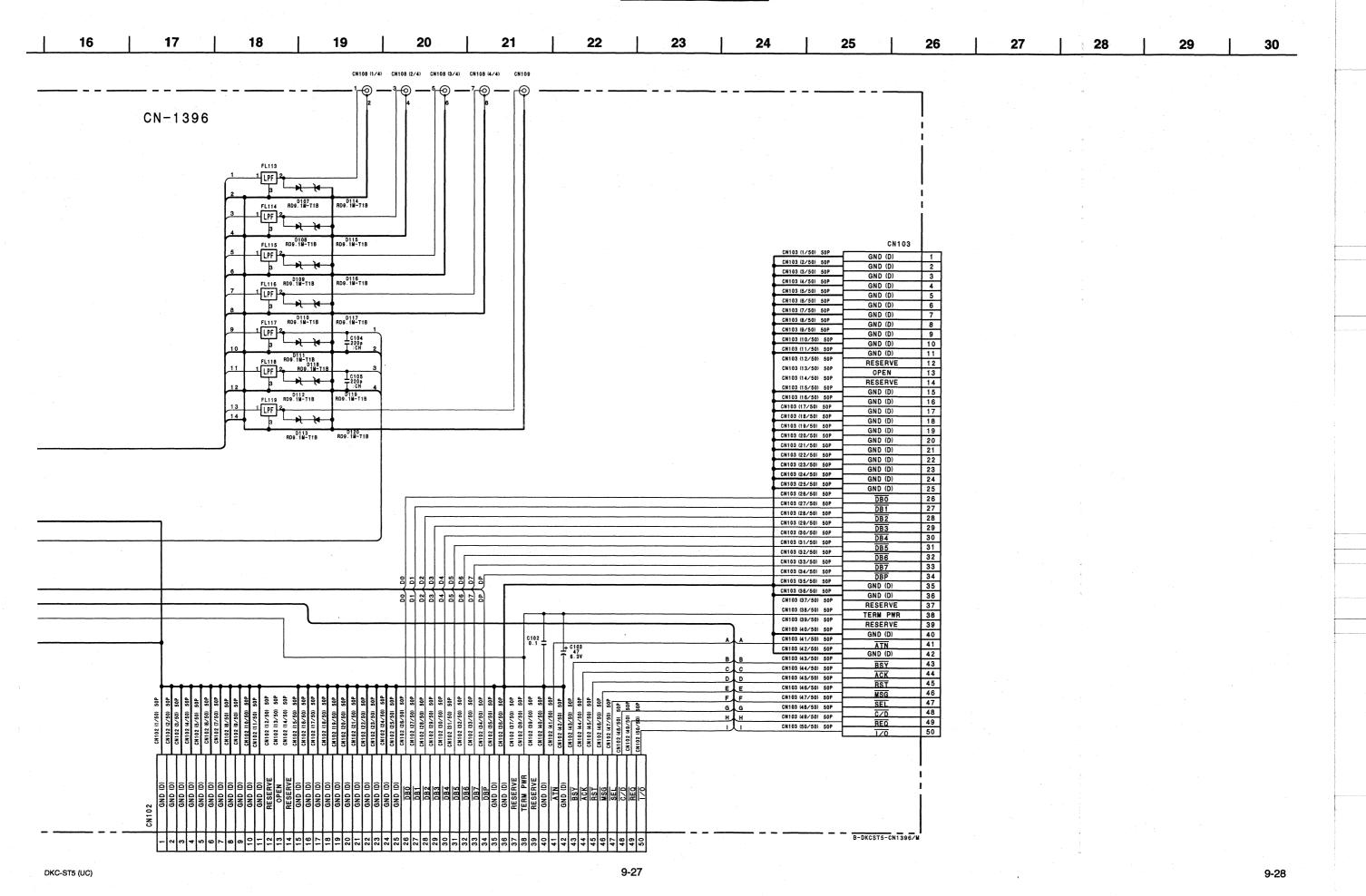


CN-1396 A SIDE 1-665-435-11

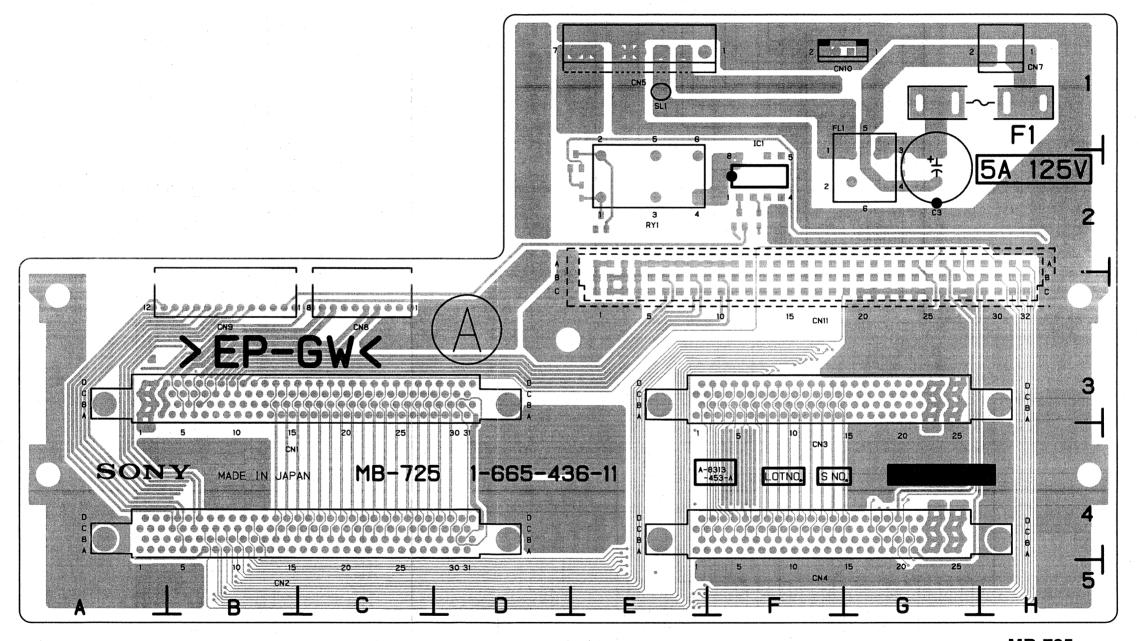


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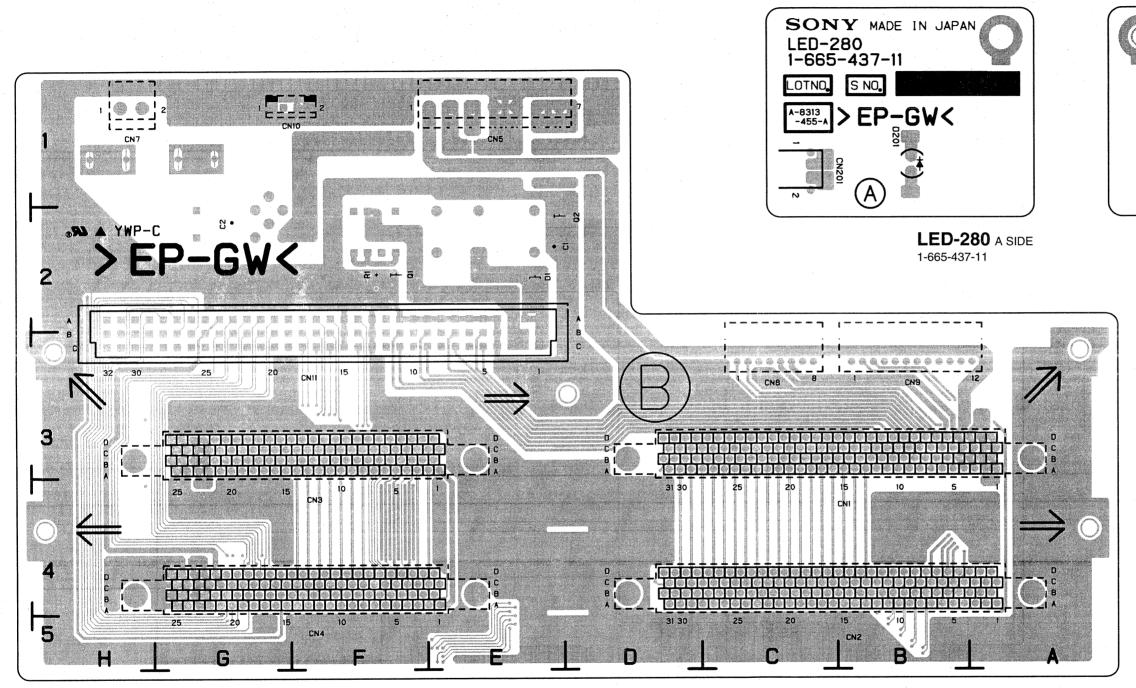




CN1 A3
CN2 A4
CN3 E3
CN4 E4
CN5 E1
CN7 H1
CN8 C3
CN9 B3
CN10 G1
*CN11 E2
*D1 E2
FH1 G1
FH2 H1
FH2 H1
IC1 F2
*Q1 F2
*Q1 F2
RY1 E2



MB-725 A SIDE 1-665-436-11



MB-725 B SIDE 1-665-436-11

. PA A YWP-C
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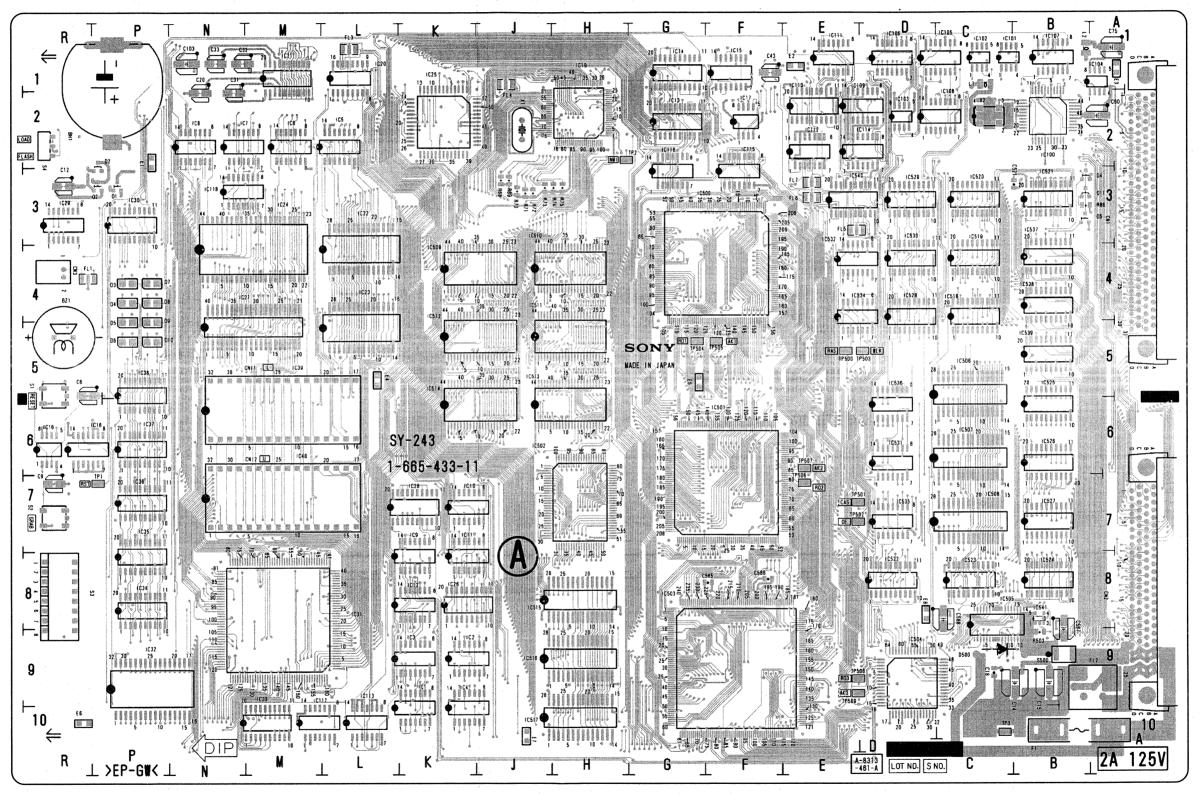
LED-280 B SIDE

1-665-437-11

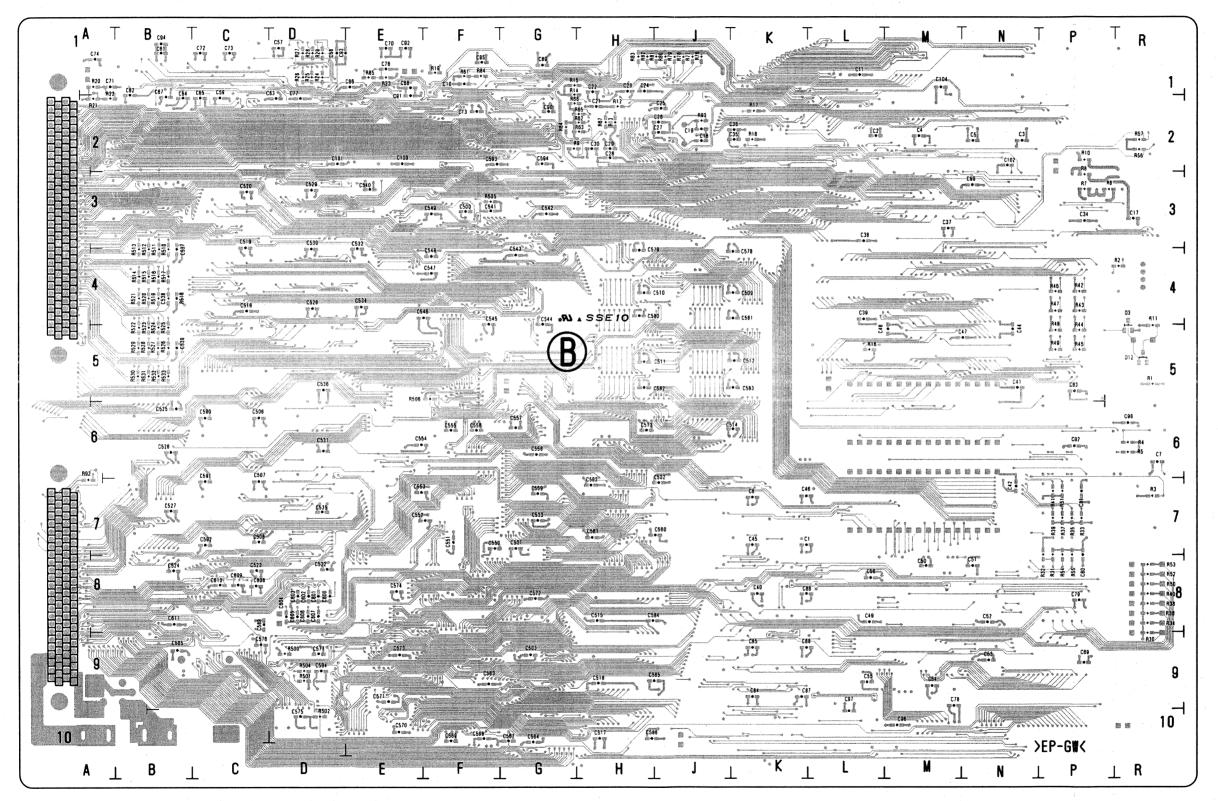
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	6 WVD (X) CN6 (g/12) 12P 7 5 WVD (G) CN9 (g/12) 12P 8 4 WHD (X) CN9 (g/12) 12P 9 3 WHD (G) CN9 (g/12) 12P 10	
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	MB-725				(+) (+)	18 CM4 (12/100) X WE R 5B 17 CM4 (18/100) X WE G 5C 18 CM4 (22/100) X WE G 5C 19 CM4 (22/100) DIR 6A 20 CM4 (22/100) DR0 6B 21 CM4 (22/100) DR1 6C 22 CM4 (24/100) DR2 6D 23 CM4 (24/100) DR2 6D 24 CM4 (22/100) DR3 7A 24 CM4 (22/100) DR3 7A 25 CM4 (28/100) DR4 7B 26 CM4 (28/100) DR5 7C 28 CM4 (28/100) DR6 7D
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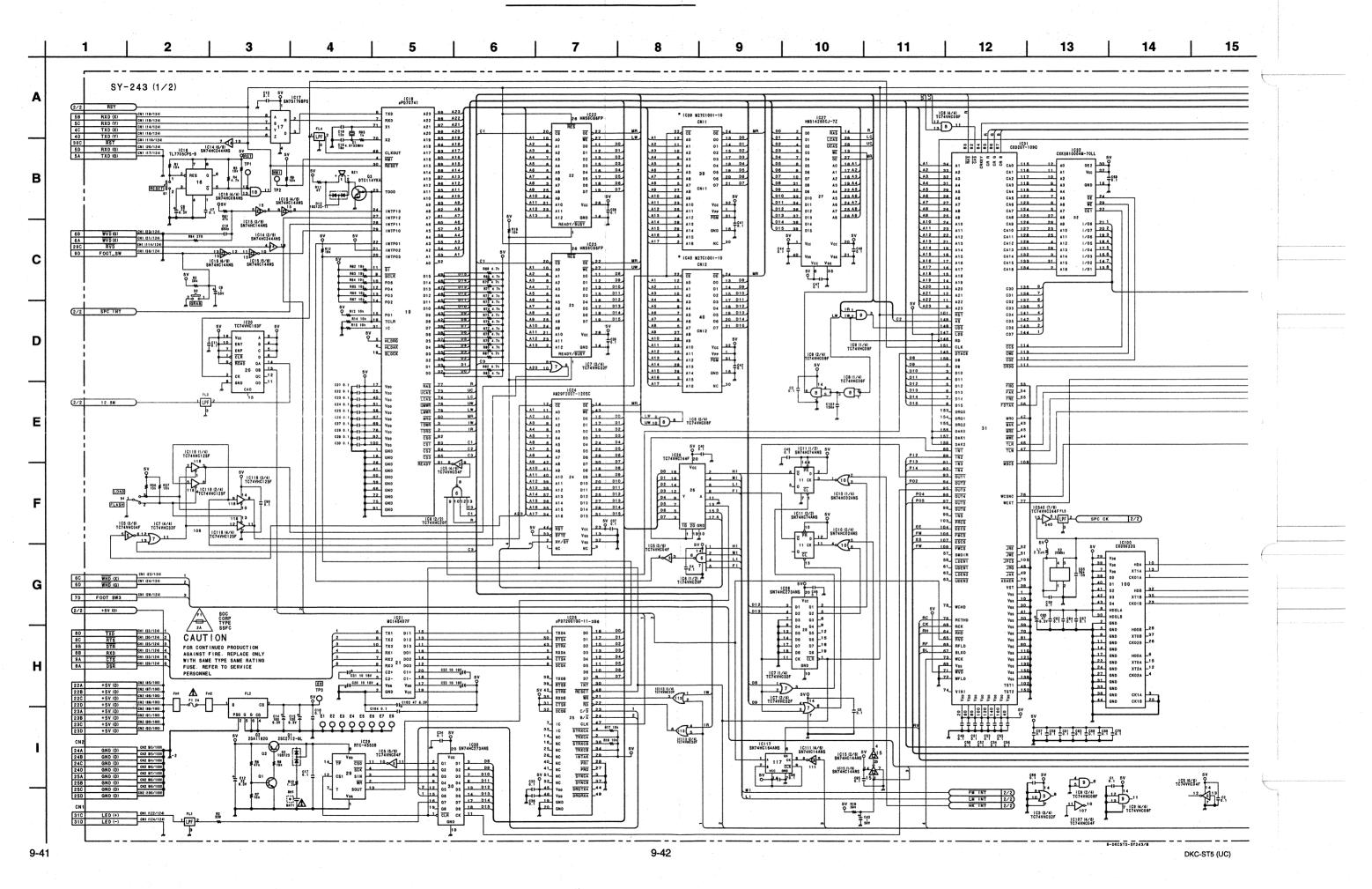
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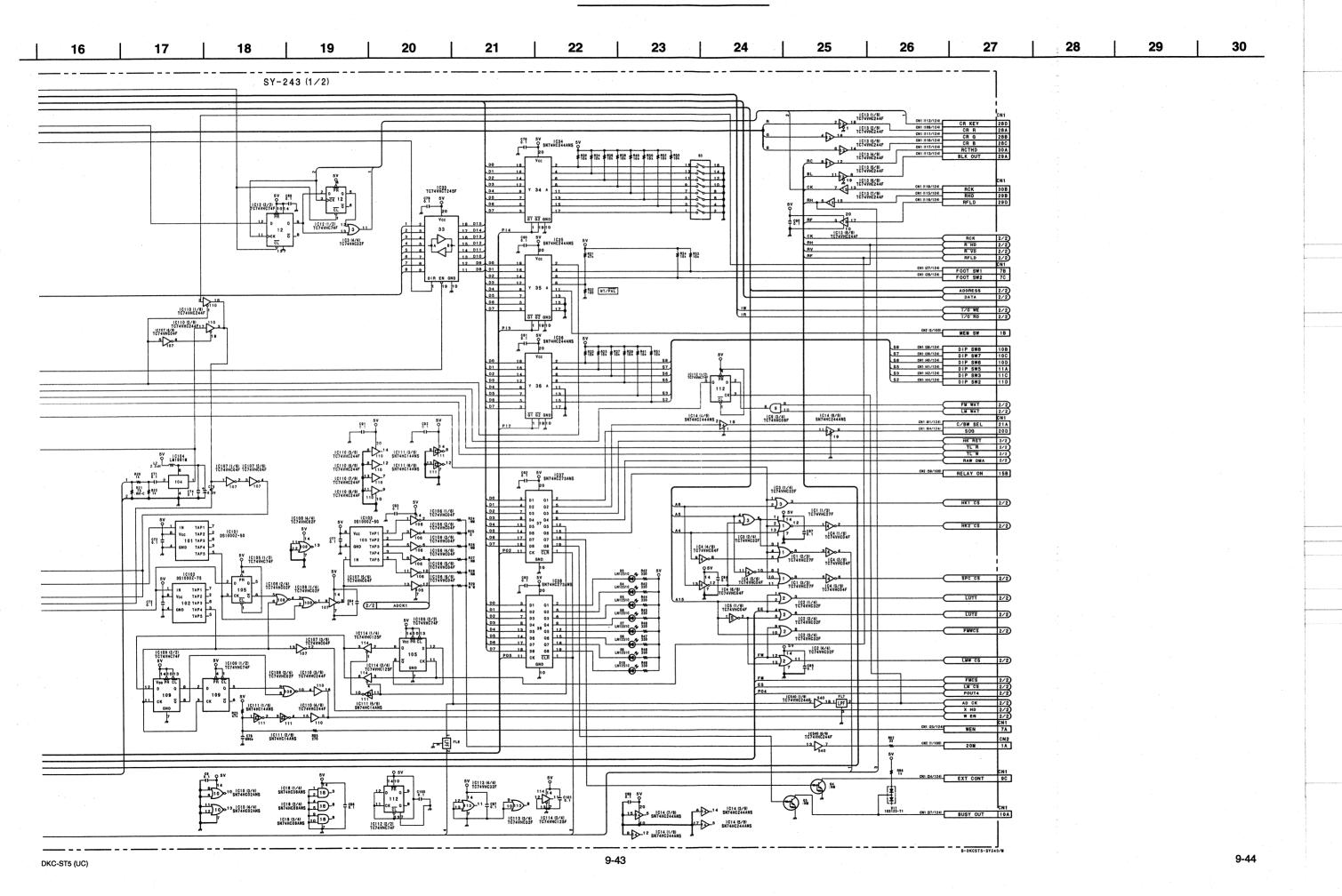


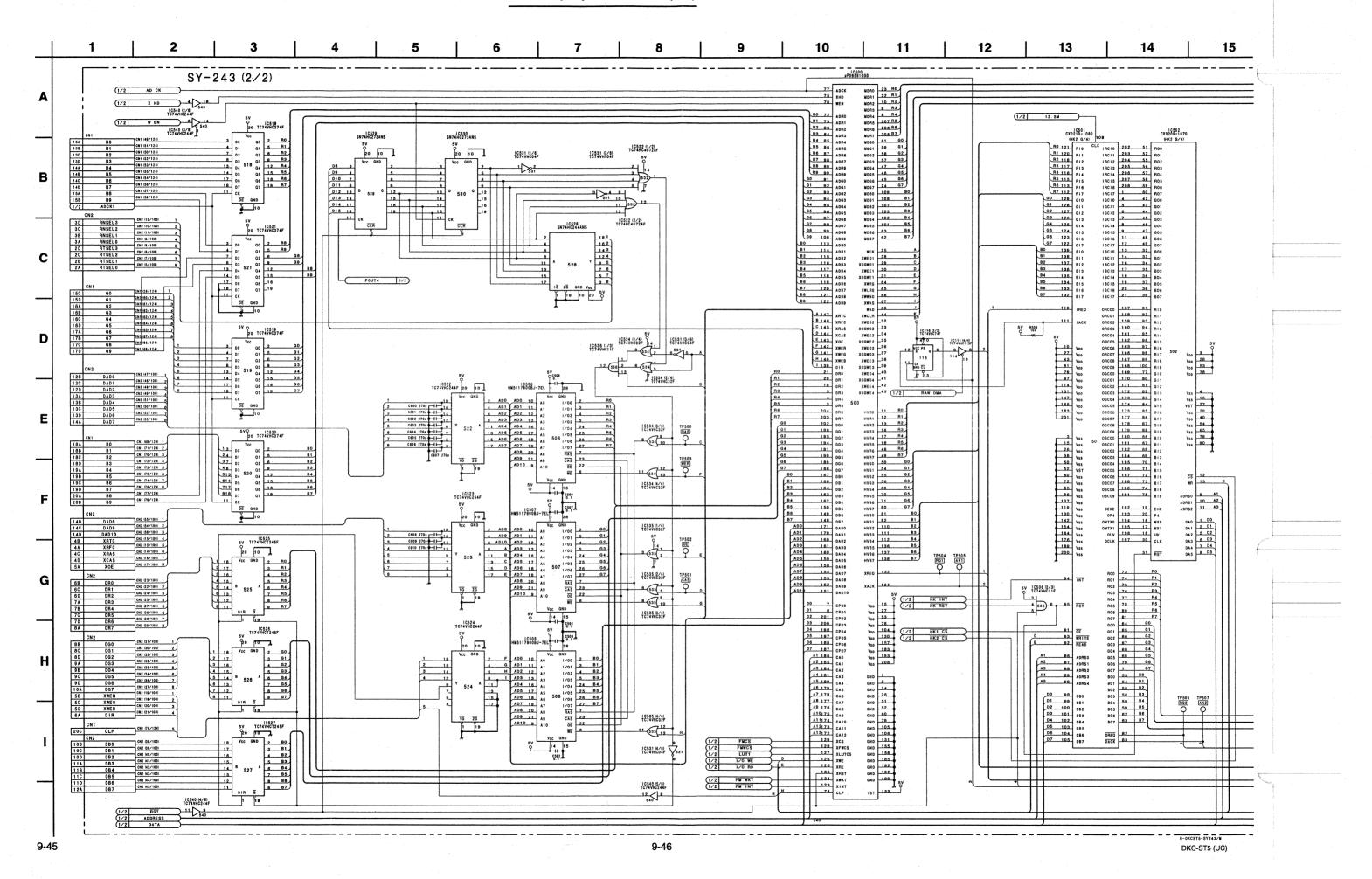
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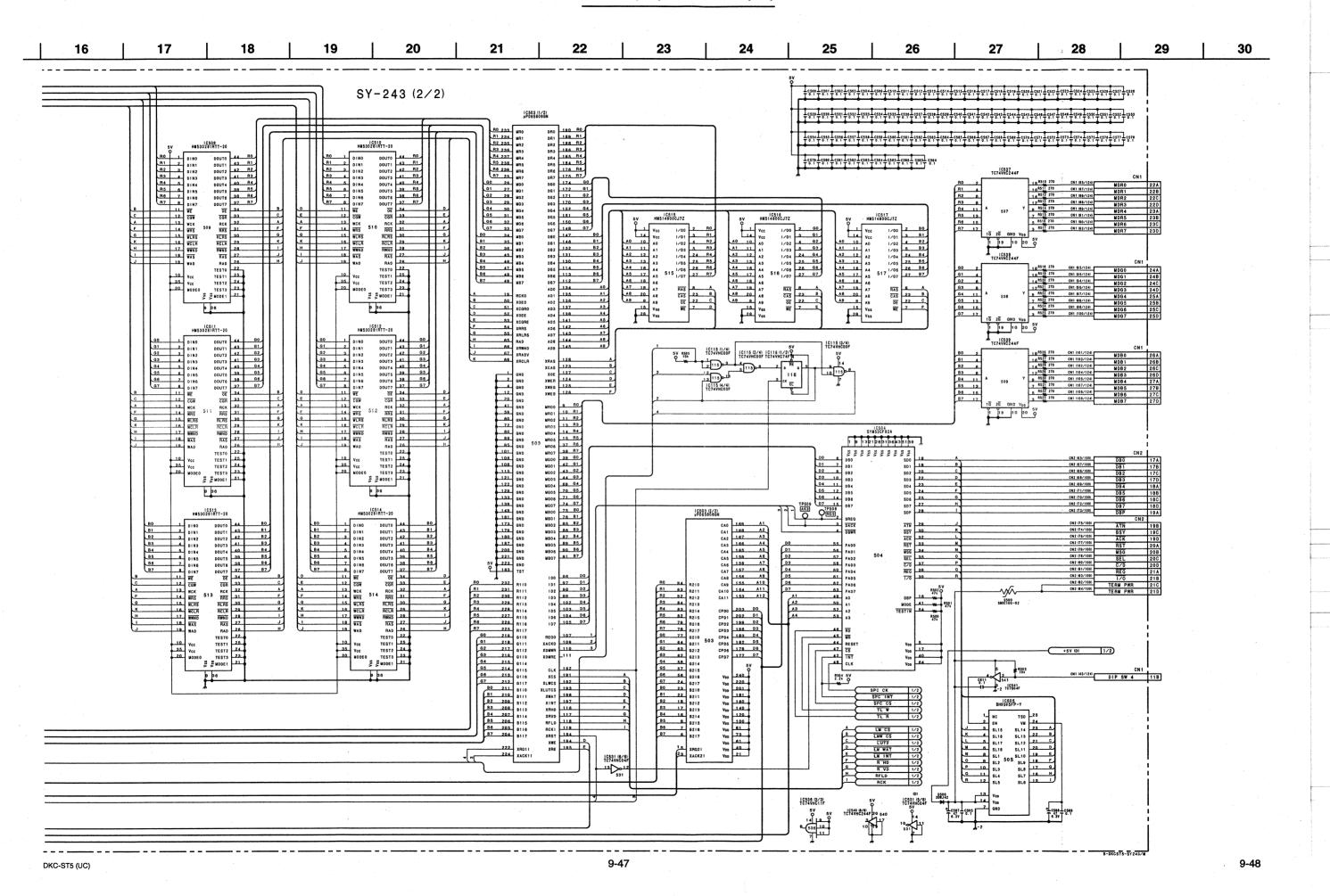


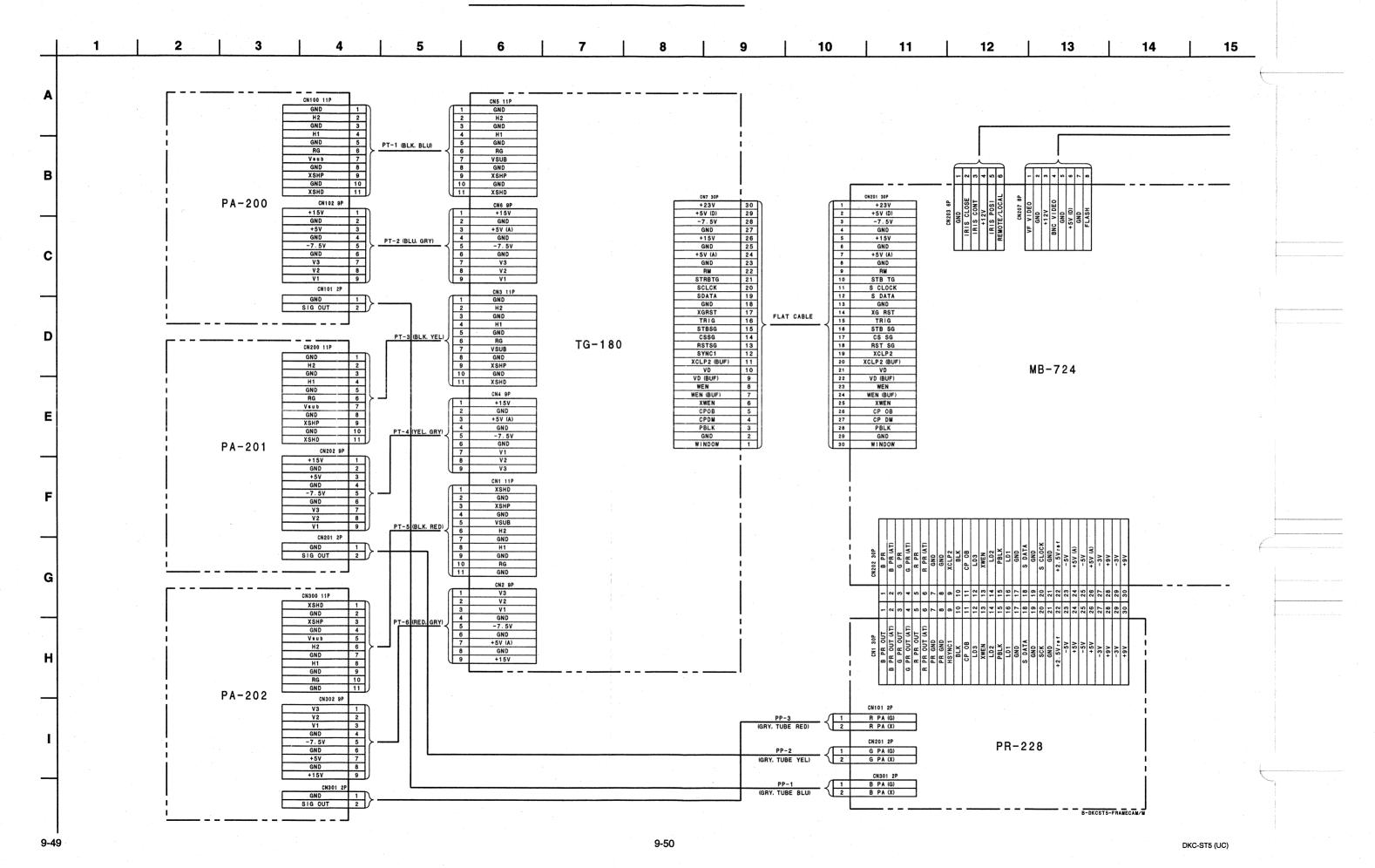
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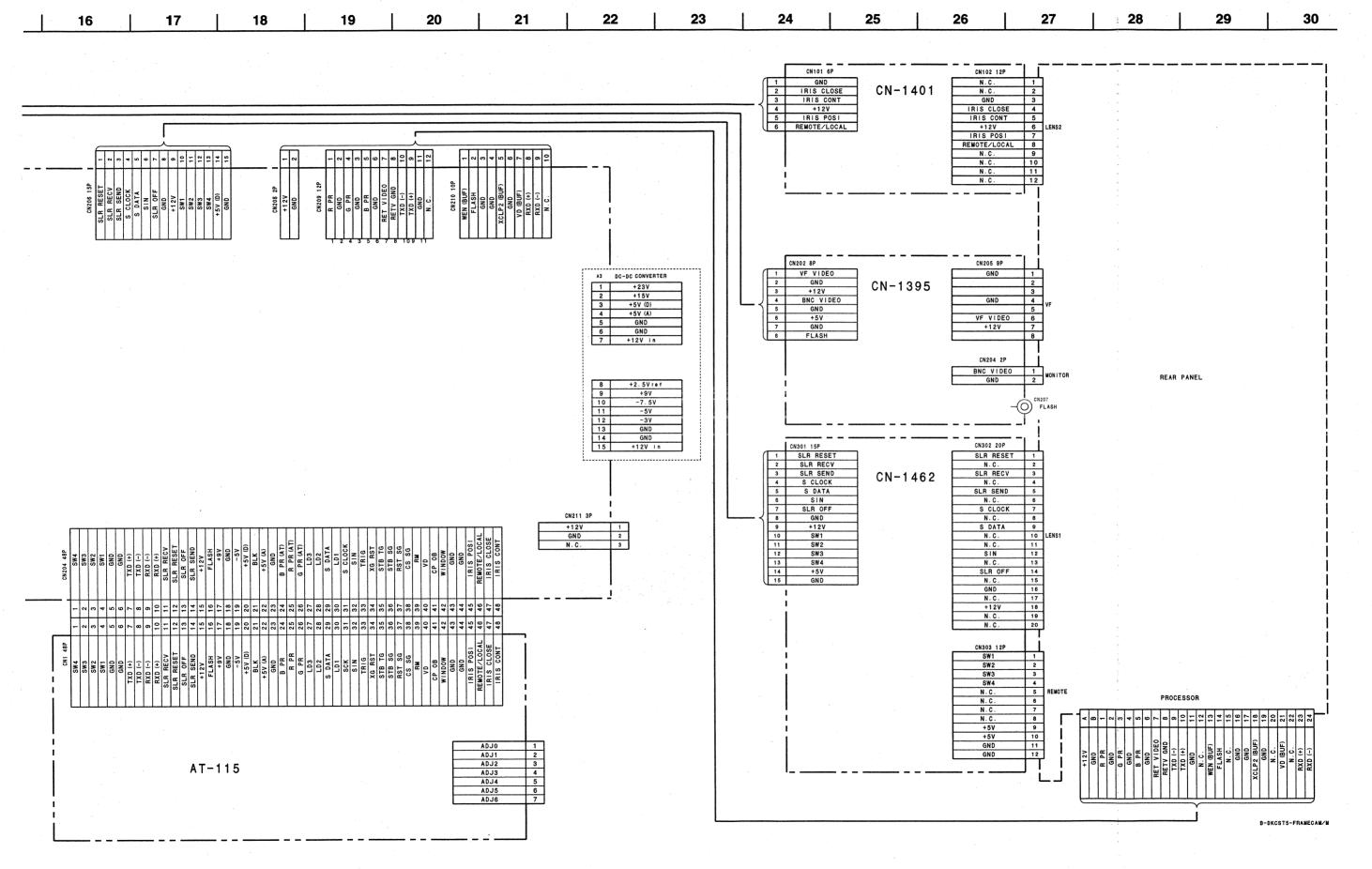


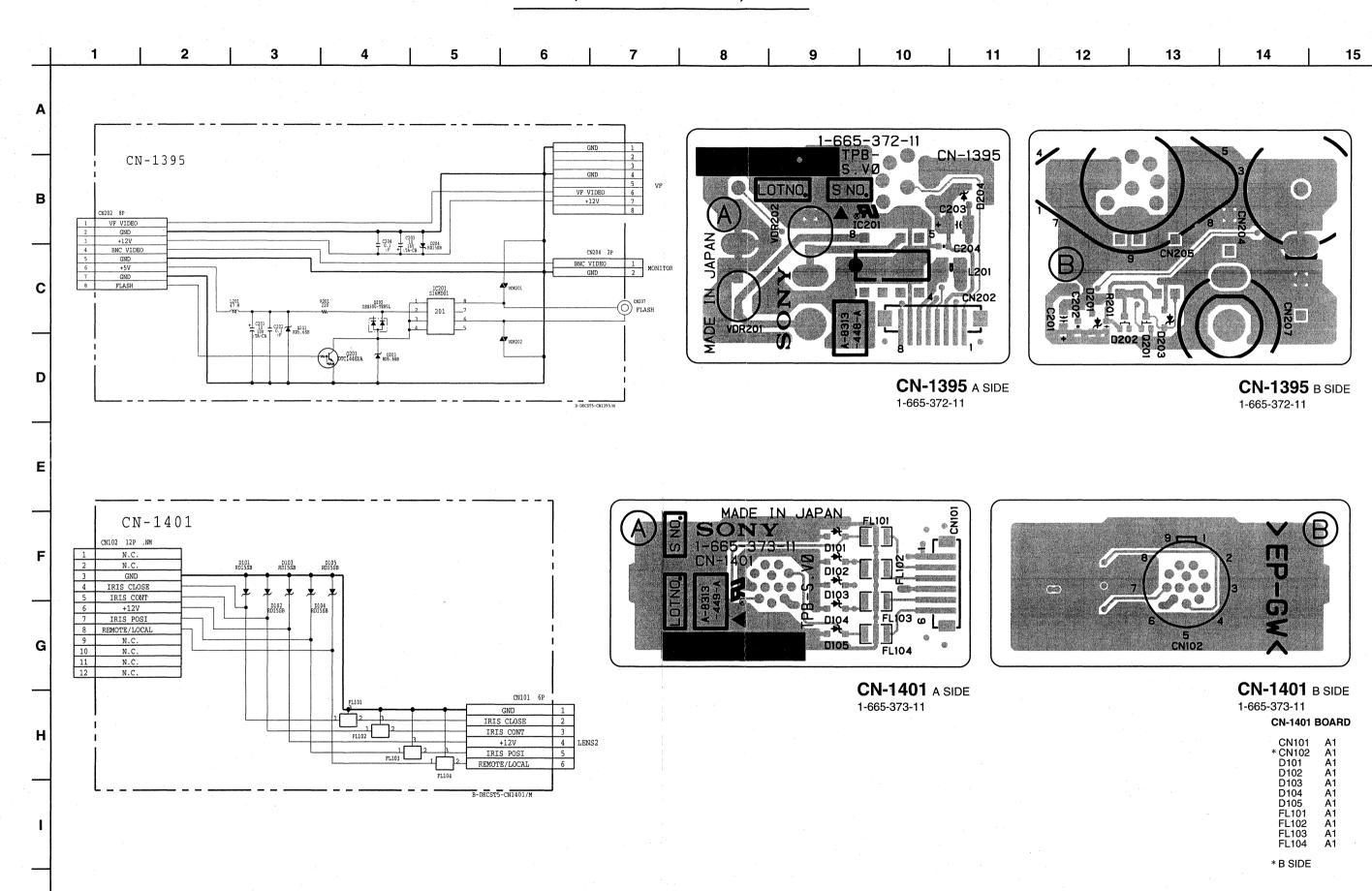






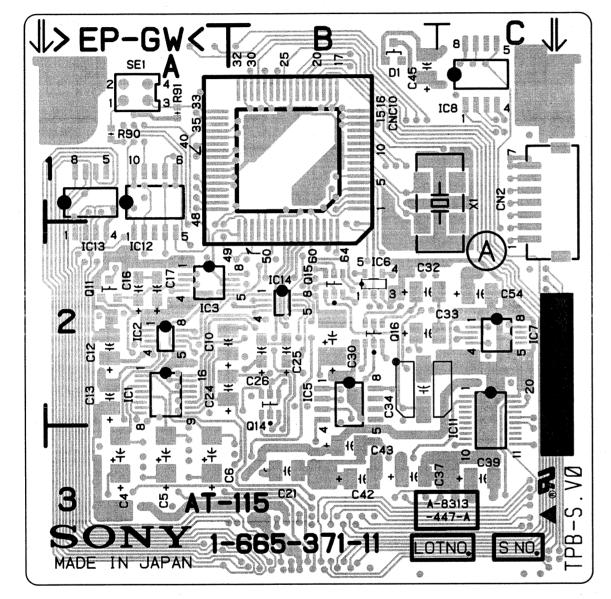




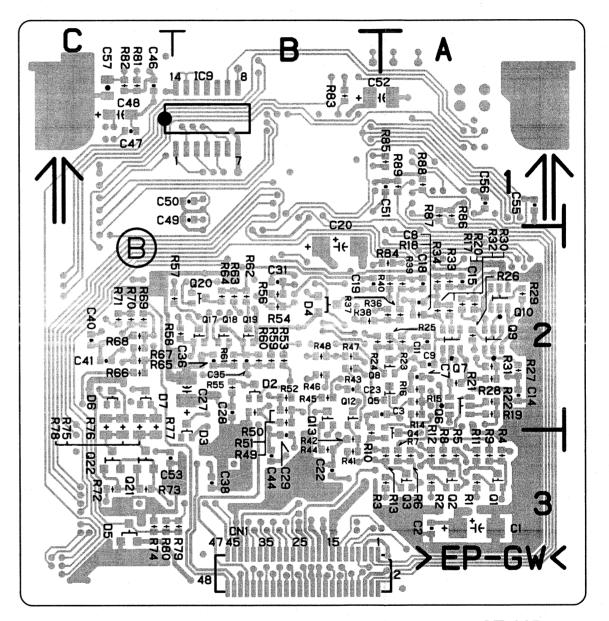


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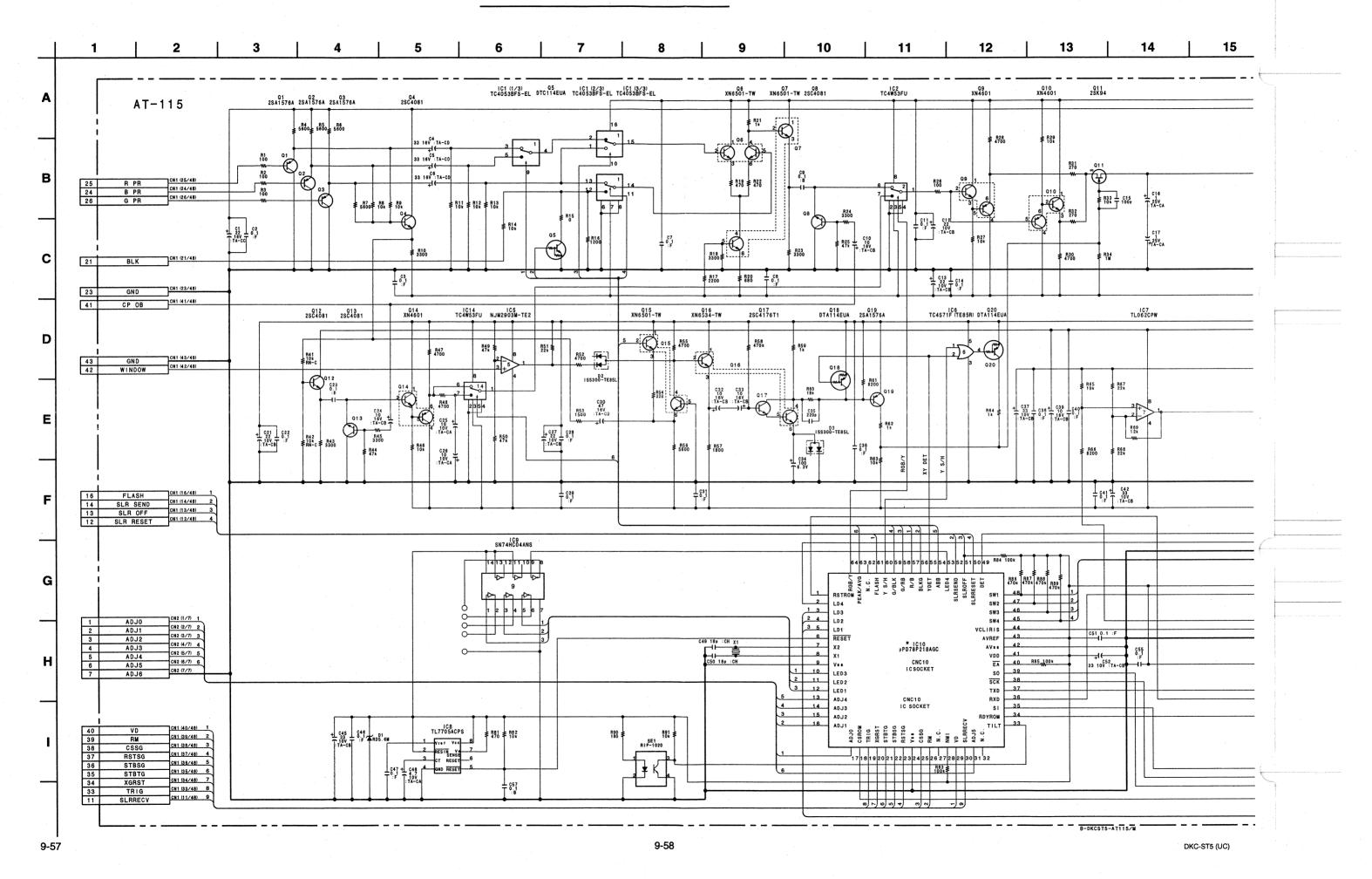
AT-115 BOARD

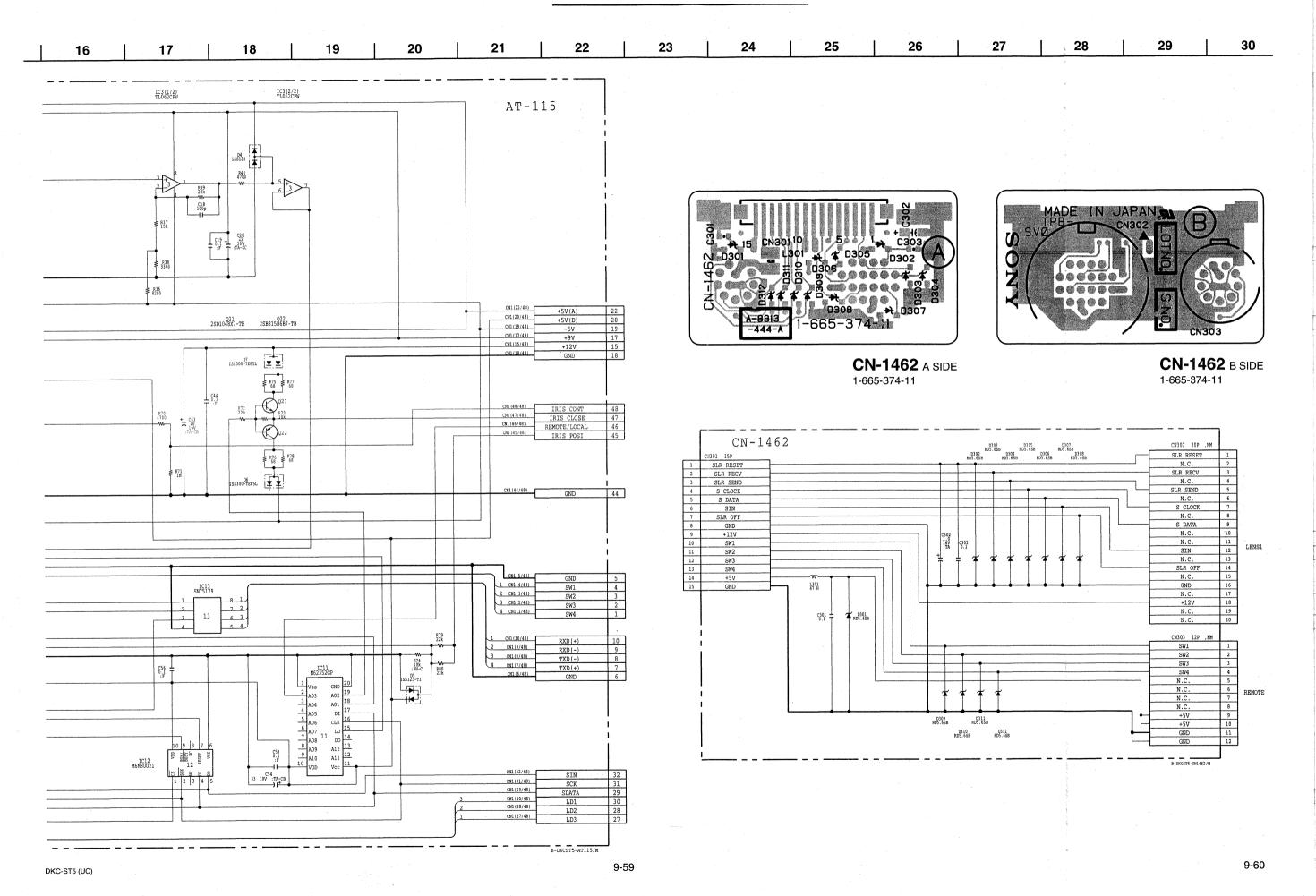


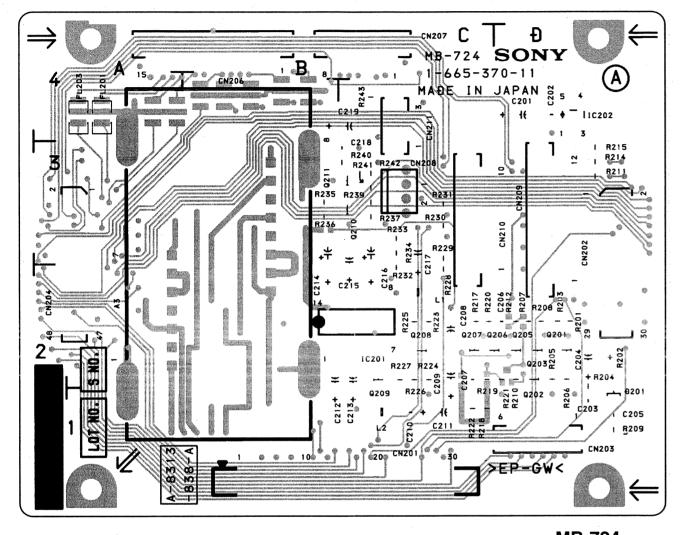
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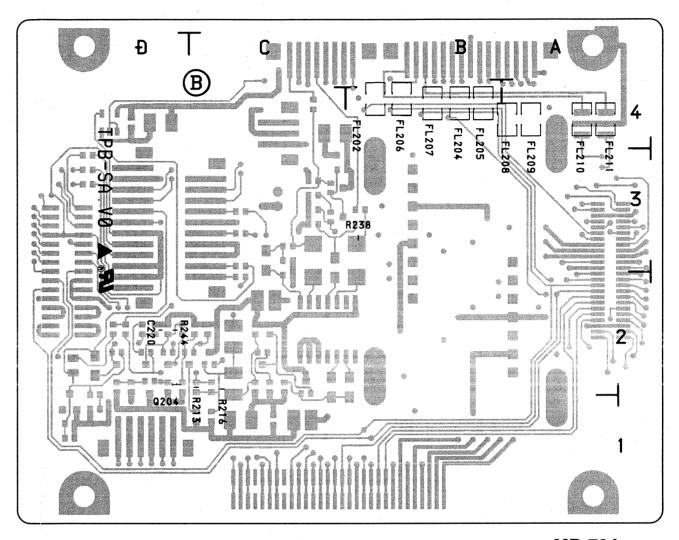
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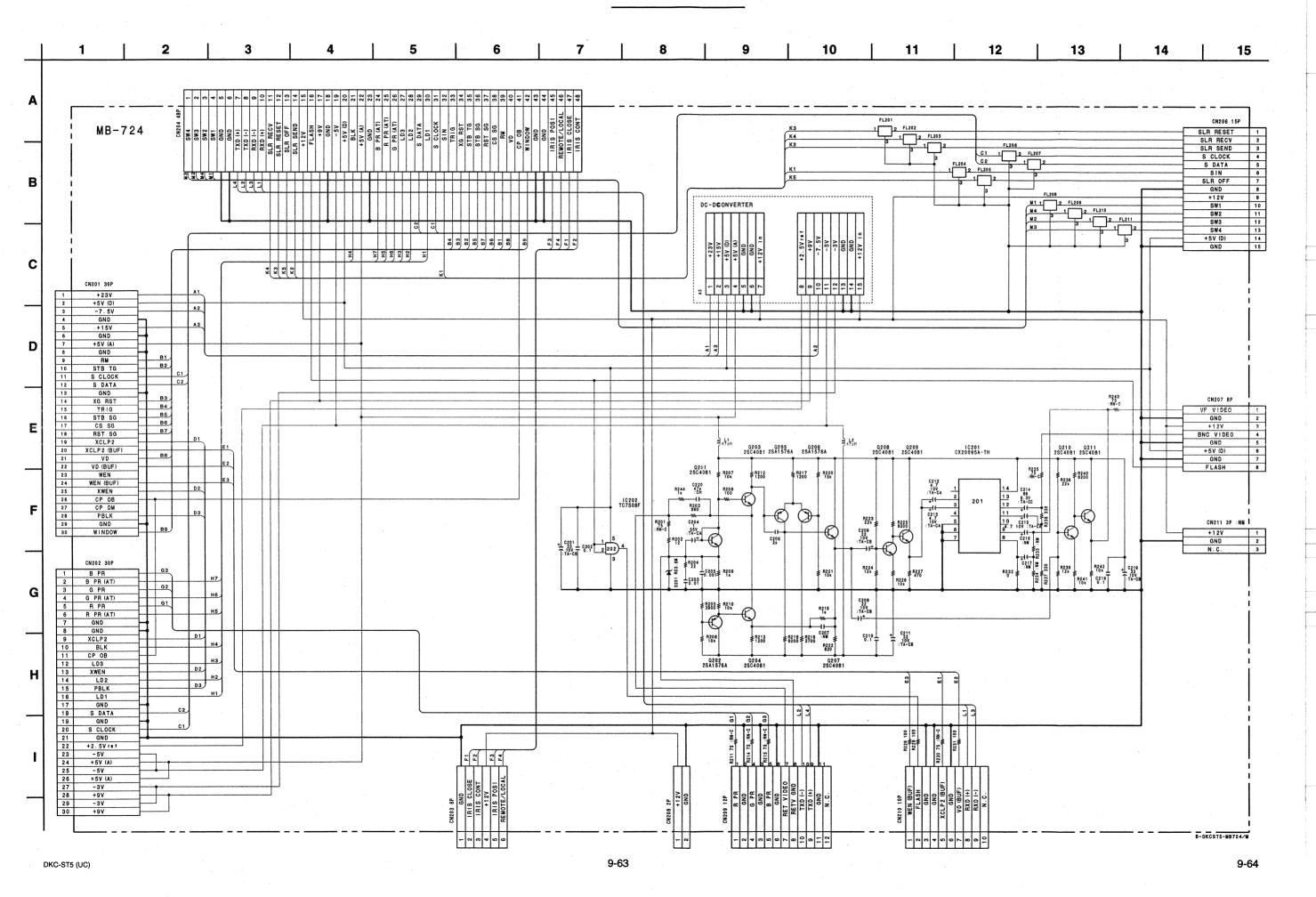
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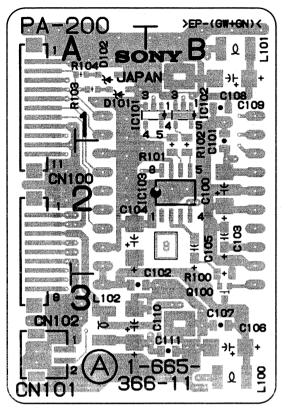


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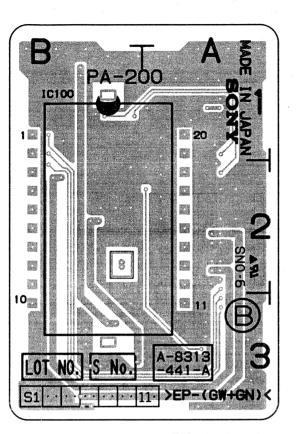
MB-724 BOARD

CN201 CN202 CN203 CN204 CN206 CN207 CN208 CN209 CN210 D201 FL201 * FL202 FL203 * FL205 * FL206 * FL207 * FL208	C1 D2 D1 A2 B4 C3 D3 C3 D1 A4 B4 B4 B4 B4 B4	* FL209 * FL210 * FL211 IC201 IC202 L1 L2 Q201 Q203 * Q204 Q205 Q206 Q207 Q208 Q209 Q210 Q211	A4 A4 A4 C2 D4 C2 D2 D2 D2 D2 D2 C2 C3 C3
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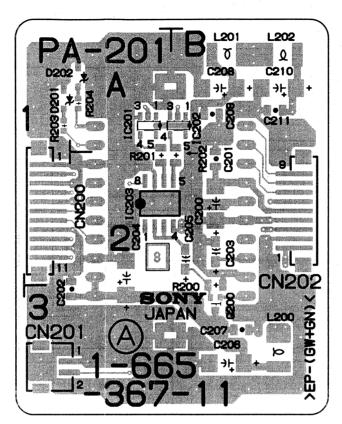




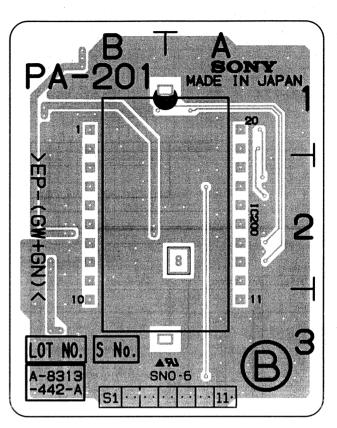
PA-200 A SIDE 1-665-366-11



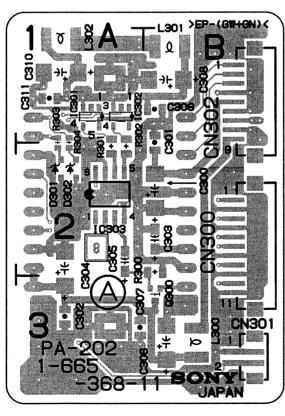
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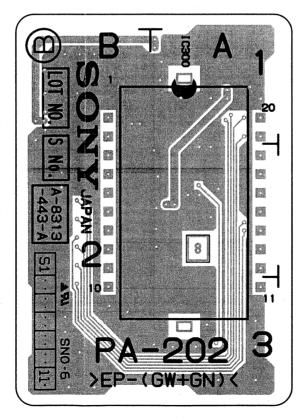
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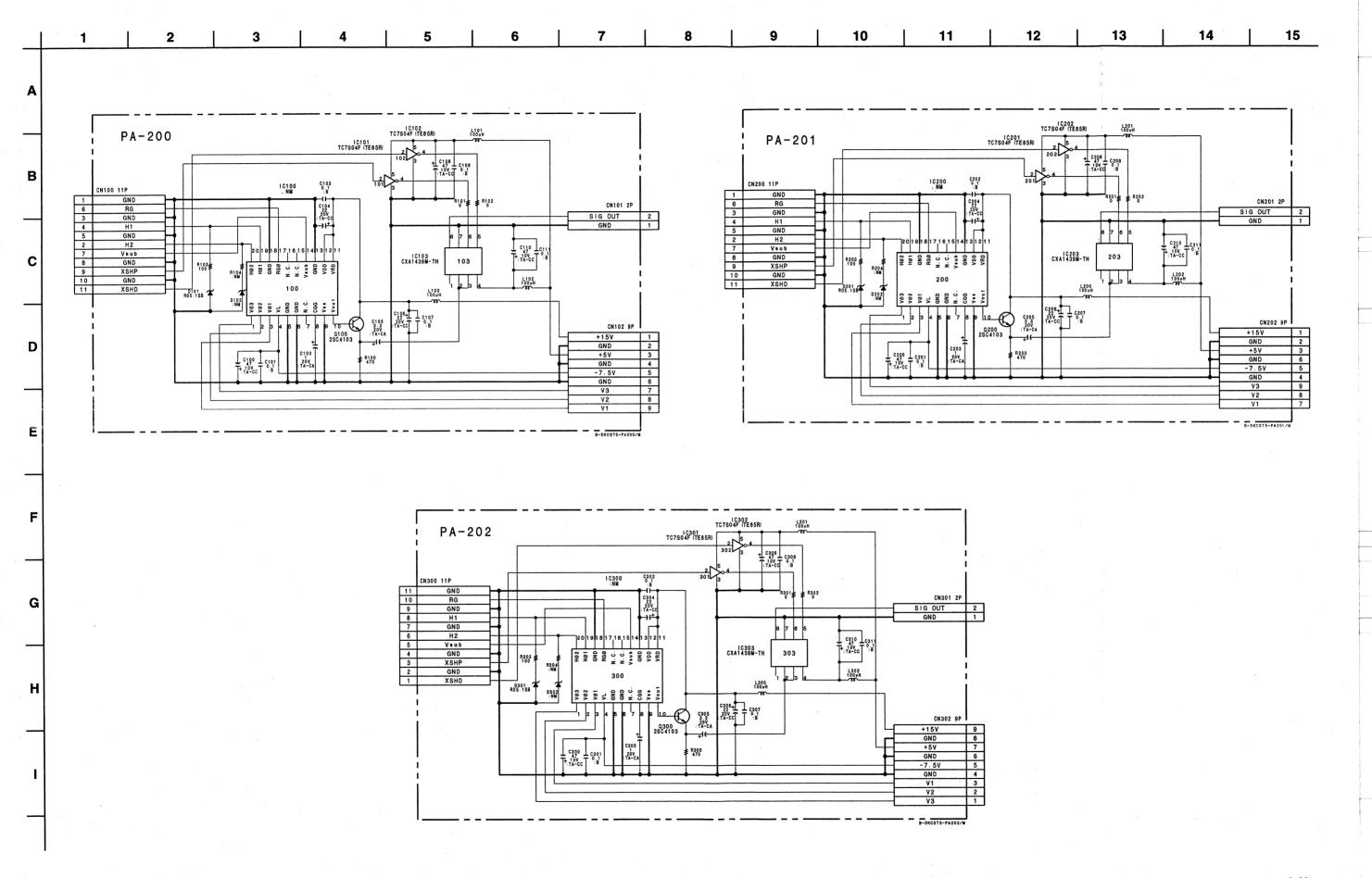
PA-201 B SIDE 1-665-367-11



PA-202 A SIDE 1-665-368-11



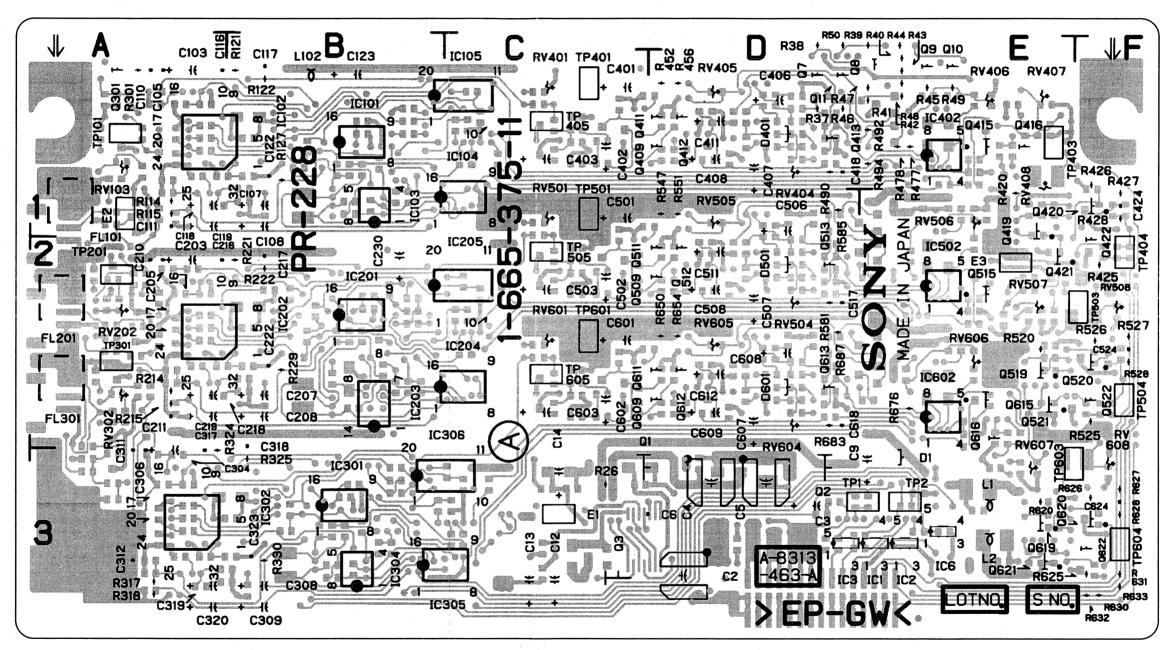
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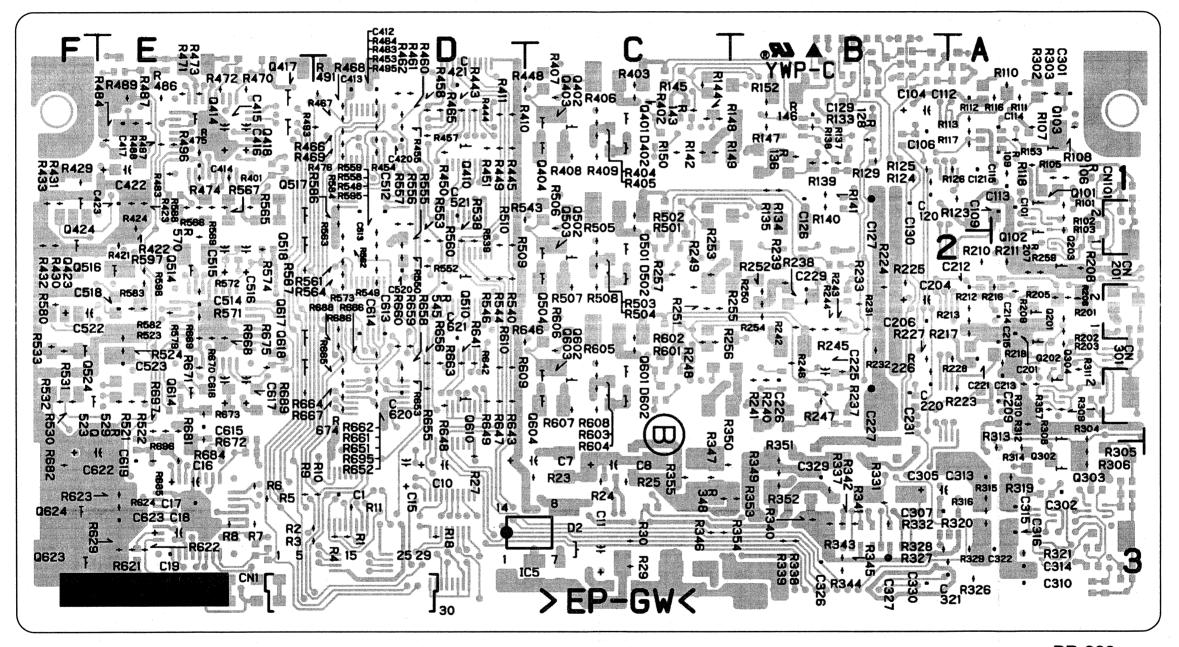
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PR-228 BOARD

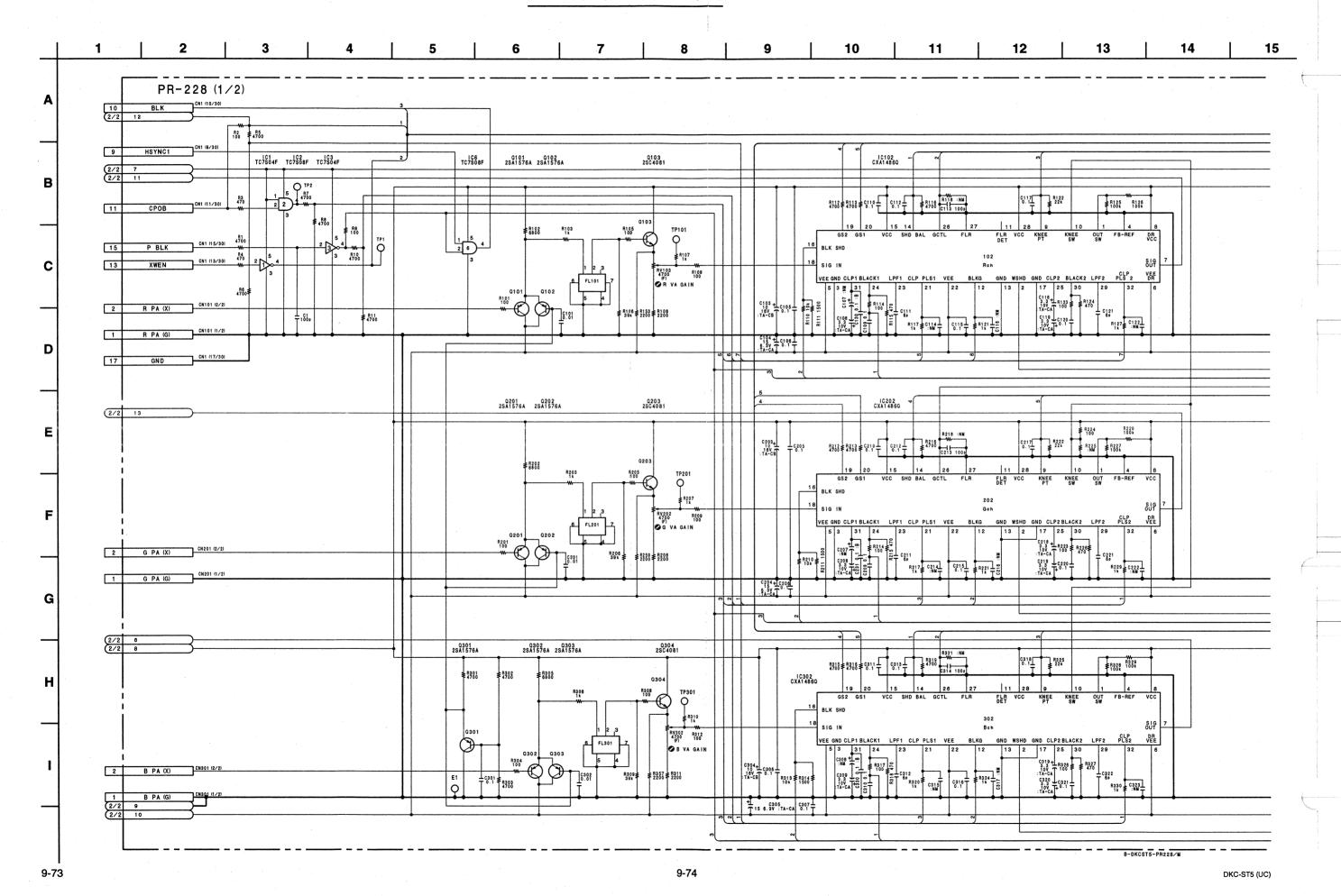
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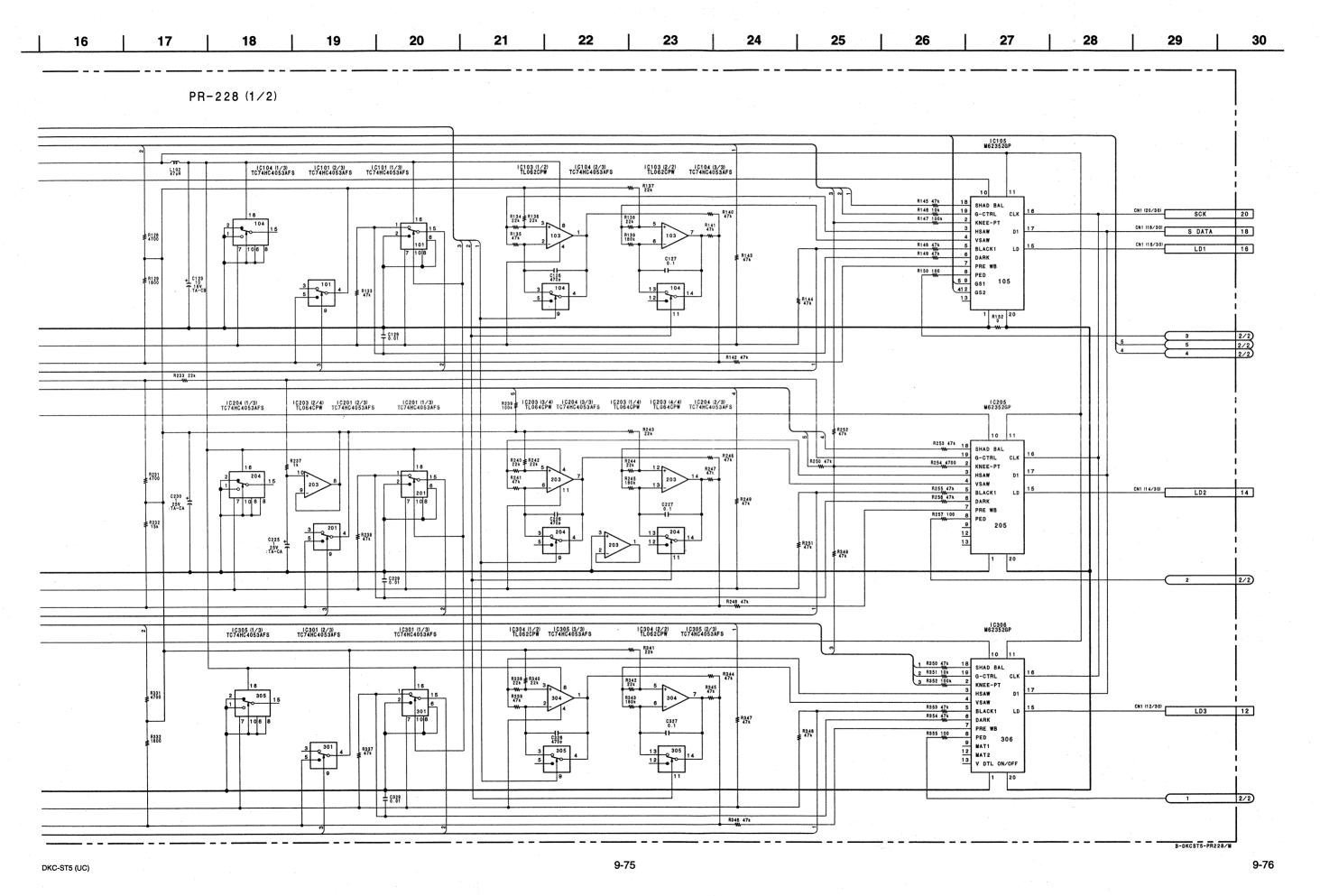


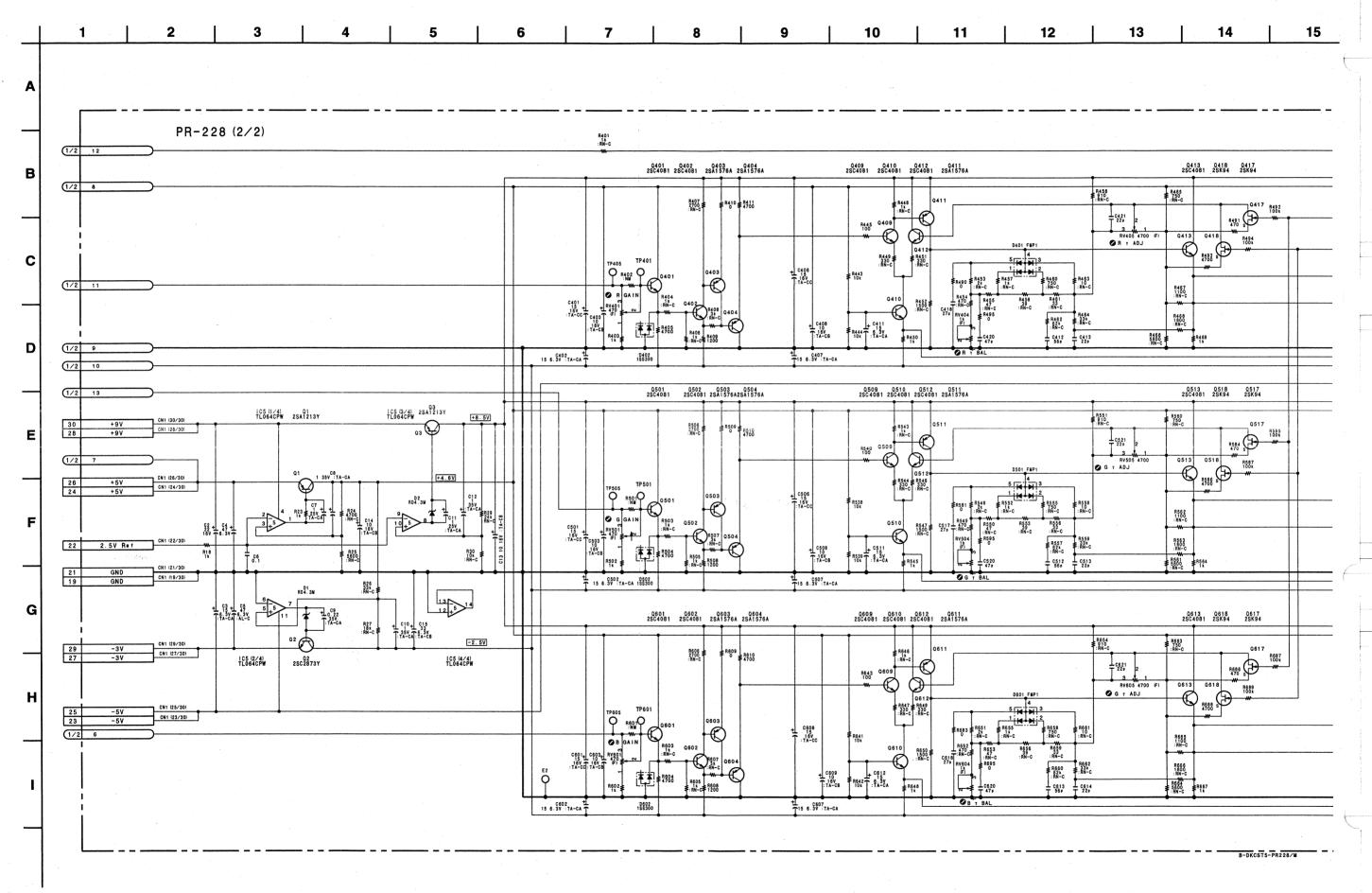
PR-228 A SIDE 1-665-375-11



PR-228 B SIDE 1-665-375-11

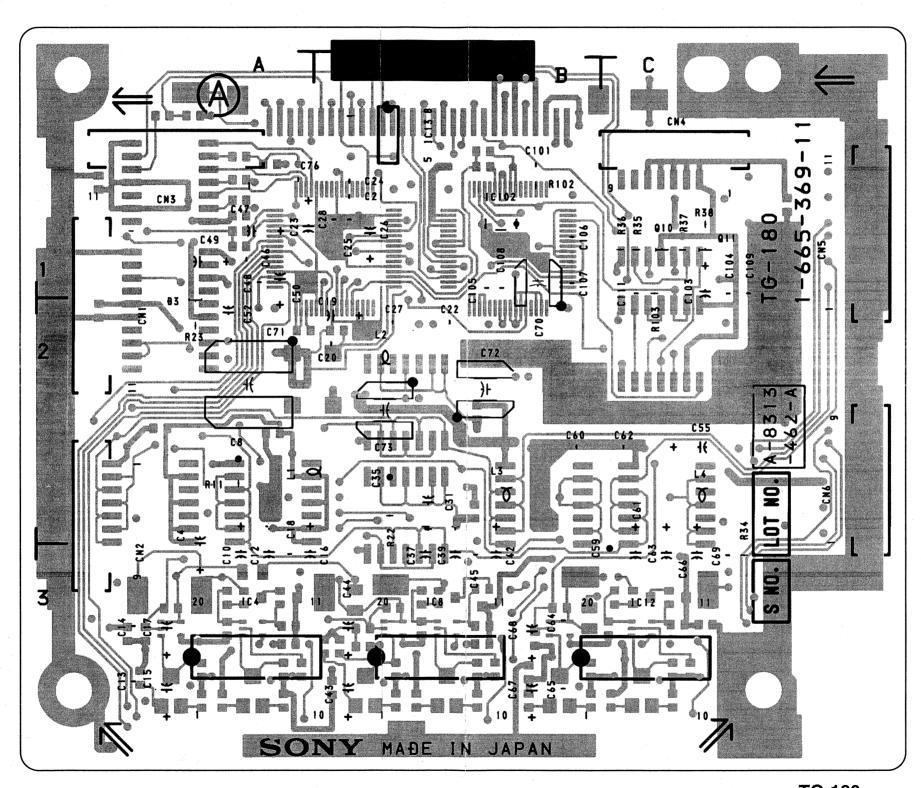




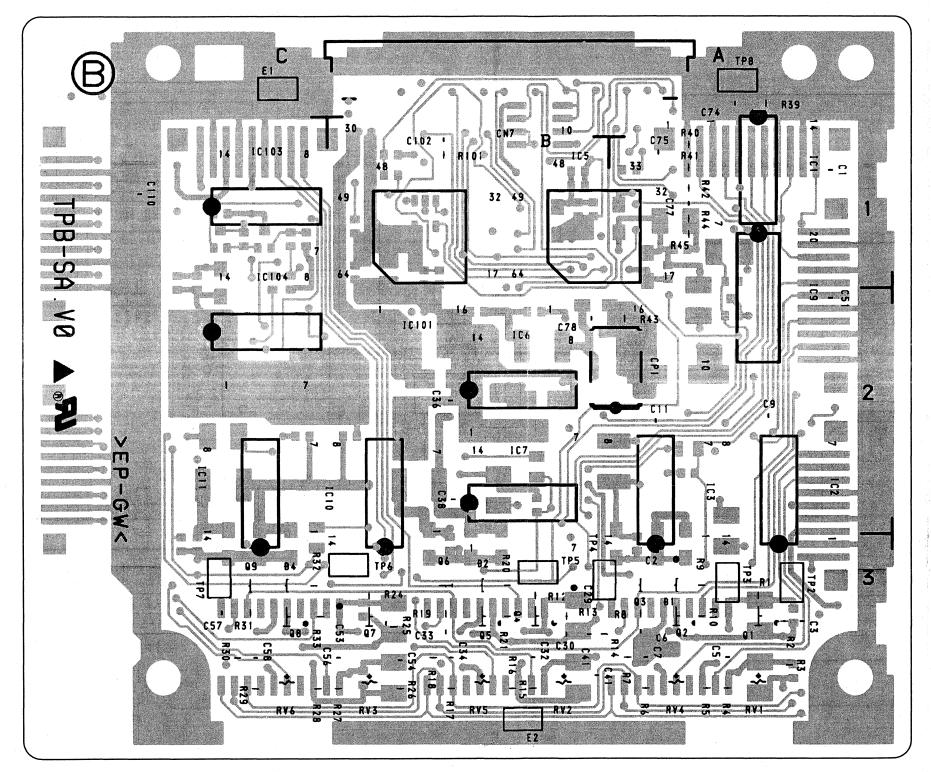


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			■ 5556 - RK-C	0614 25K853 R670 : RH-C 7 W 7	C602 (1/2) C602 (2/2) TL062CPW TL06	6 88007 RN-C 0 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1	0616 TP603 ₹2200	0832 934 11-08	19 (1/2) Q619 (2/2) Q629 P4601 XP P4601 XP P4601 XP P4601 XP P622	# R625 0620 0620 8627	± 0624	1 25Å1576Å 1 25Å1576Å TP604 30 ≠ R632 Q 31 ≠ R633	CN1 (1/30) B CN1 (2/30) B PR	1/2 PR OUT 1 OUT (AT) 2 R GND 7 R GND 8
						R676 15k W								1/2

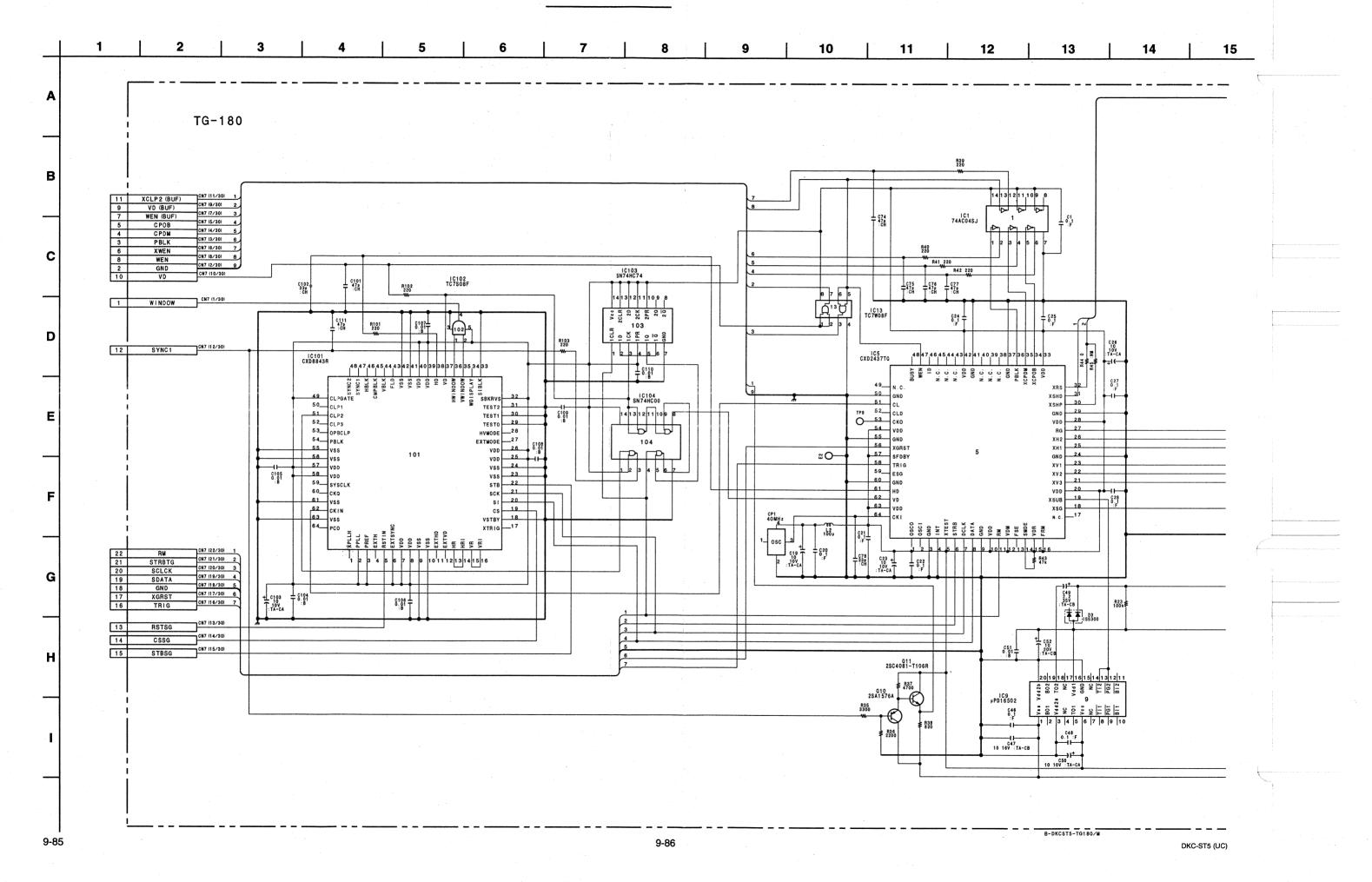
TG-180 BOARD

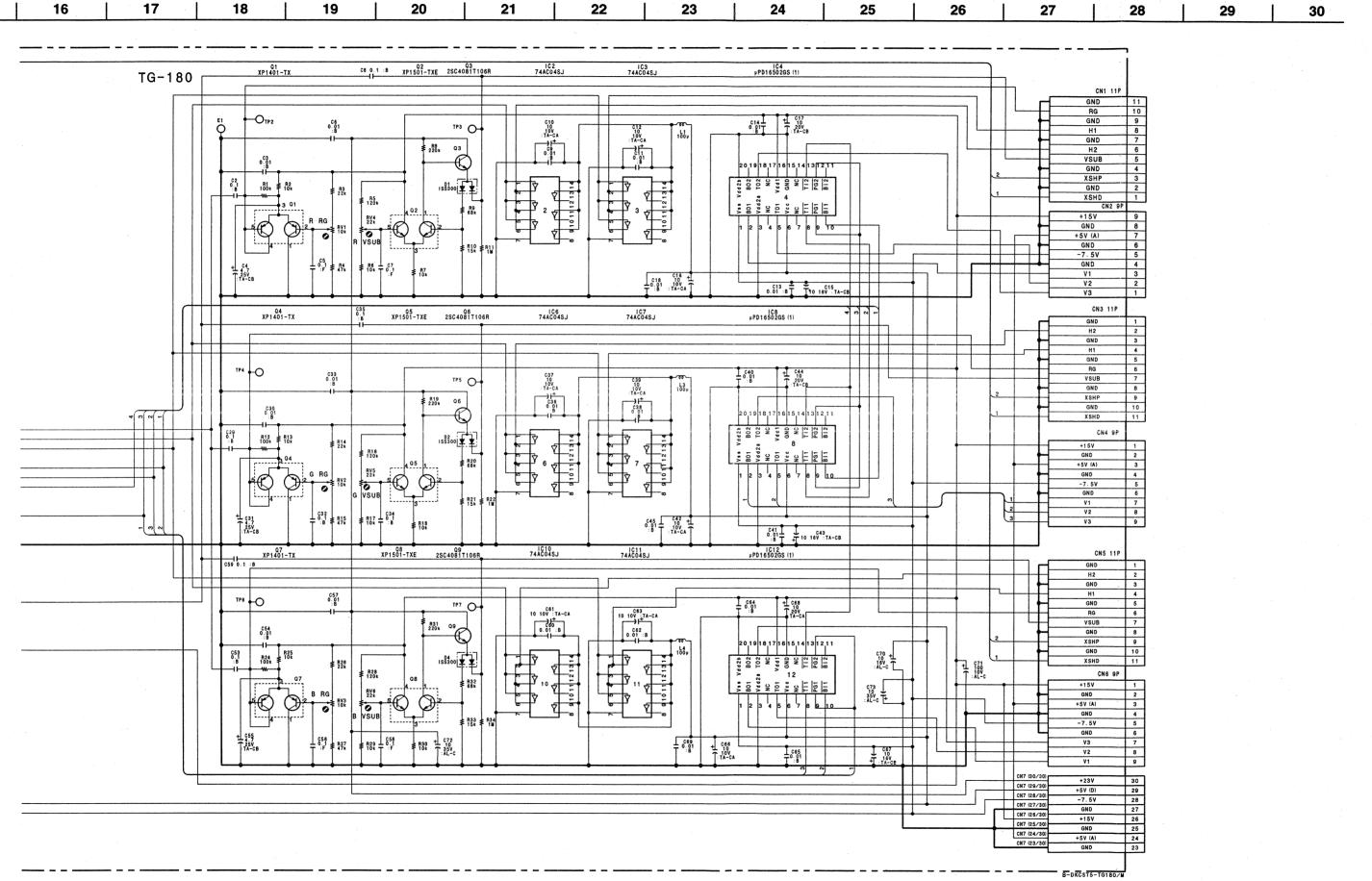


TG-180 A SIDE 1-665-369-11



TG-180 B SIDE 1-665-369-11





DKC-ST5 (UC)

	1	2	3	4	5	6	7	8	9	10	11	12	13	14
A												SI3 A	TB 522 T	523 C
В												STE-BY S2 LIVE MEMORY	512 	\$24
c		RI CN1 12P	— – – —— M – 171	R25 R24 R23 R2 10k 10k 10k 10	•	LIVE /MEMORY		FRAME CHA	RACTER NOFF			93 WEMORY S4	\$10 (FOCUS) \$11	\$8
D		1 Si 2 Si 3 Si	W1		RELEASE W R1 1.1k R2 1.5l	^ - ~	~ \	R5 R6 S.1k	NOFF			2 R12 R11		—————————————————————————————————————
E		10 +	5V 5V ND	C2 10 F 10 V :TA +16	R7 R 1.1k S8	N	FOCUS Q	E.ZOOM S R11 3.3k R12 5.1k	3 O			3 2 2 5 1 LOT NO. S NI RM - 1 7 1 - 665	I BOI	A NO. NY A-8313
F		12 G	ND .	J	MENU R13 1.1k R1 1.	- 15	<u></u>	R17 R18 5.1k	DOWN			MAĐE 51	IN JAPAN - A71 SNO-6	-470-A
G					USER W R19 R 1.1k 1.2k 1.2k 1.2k 1.2k 1.2k 1.2k 1.2k	26 5k S23 R27 1.8k S24	R28 2.4k S25		_ - - -			971LL 518	90WN FRA	ME ON/OFF
H			——————————————————————————————————————					B-DKCS	T5-RM171/M			1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1	14 R16 C1	
1												0 1-	15 C2+ H	RIB
													RM-17	

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